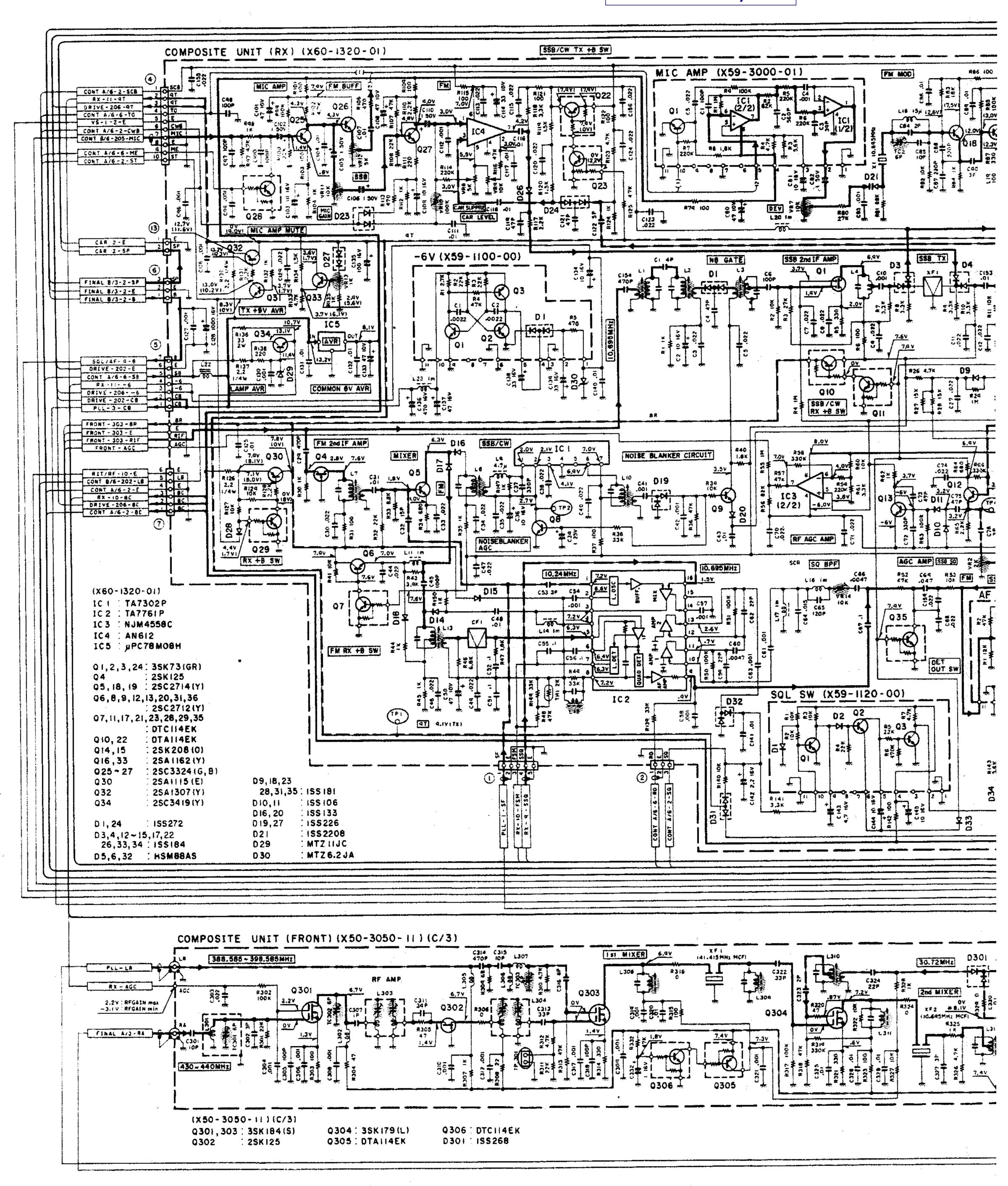
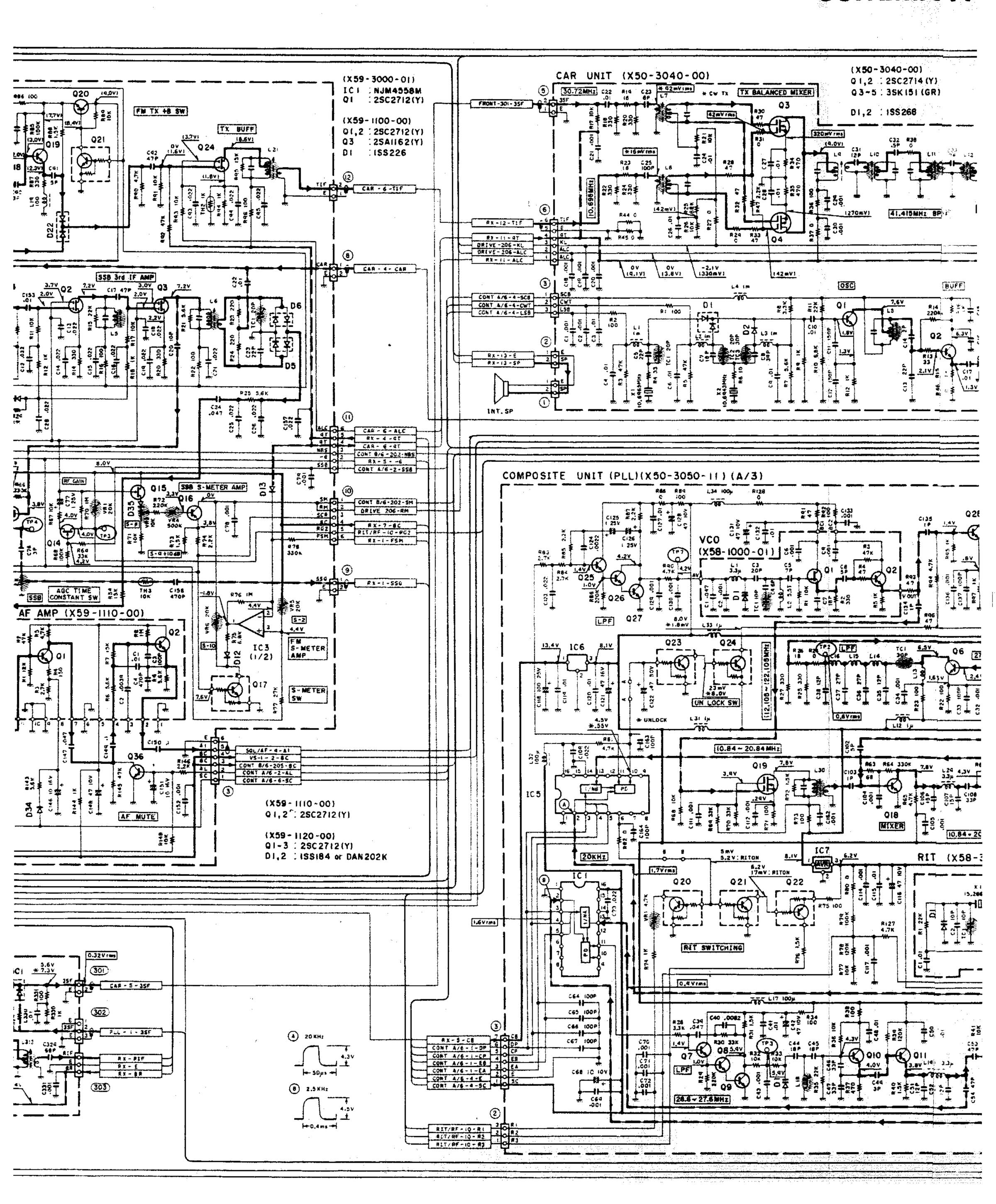
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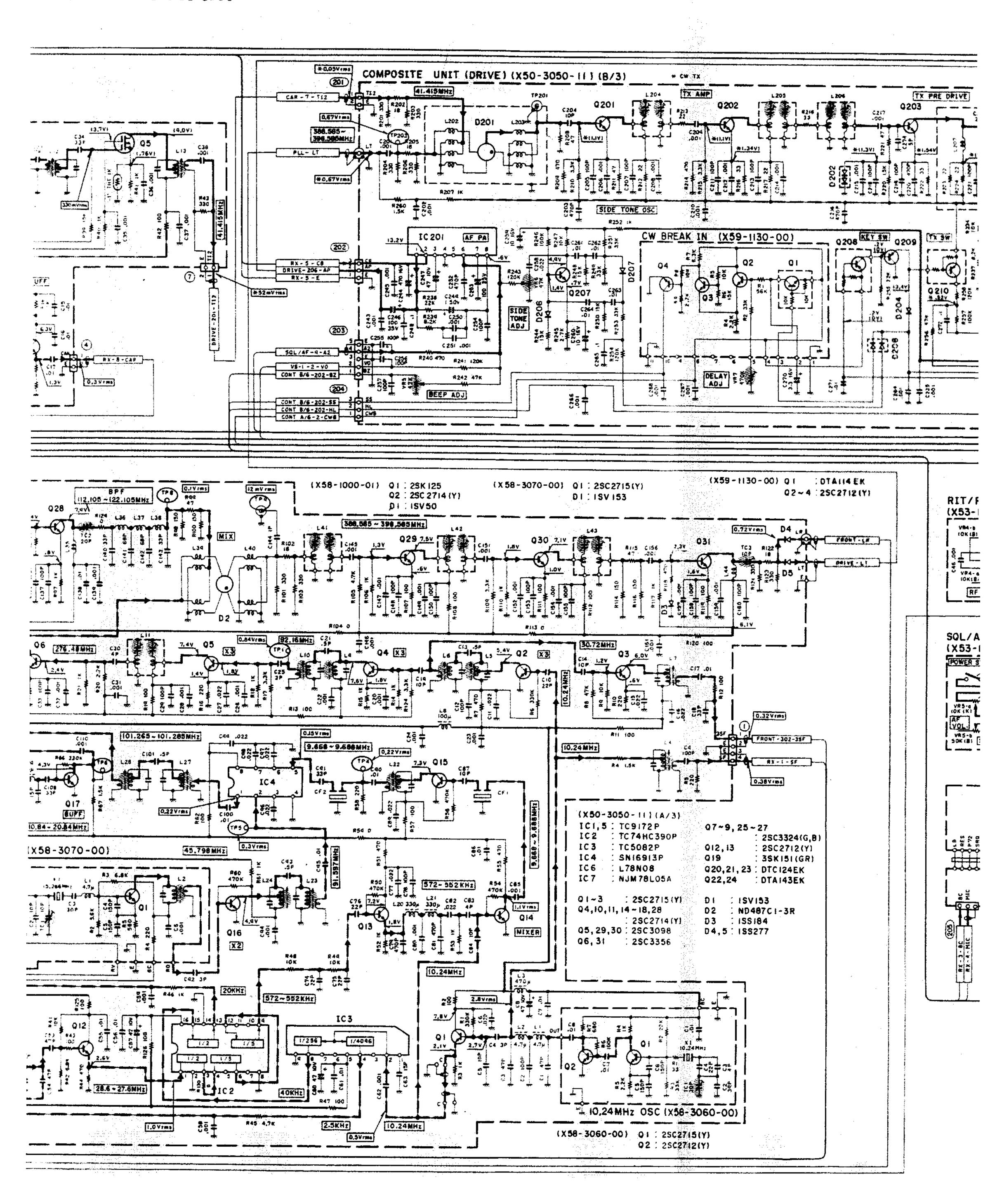
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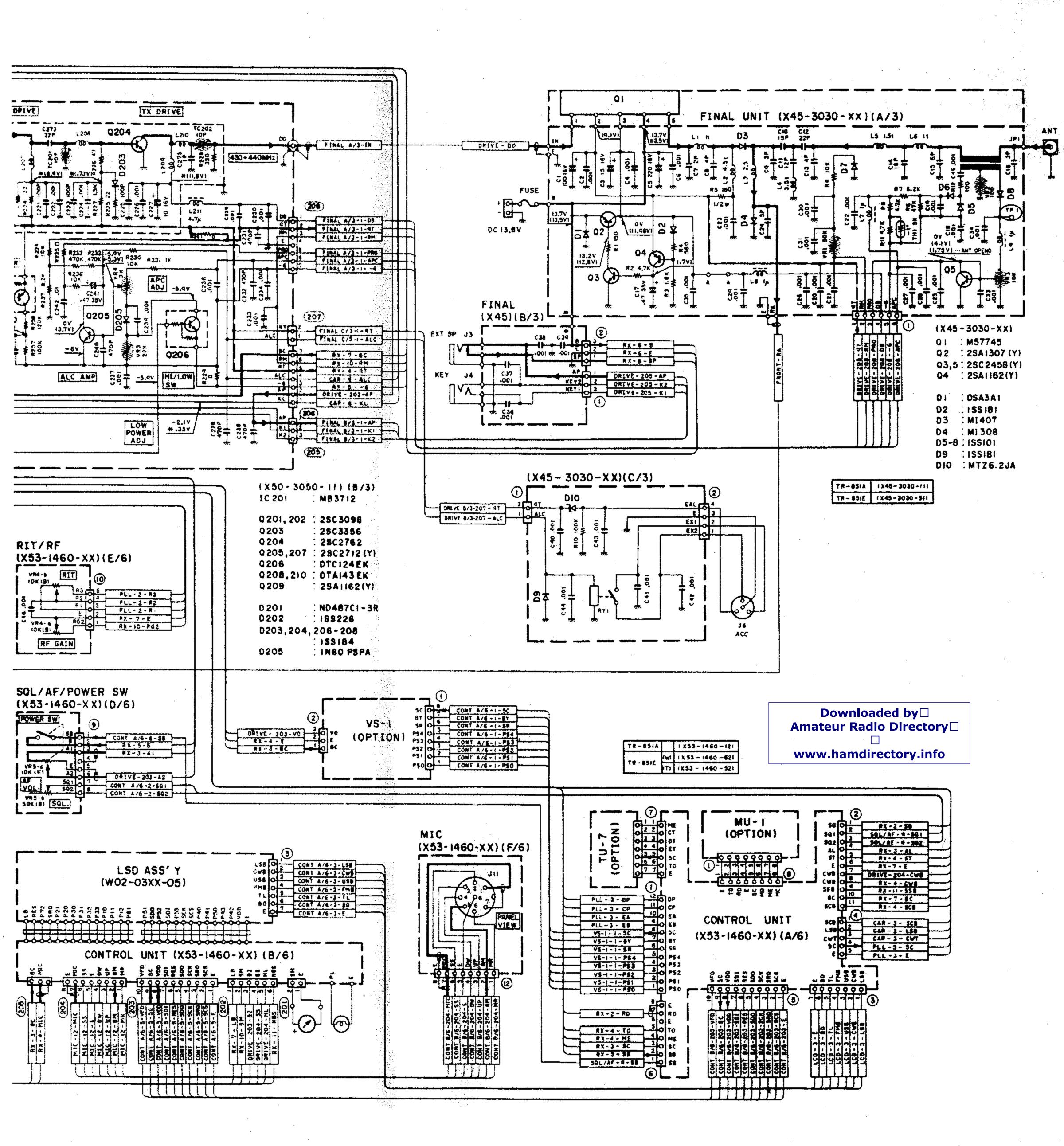


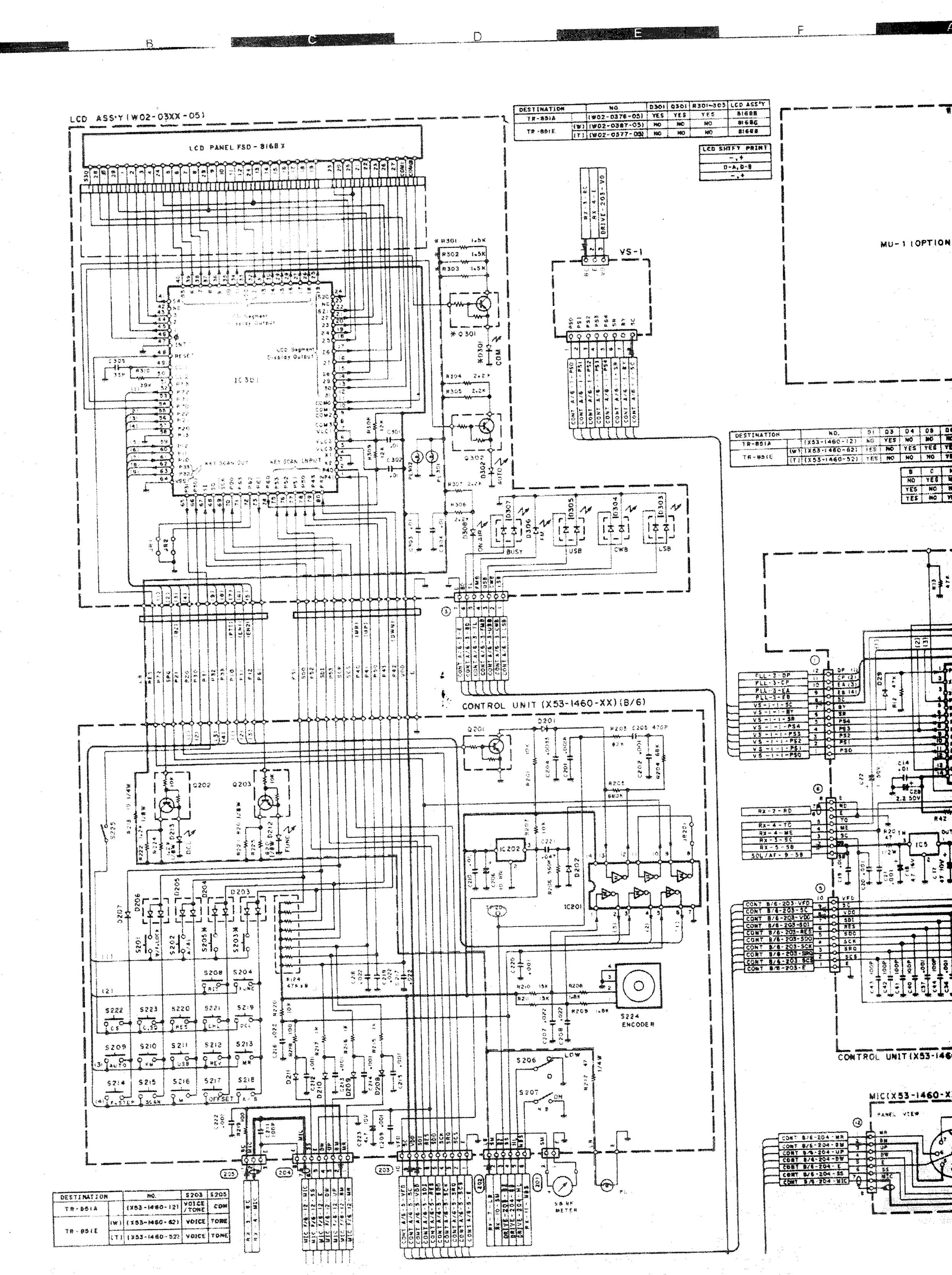
# SCHEMATI



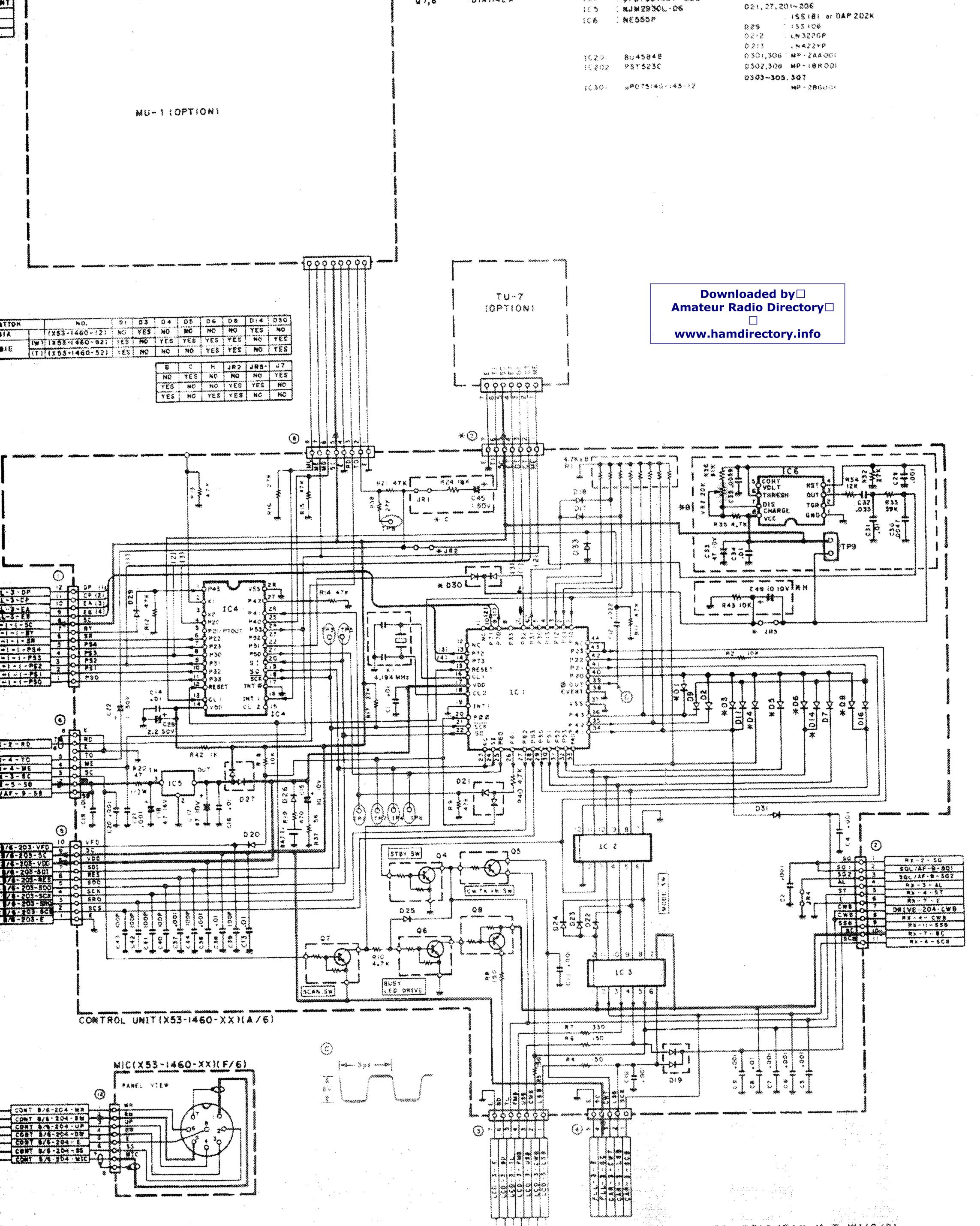
# TIC DIAGRAM







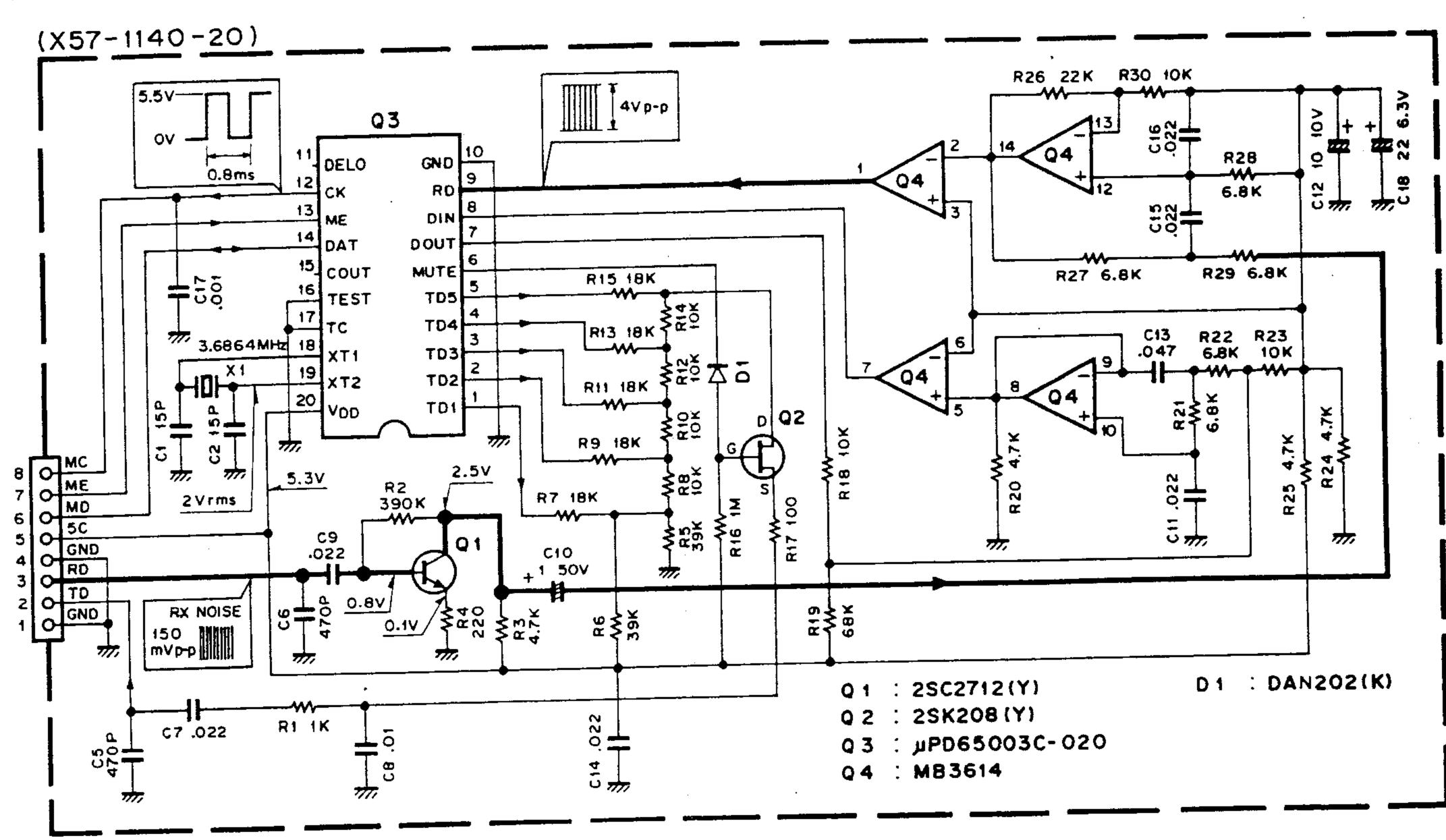
#### В CIRCUIT DIAGRAM TR-851A/E 01-9, 11, 14,16 -18.22 -24,207 -211 # PO 750 BHG - \$6 8 - 22 Q4,6,201+203,301,302 3 C 3 455133 OTSCIZAR DICHAEK 10.2 019.20,25,26,30,51,33 DY 54:438 \* DIAHATK 3 ( 3 ្ន 155 184 of DAN 202 N COTAHAEK \$P075075CT - 226 3€# Q7,8 021, 27, 201~206 M1M5830F-0@ 103 . 188181 or DAP 2028 106 **86555**8 1 188106 CRAZZZE 0238 CM422×4 0301,306 MP-ZAAGGE 8045848 3620: \$302,308 MP~188001 PST5230 3000 0303~305.307 G#075146+145-12 MP - 286001 \$ ( 3 C × MU-1 (OPTION)





# MU-1 (MODEM UNIT)

### **MU-1 SCHEMATIC DIAGRAM**



# Modulation output (TD terminal output on MODEM unit)

Condition		TD terminal output	
ME	MD	Frequency (Hz)	Output voltage (V)
5V	5V	1.200	1.3 ± 0.15
5V	OV	1.800	1.1 ± 0.15

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### Demodulation output

Operation condition (RD terminal) :  $40mV\pm3dB$  (Confirm DAT terminal voltage by receiving a  $60dB\mu$  signal from SSG)

SSG MOD, frequency	DAT terminal voltage
1.200Hz	5V
1.800Hz	0V