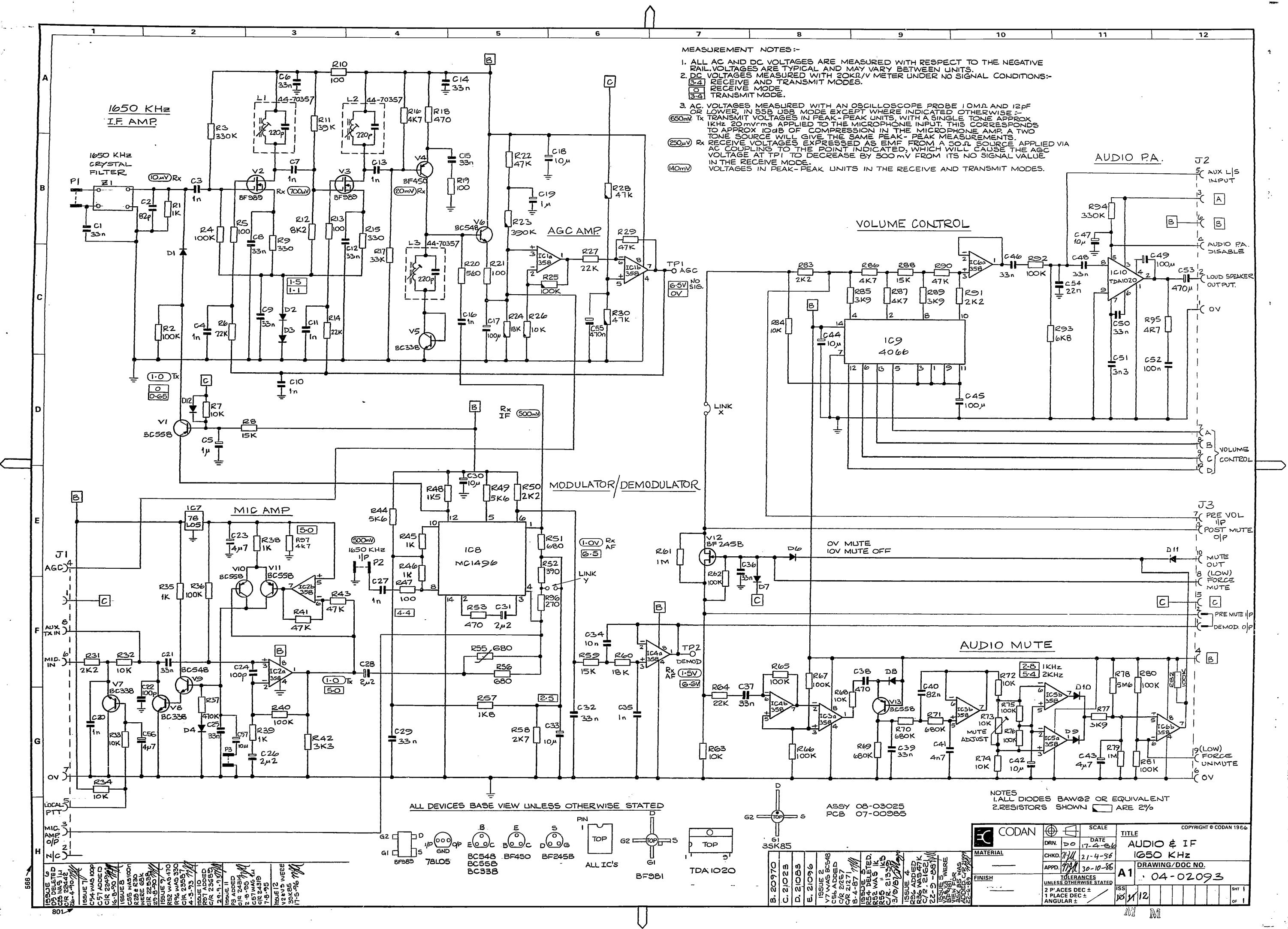
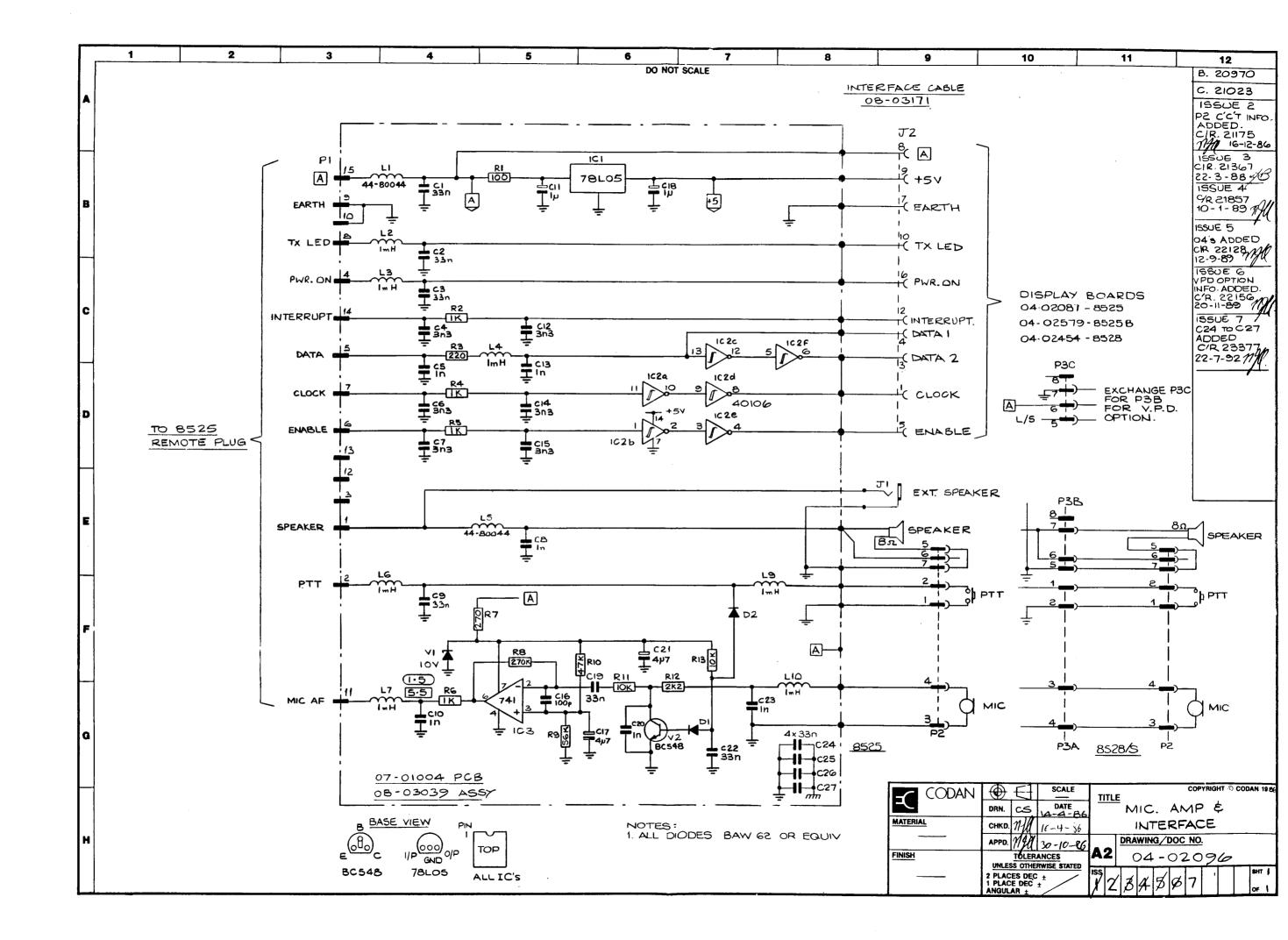
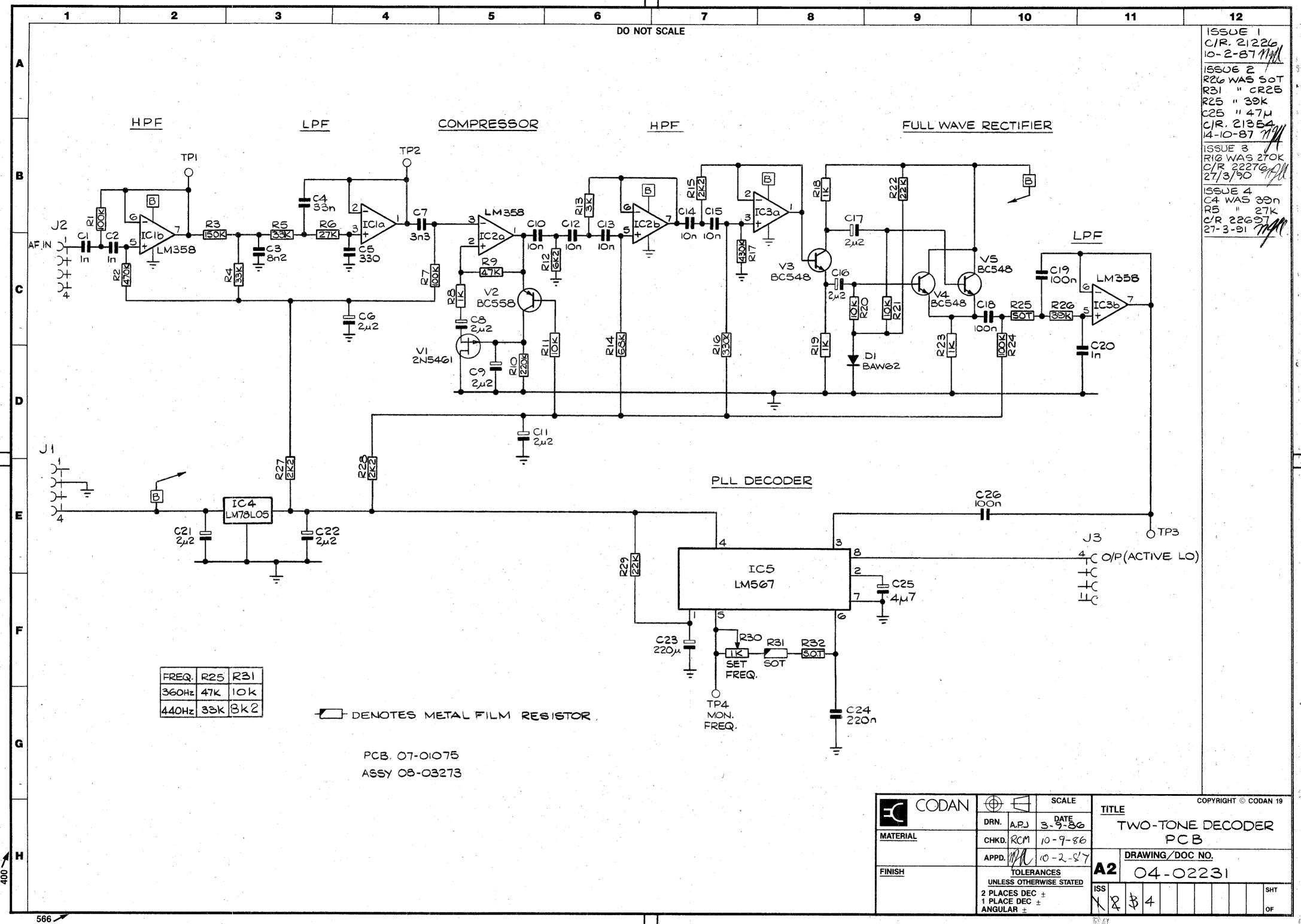
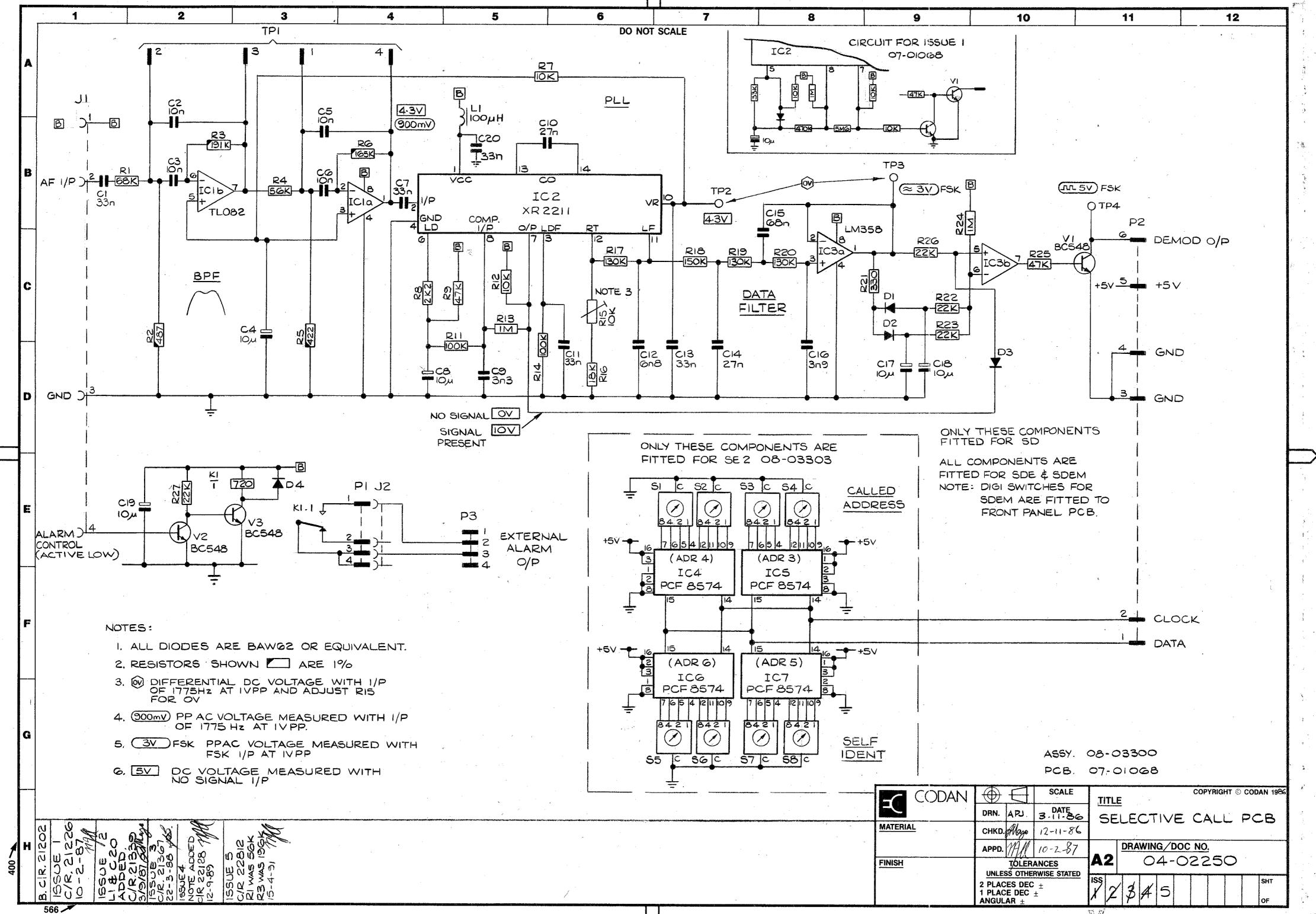


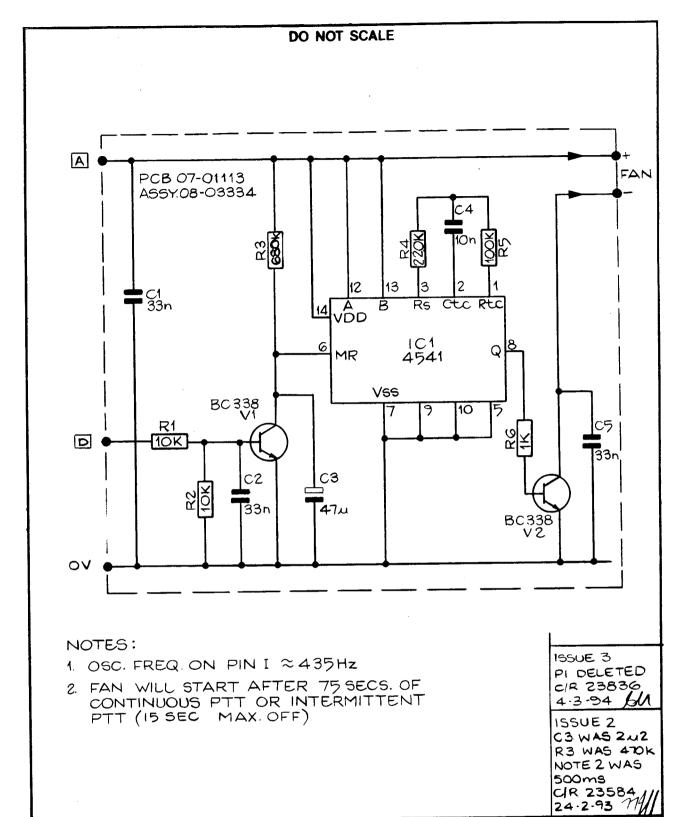
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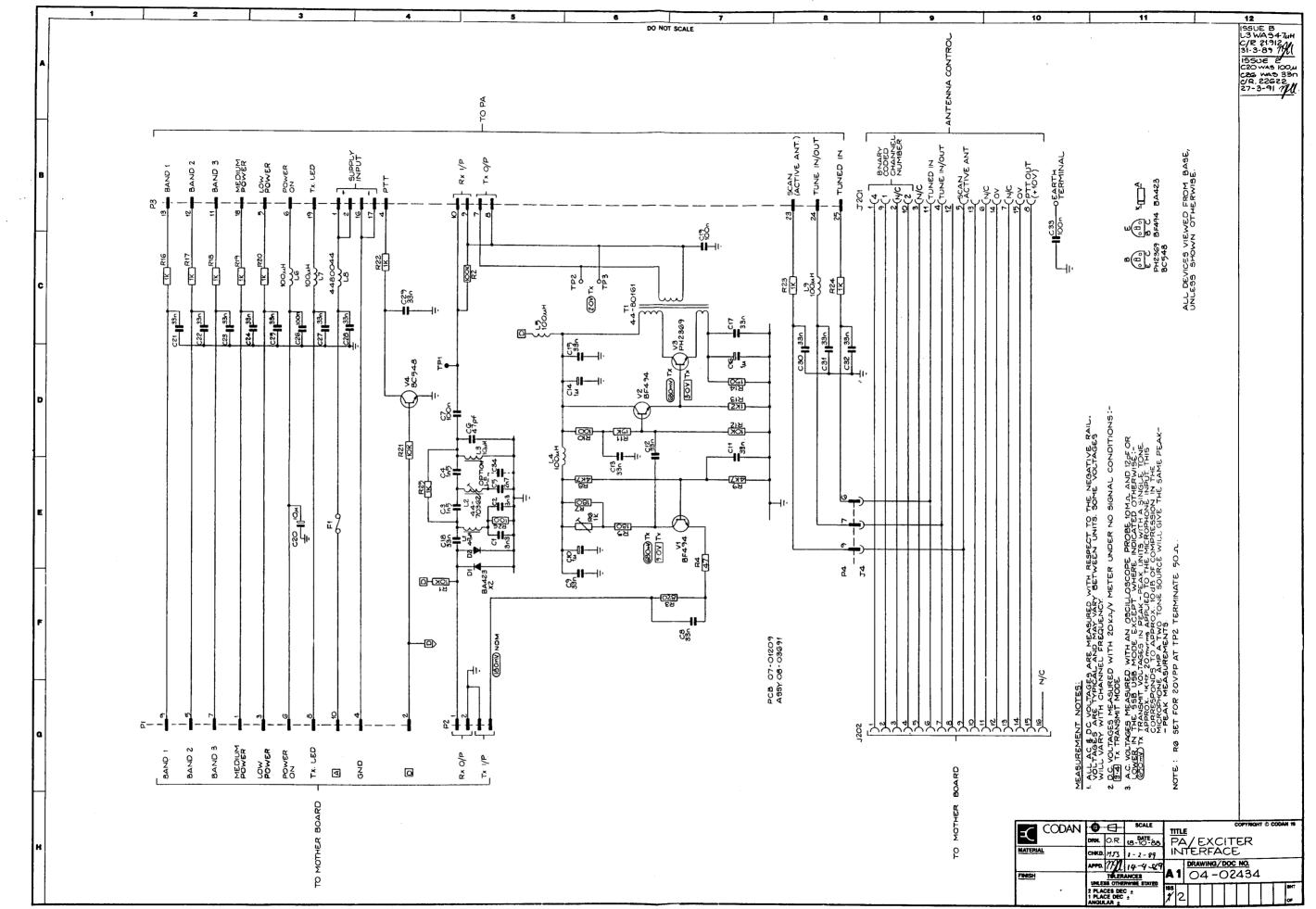


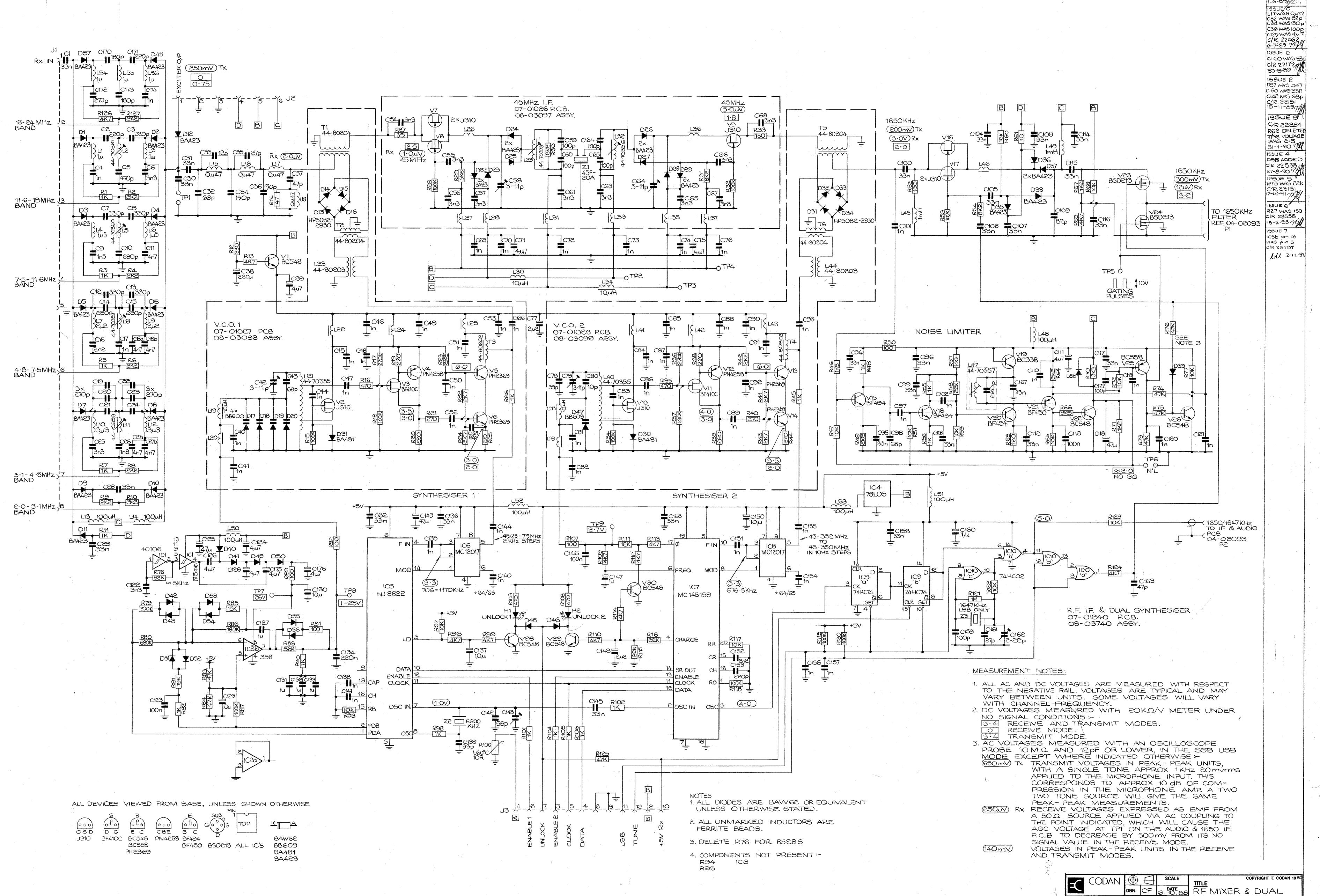






CODAN	SCALE	COPYRIGHT © CODAN 19
	DRN. O.R. 7-9-88	FAN CONTROLLER
MATERIAL	CHKD. 1191 5-10-88	
	APPD. MILL 14-9-89	DRAWING/DOC NO.
FINISH	TOLERANCES UNLESS OTHERWISE STATED	<b>A4</b> 04-02260
	2 PLACES DEC ± 1 PLACE DEC ± ANGULAR ±	1SS   X   Z   3





04-02450

SYNTHESISER

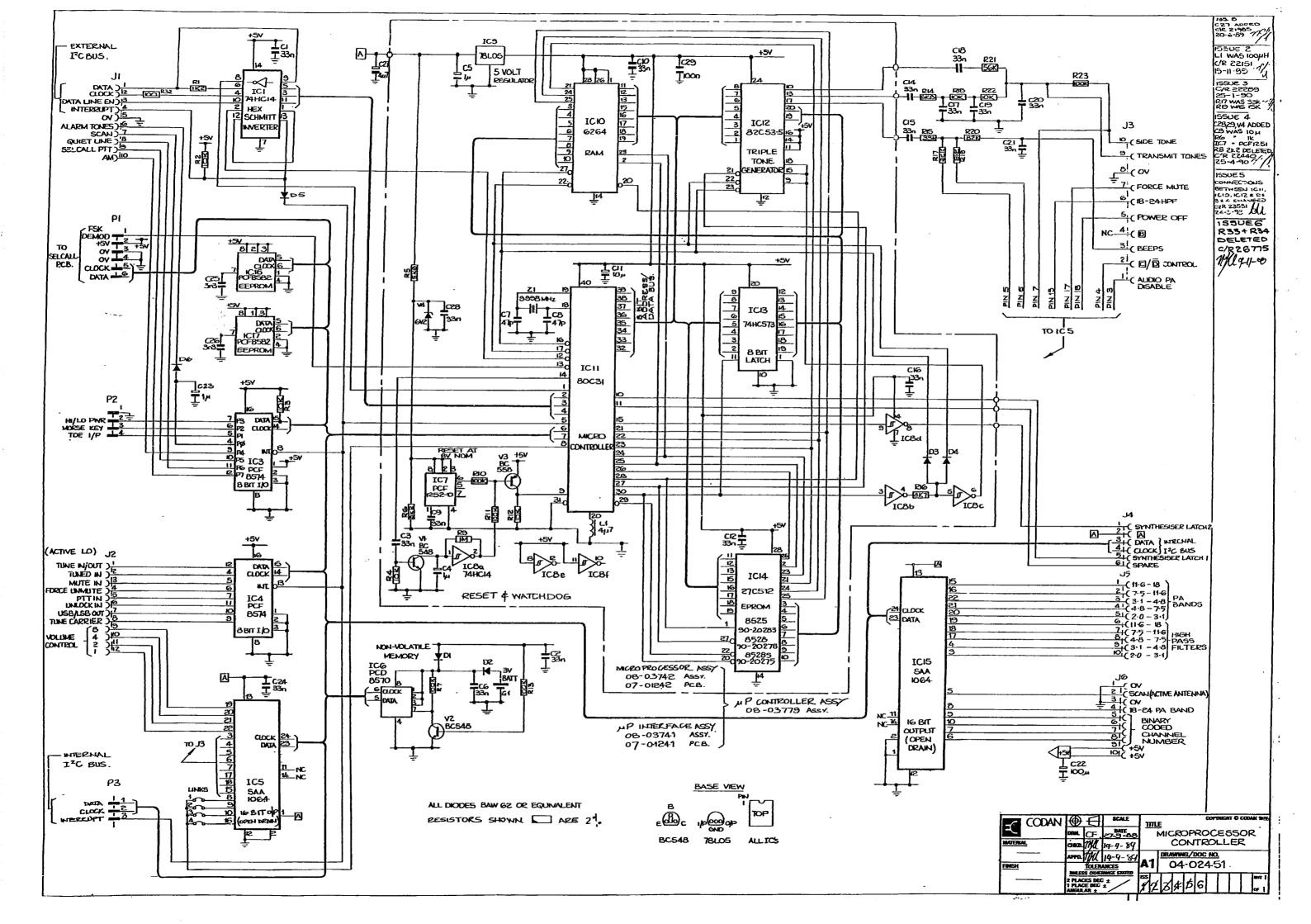
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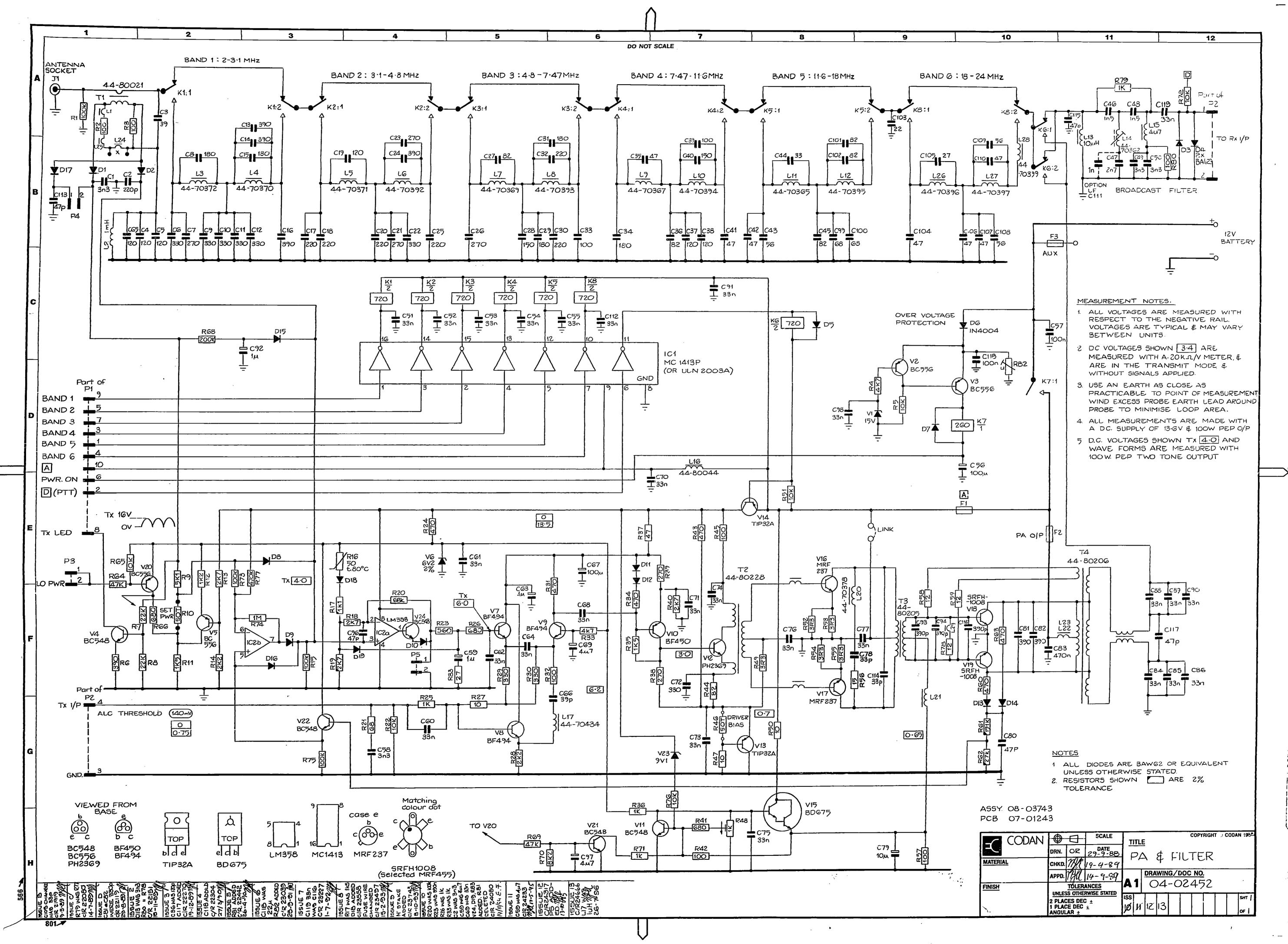
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UNLESS OTHERWISE STATES

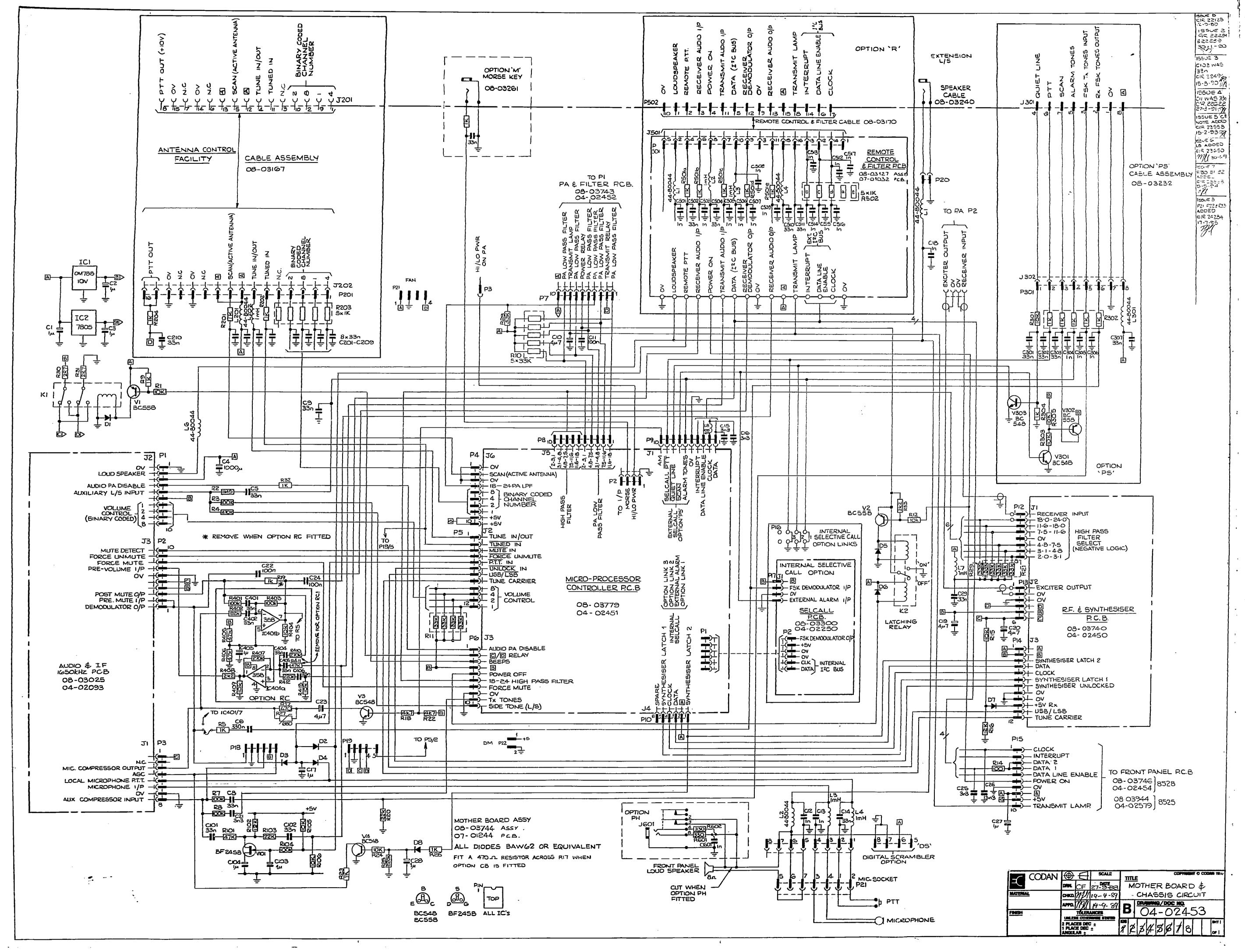
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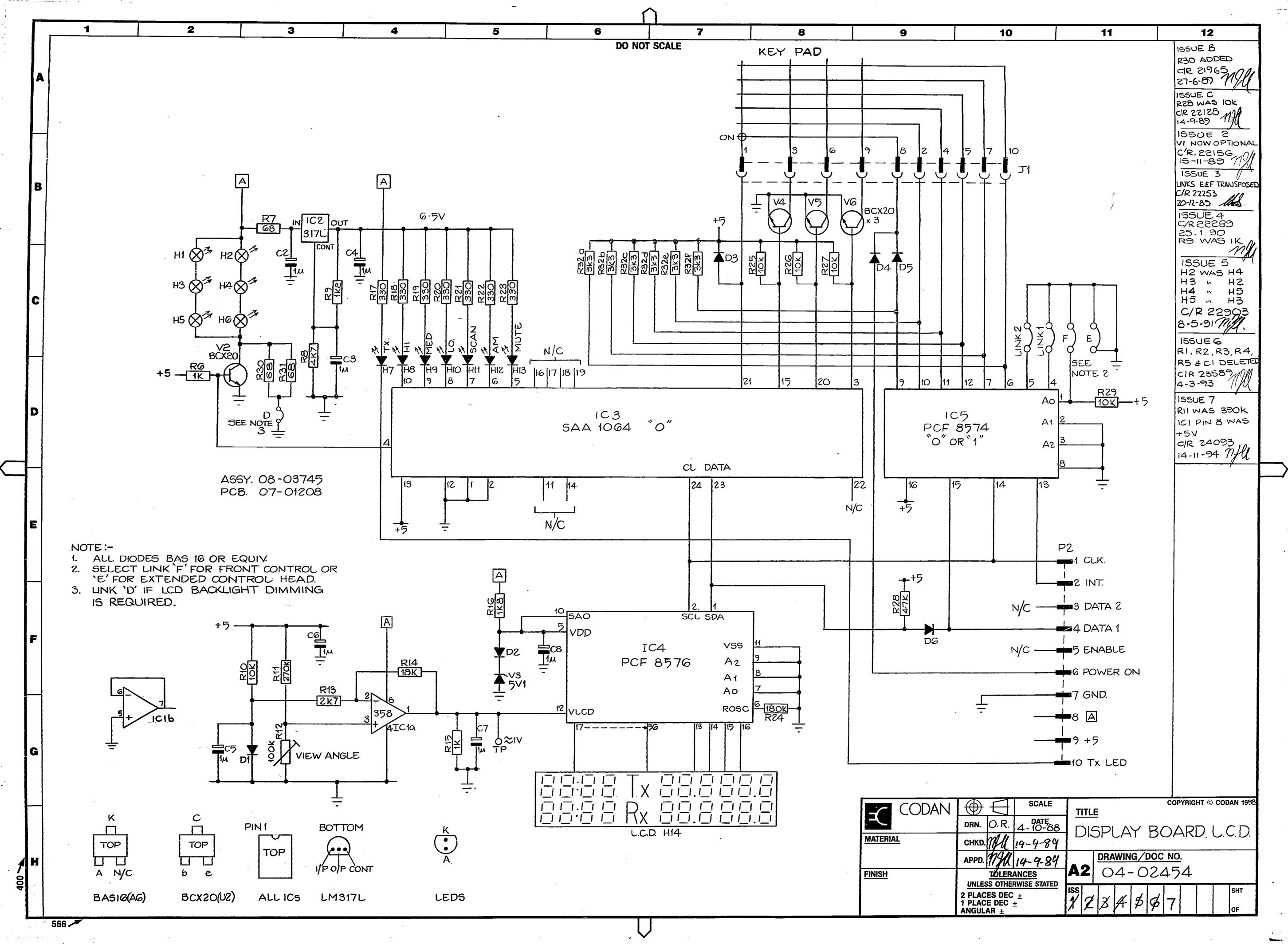
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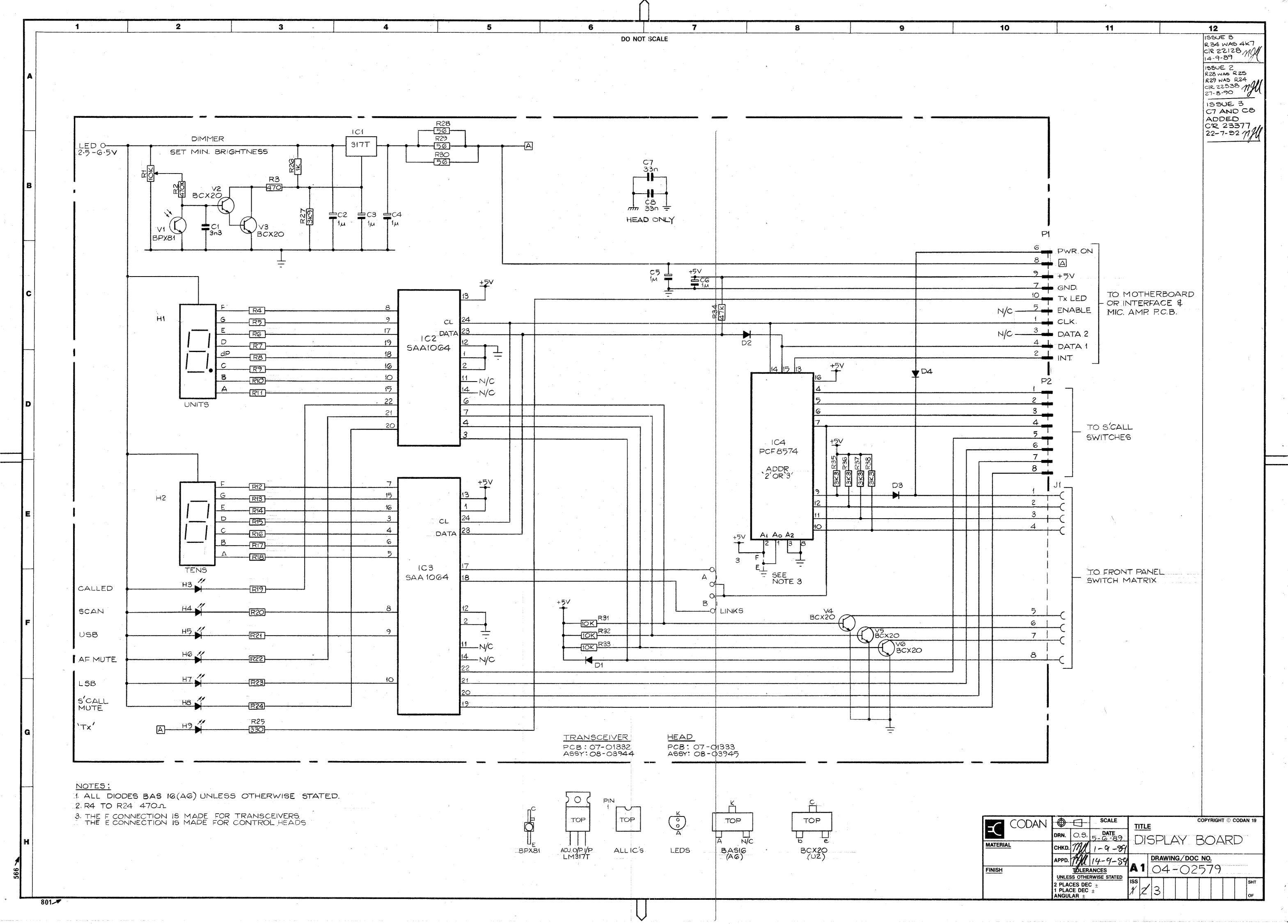
CIR 21985

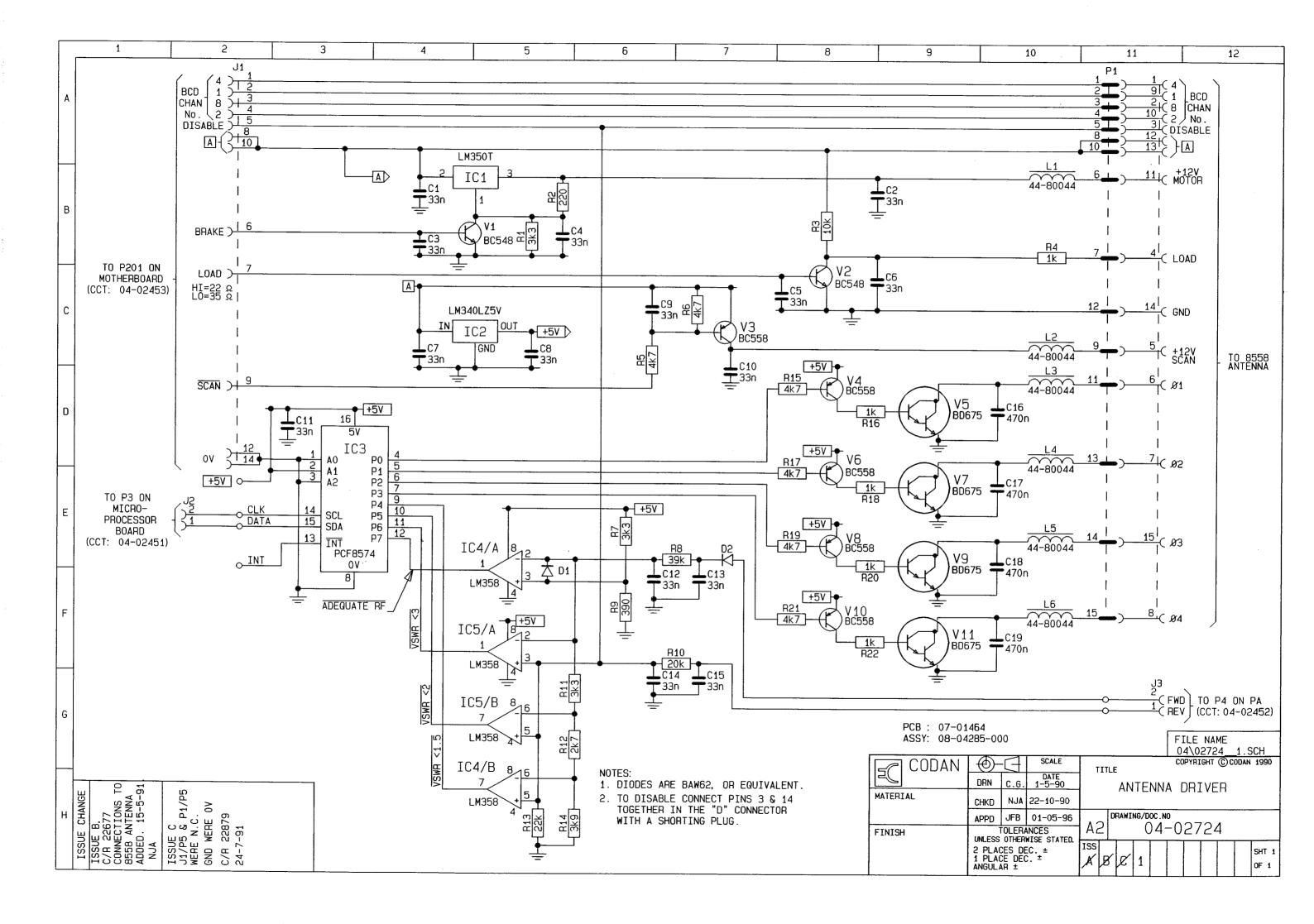


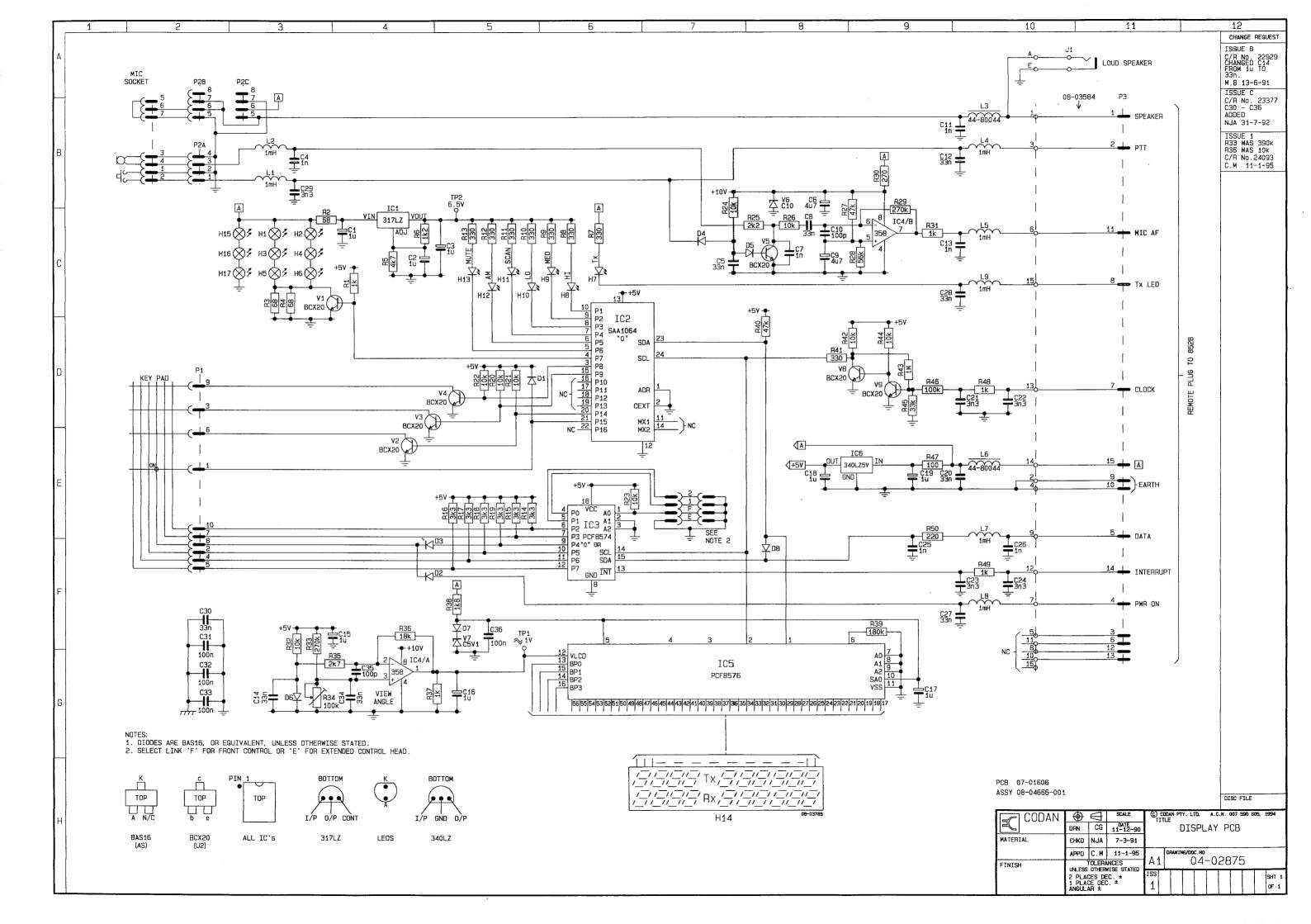


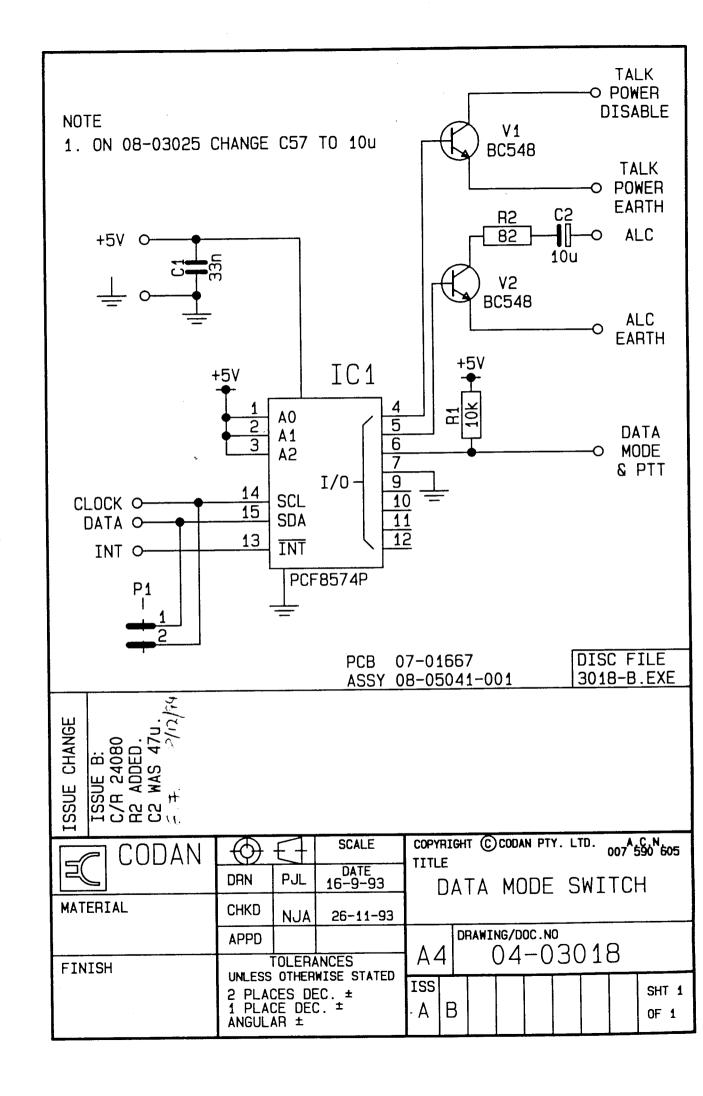


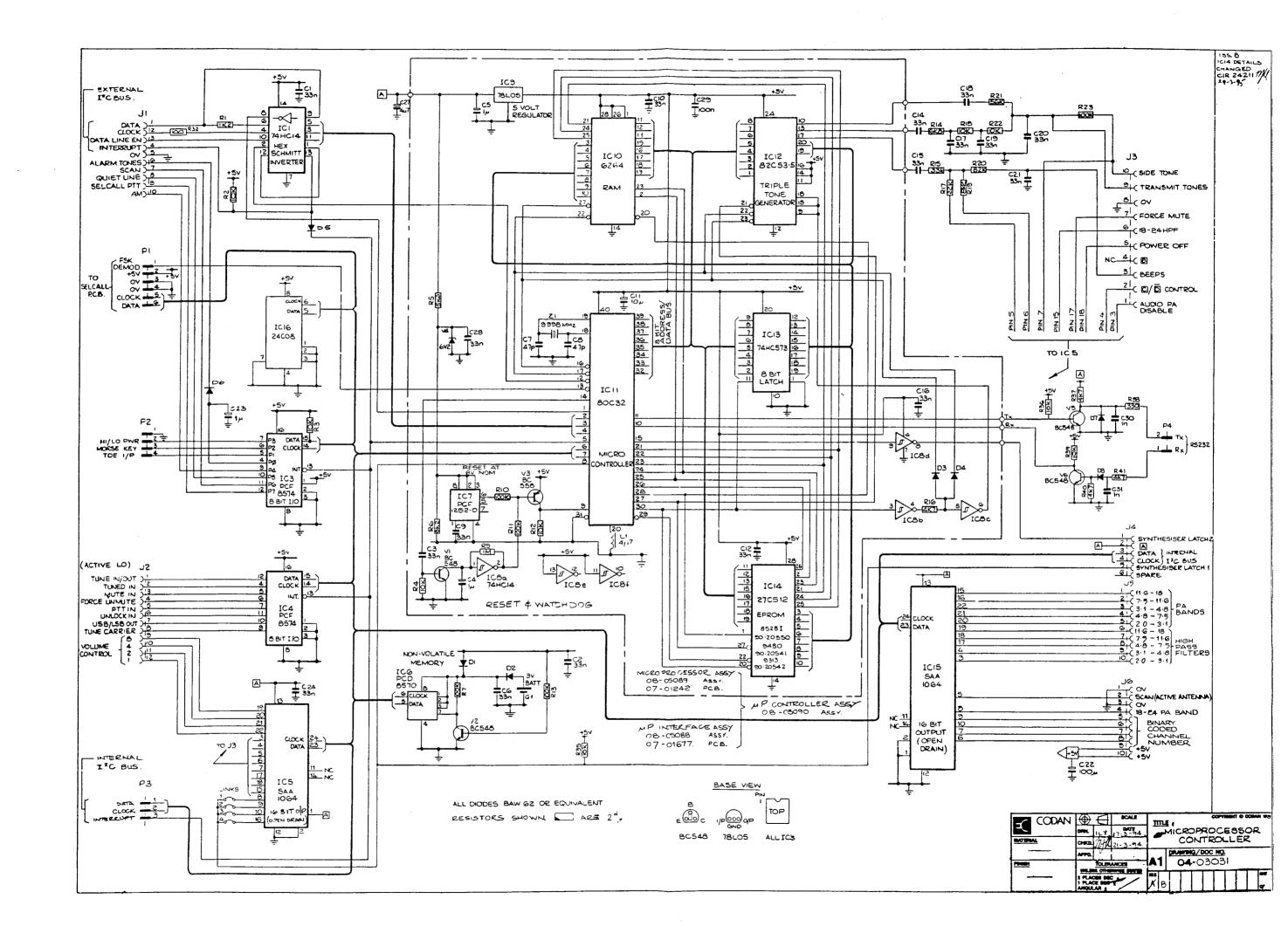


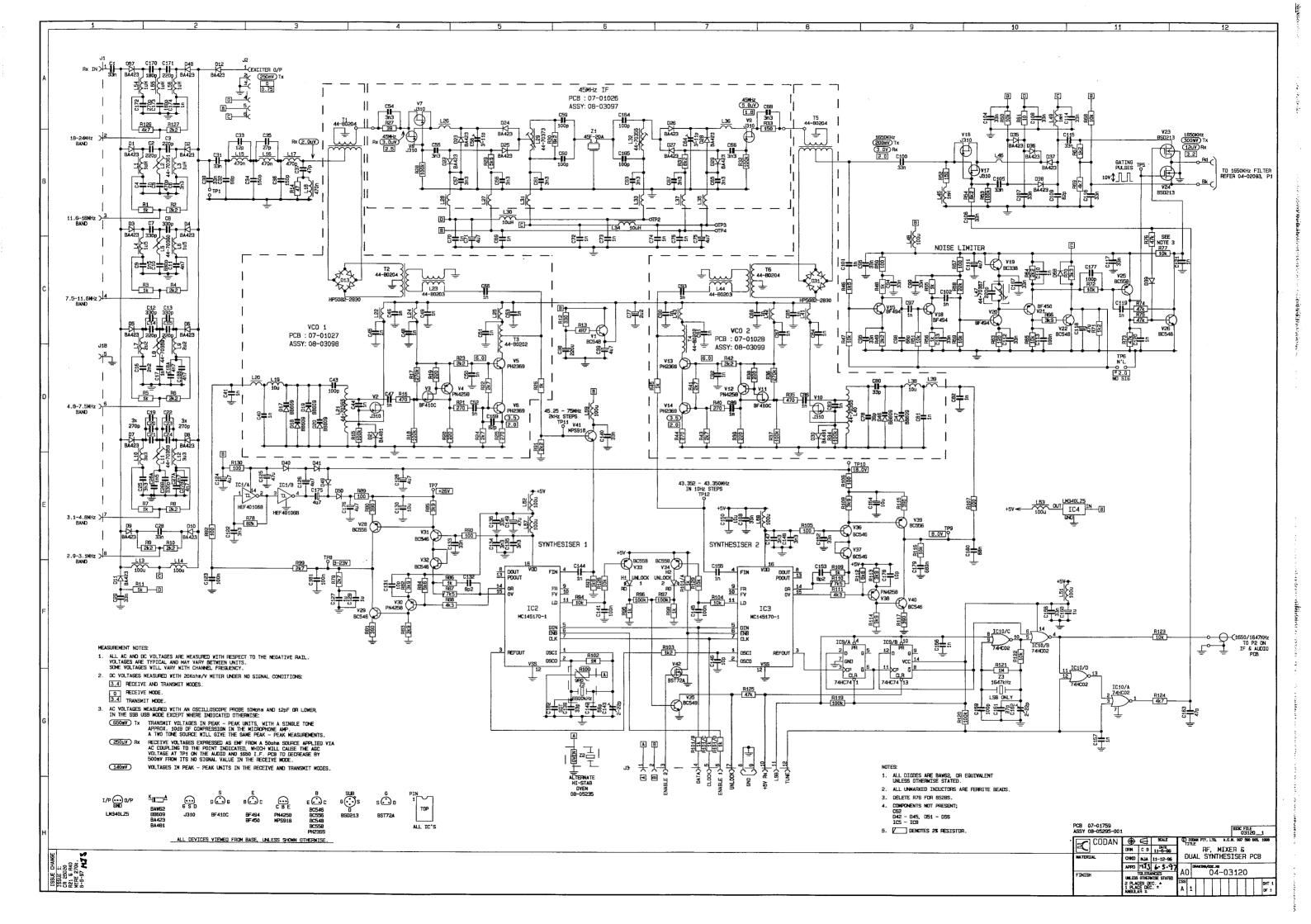


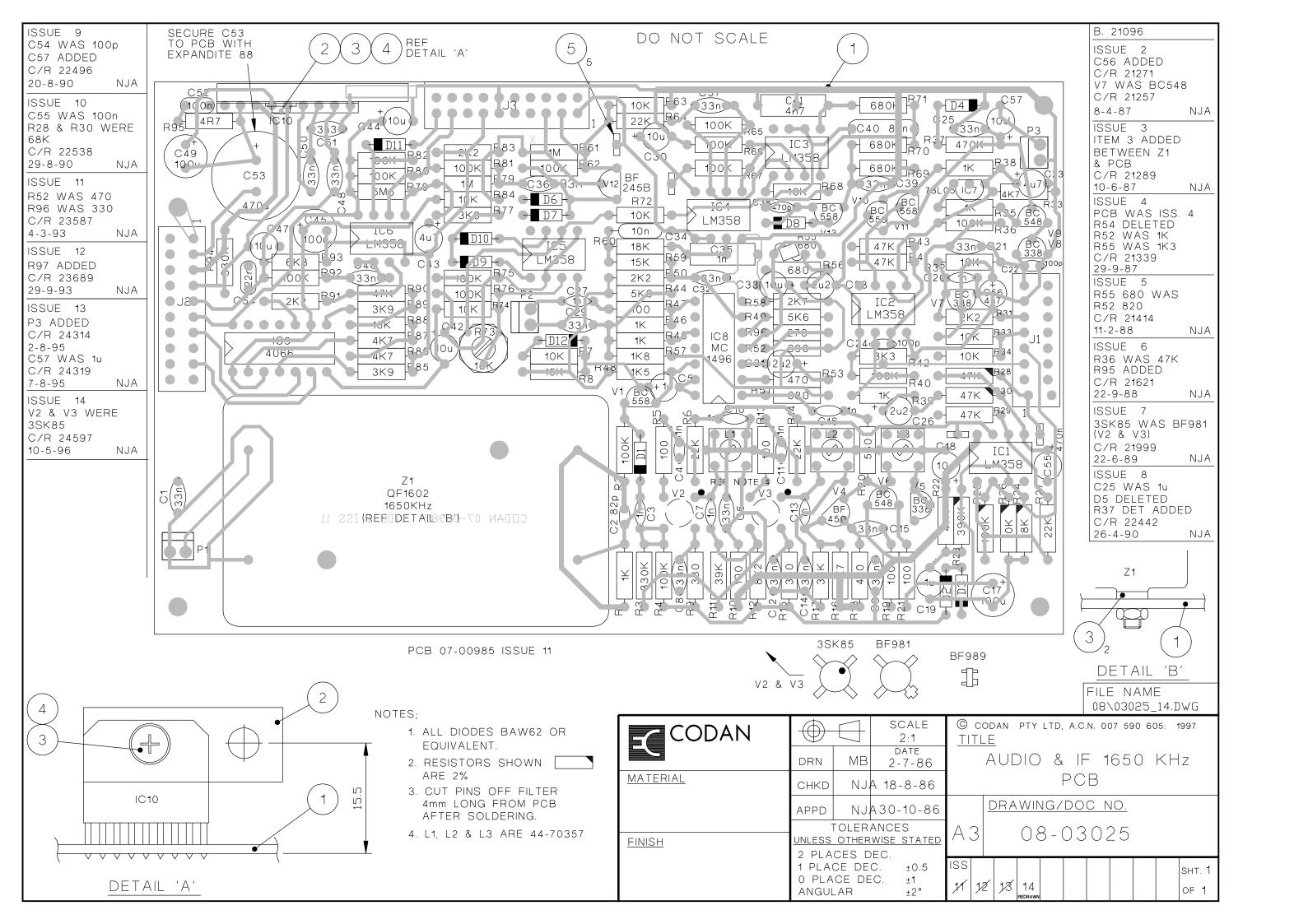


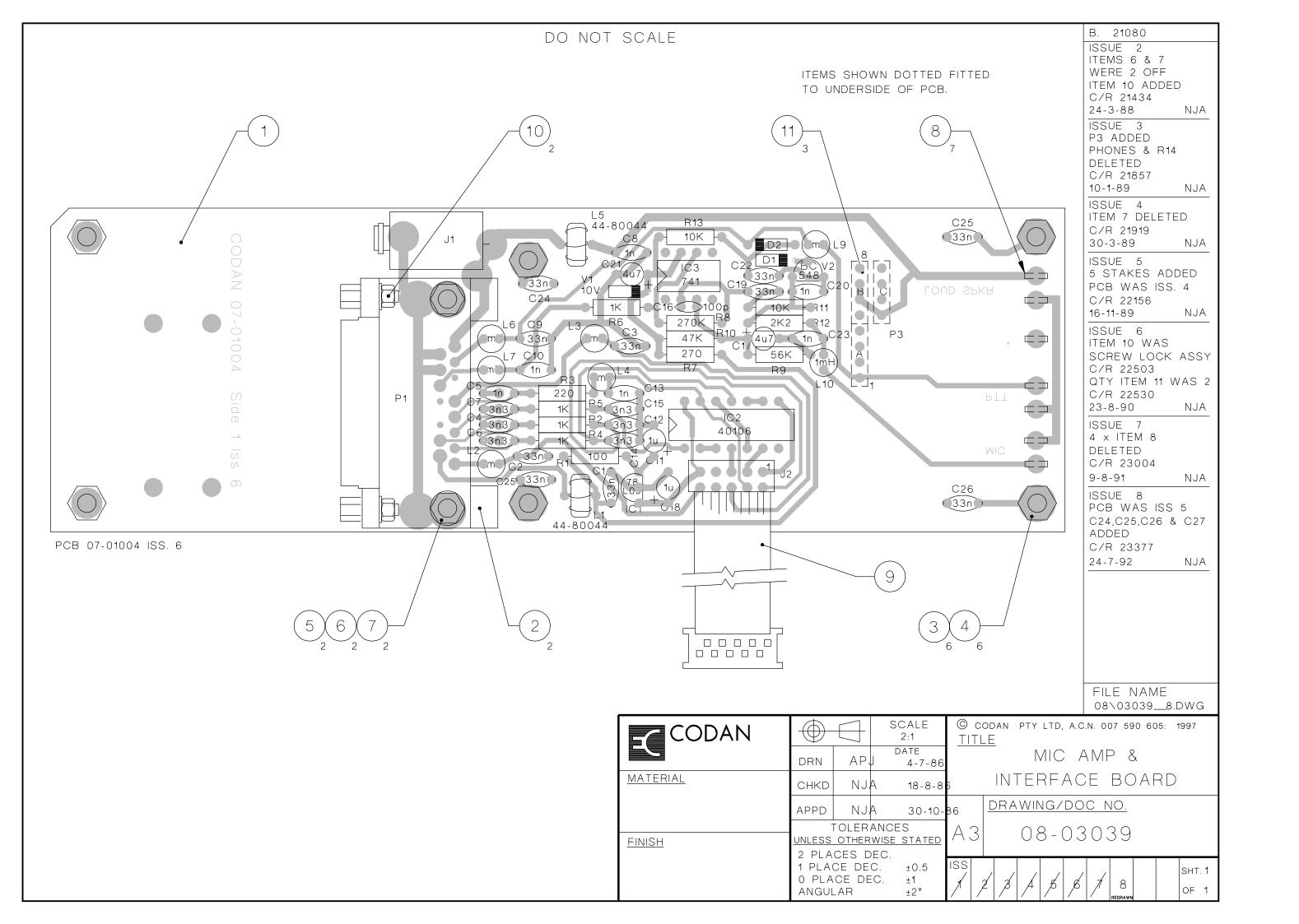


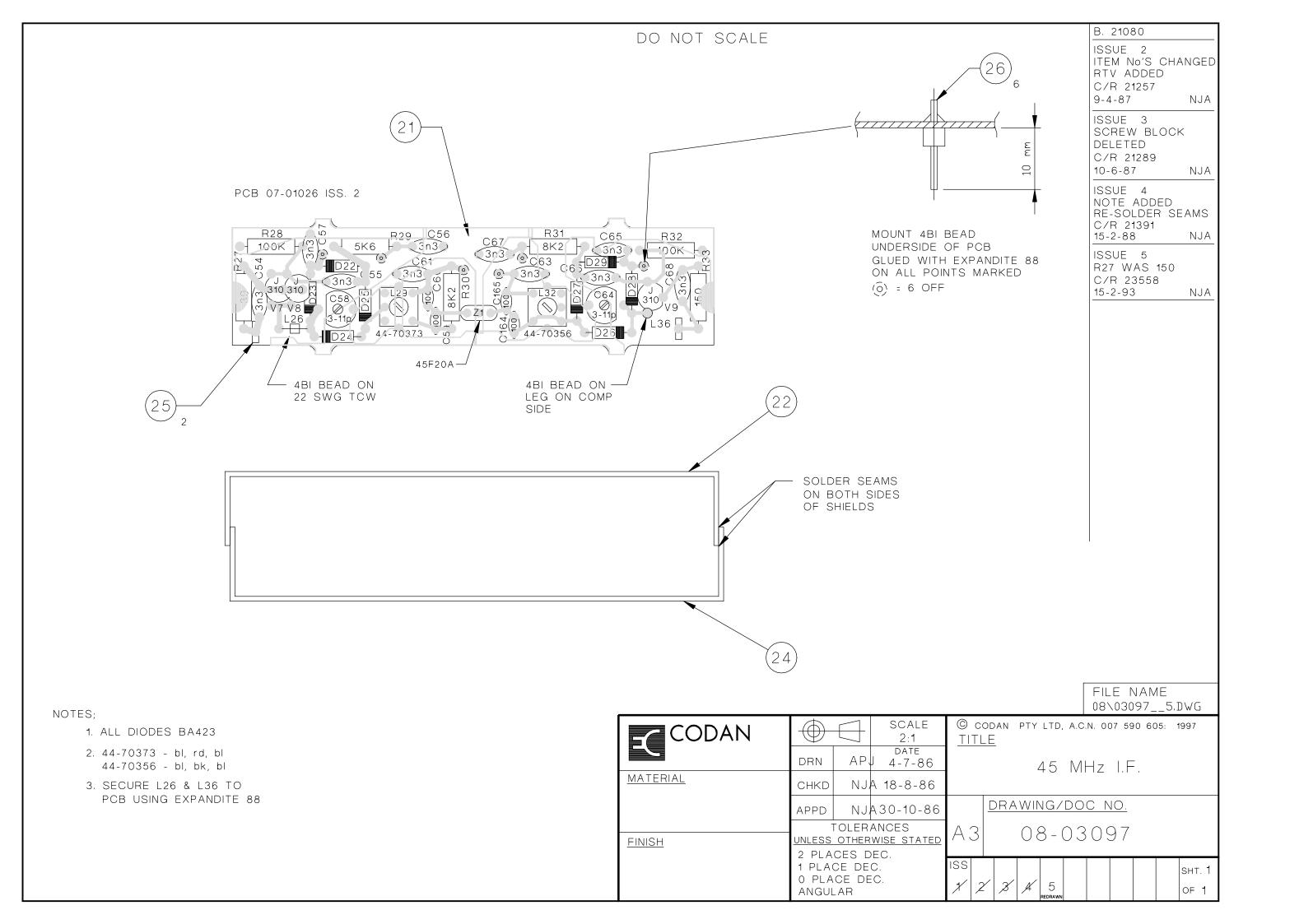


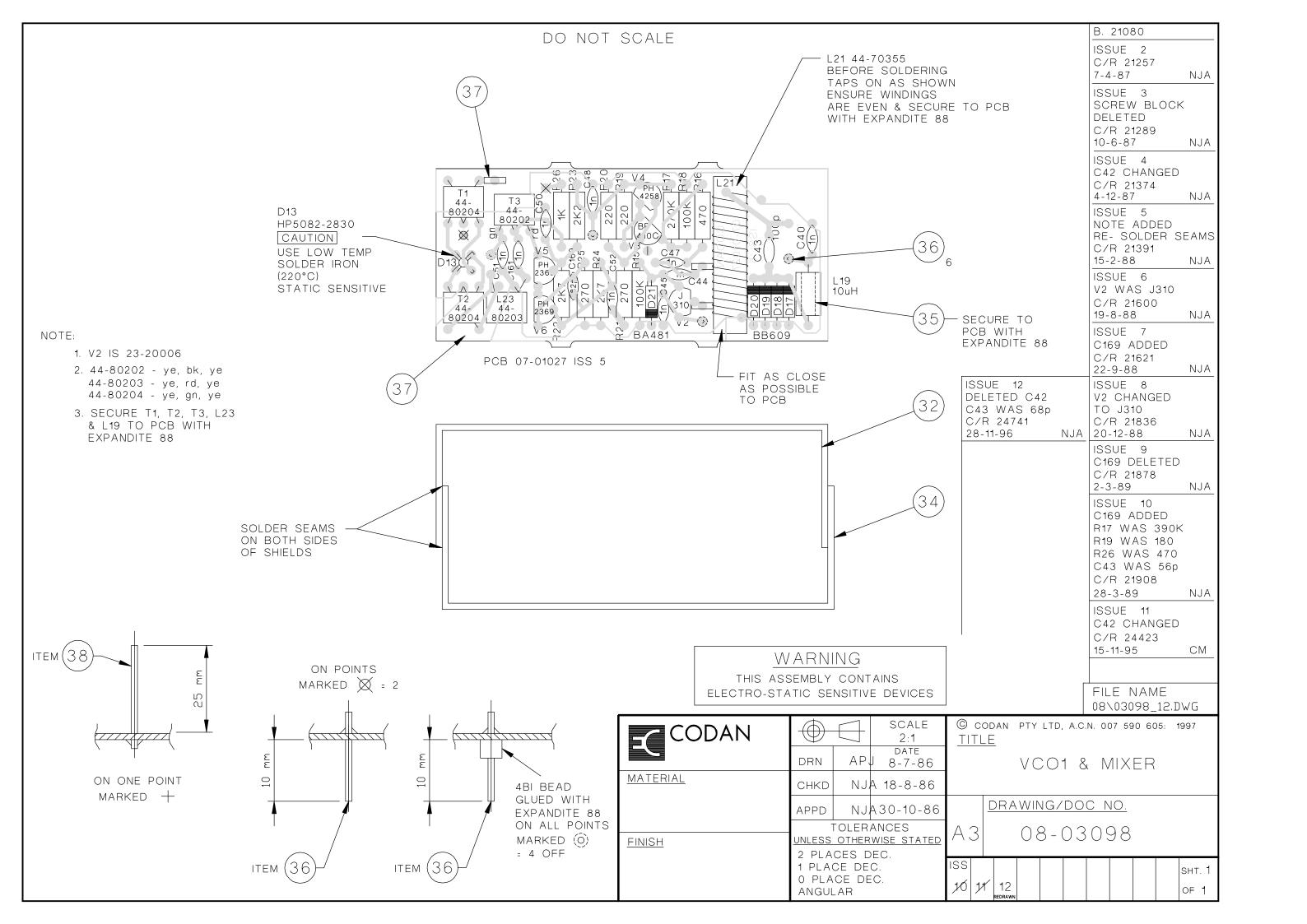


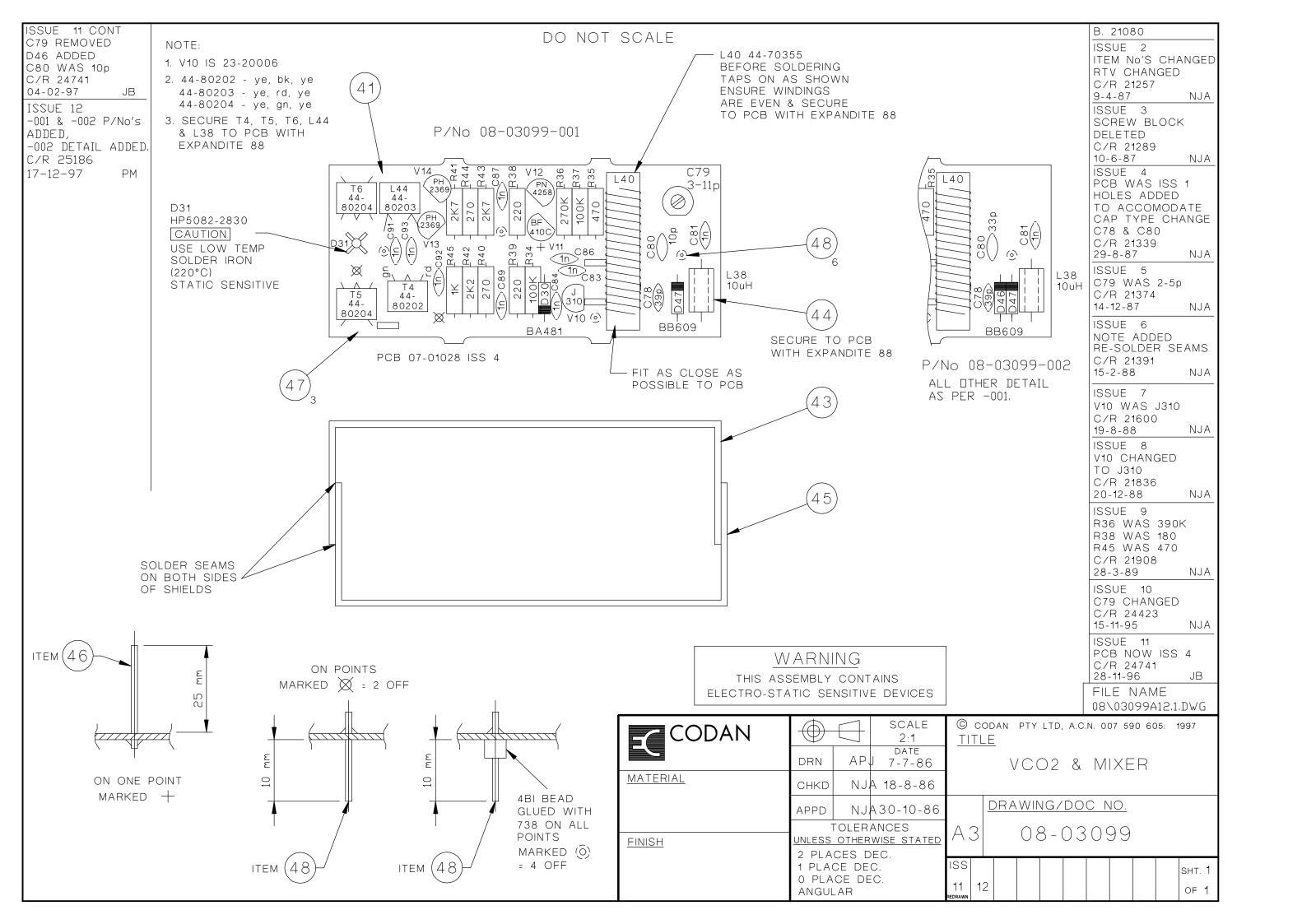


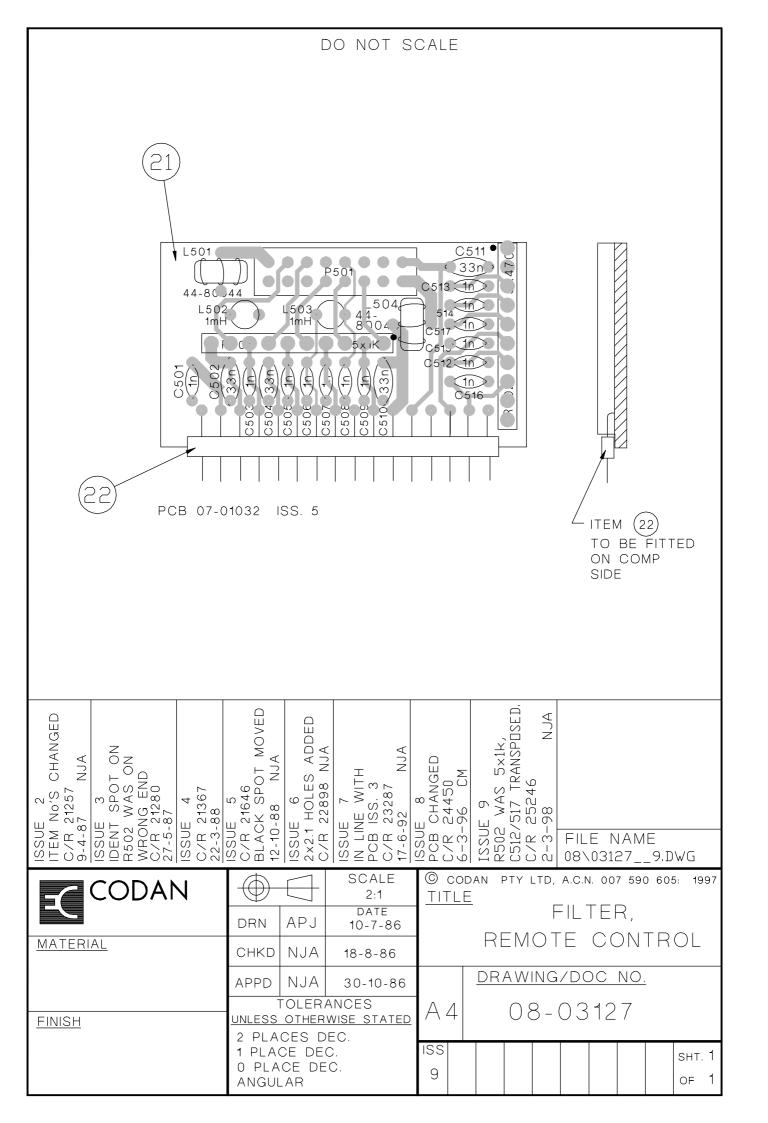


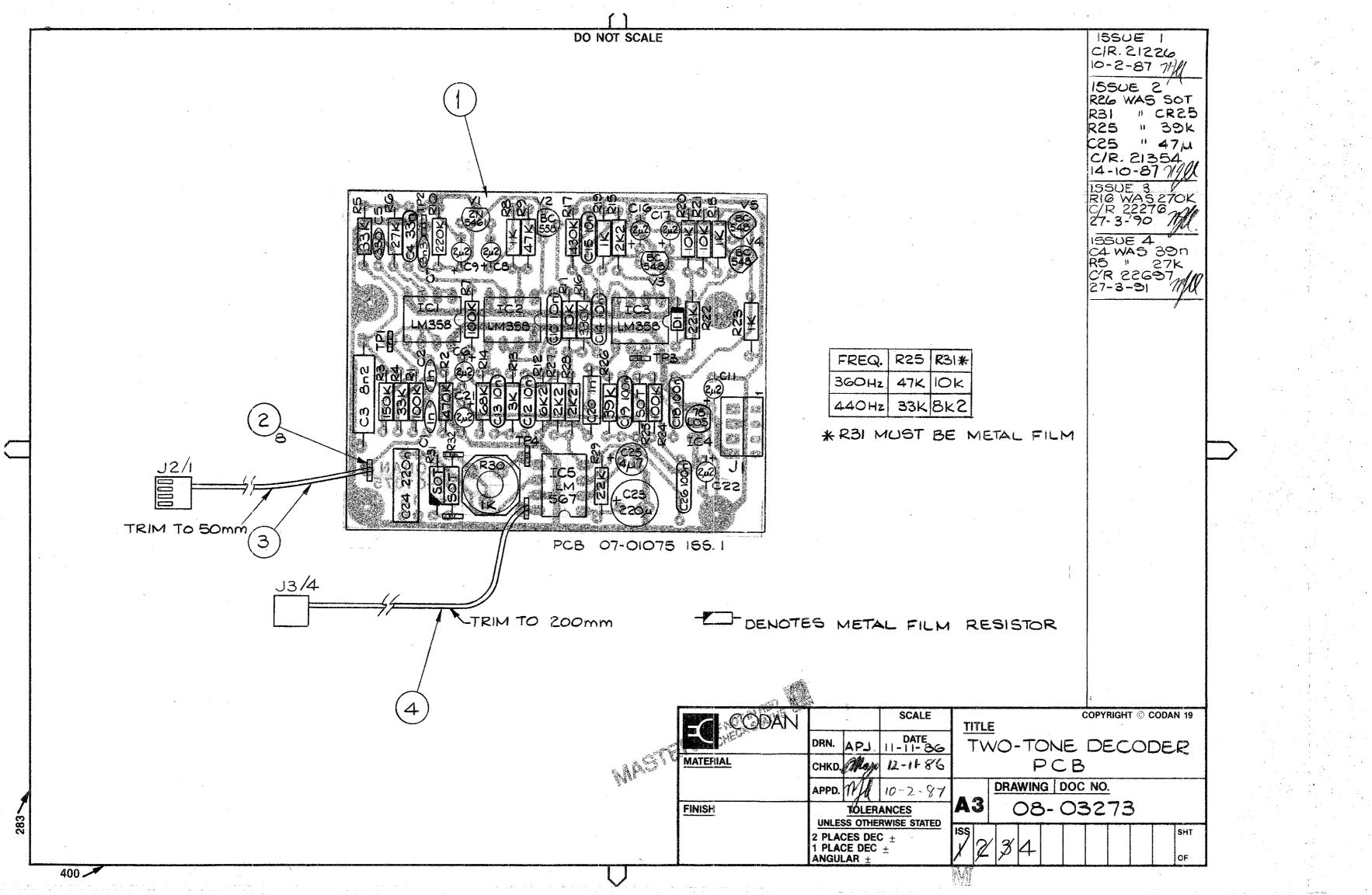




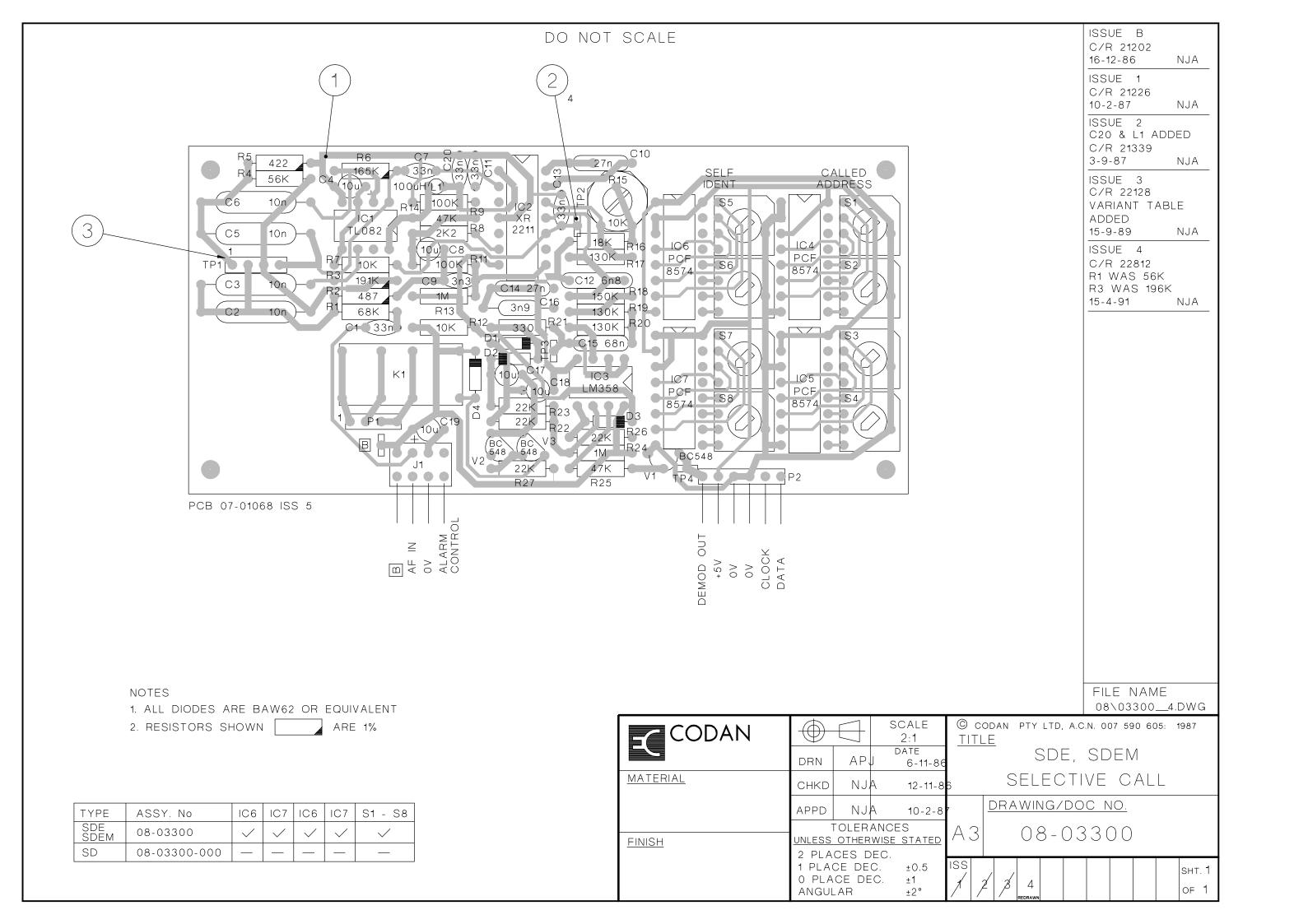


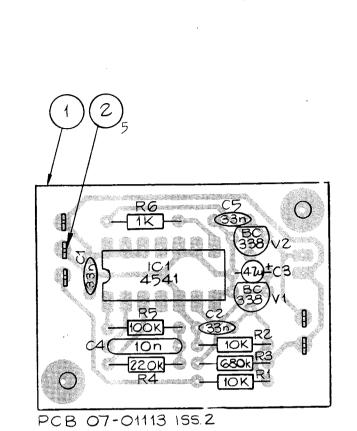






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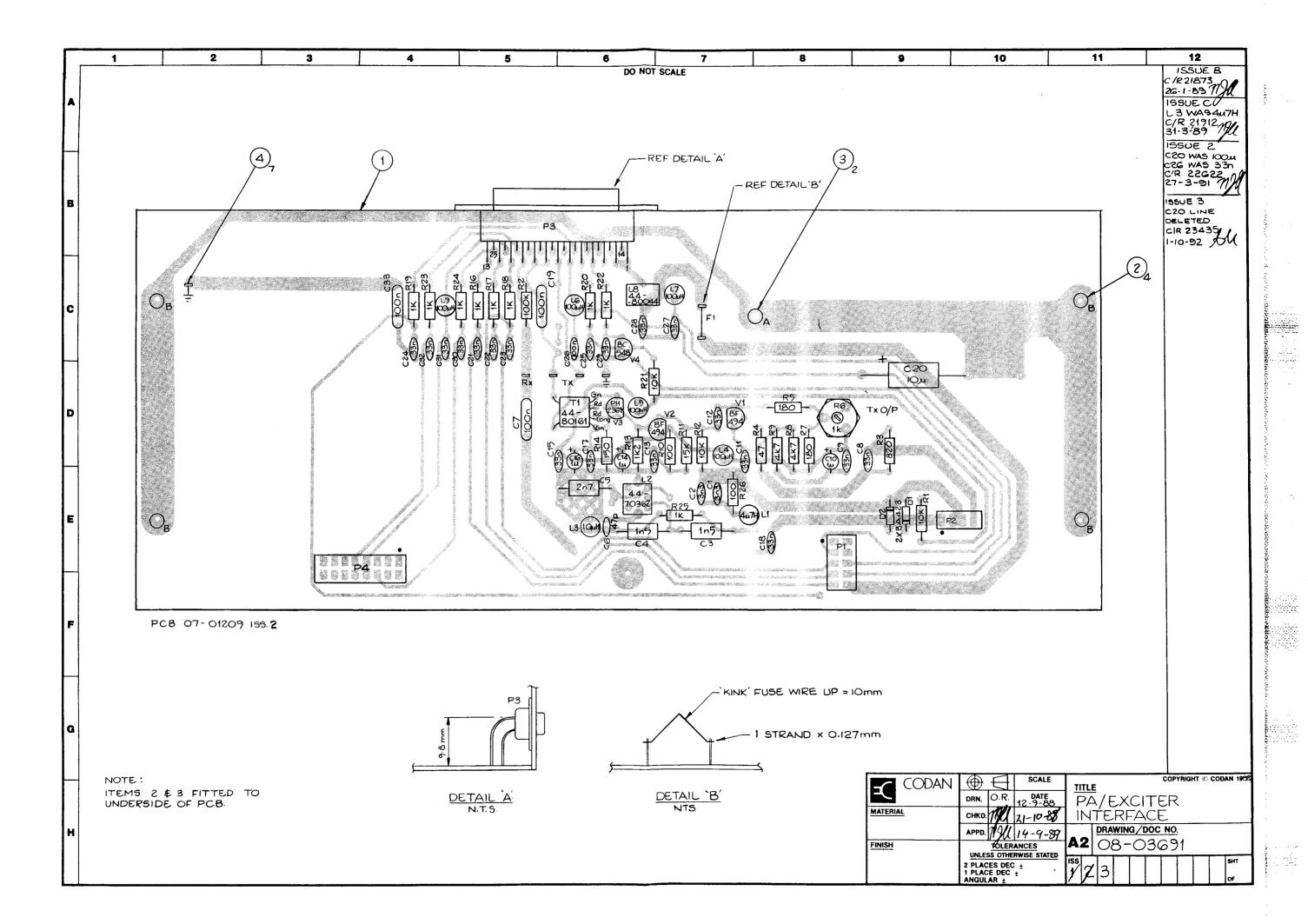


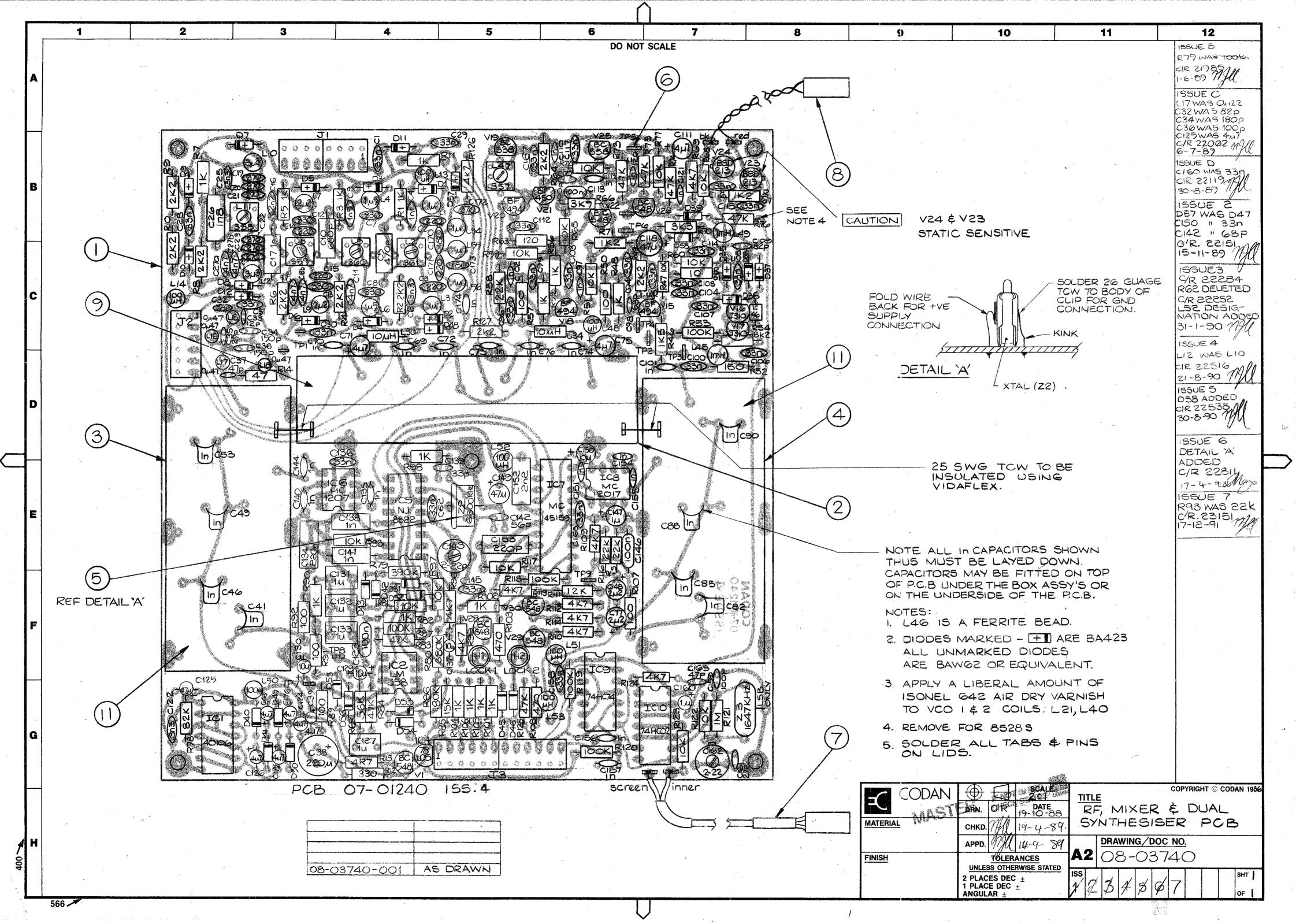


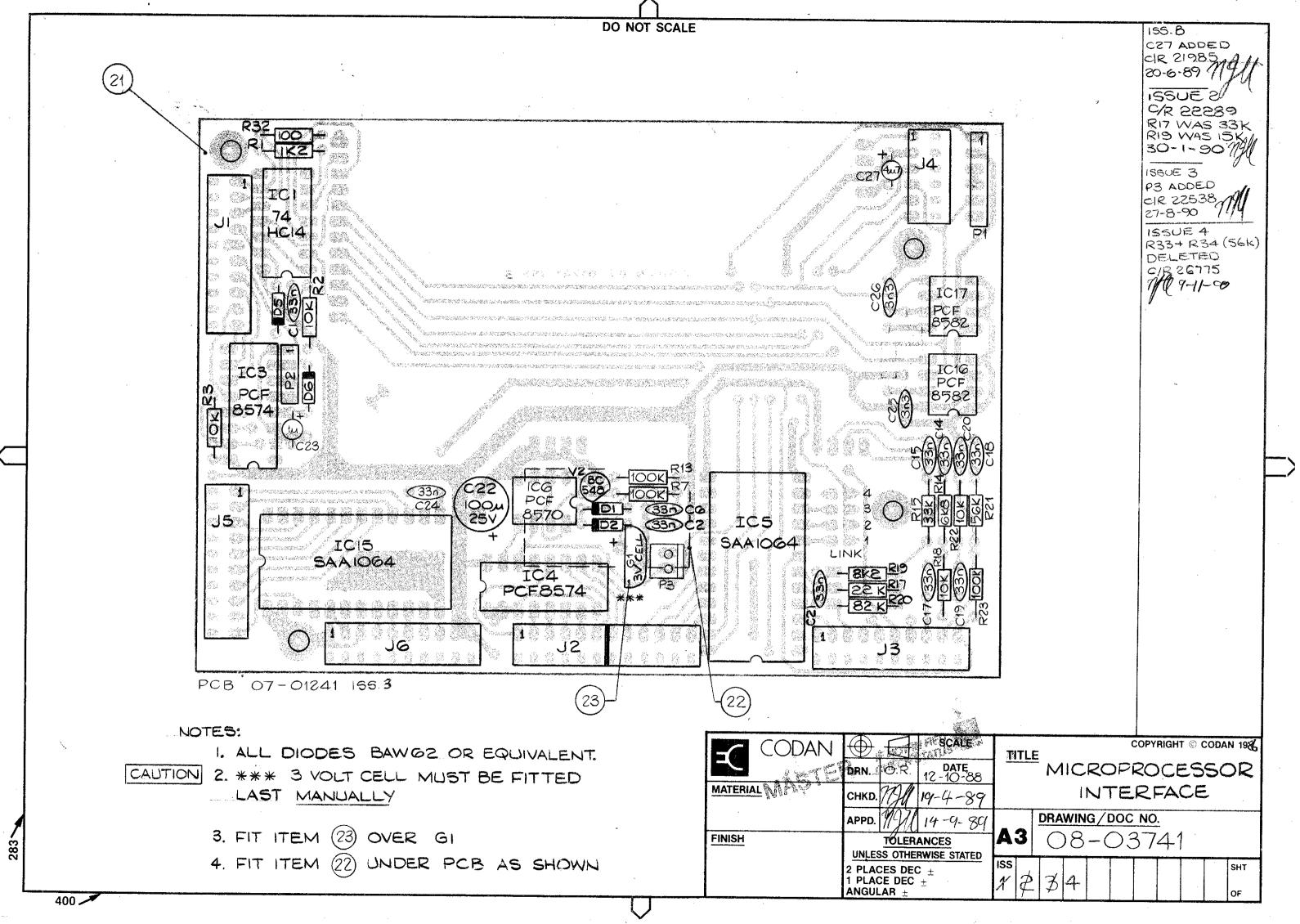
DO NOT SCALE

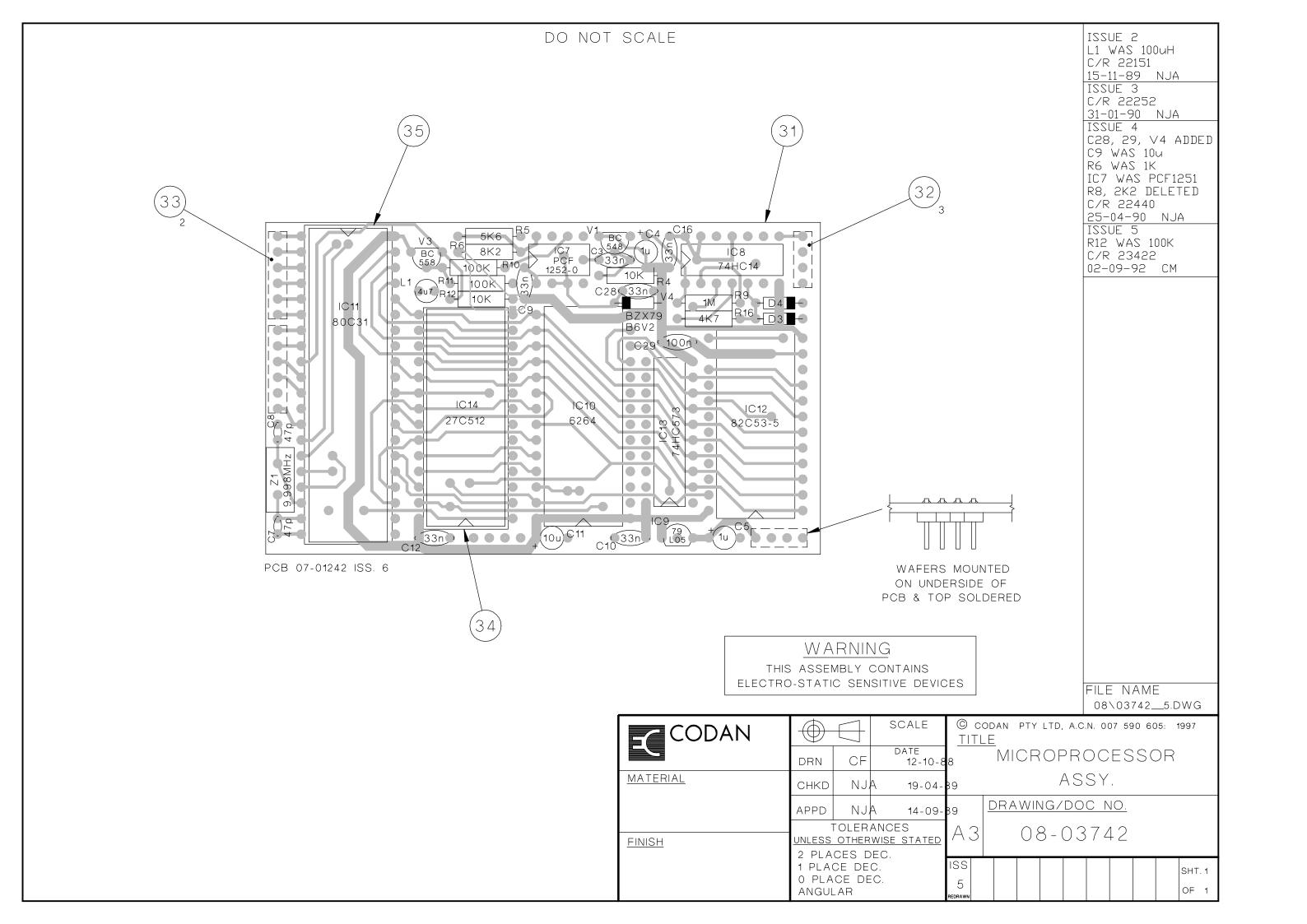
ISSUE B
R4 VALUE
CHGD FROM
200K
C/N 21697
24-11-88
ISSUE 2
C3 WAS 242
R3 WAS 470K
CIR 23584
24-2-93/

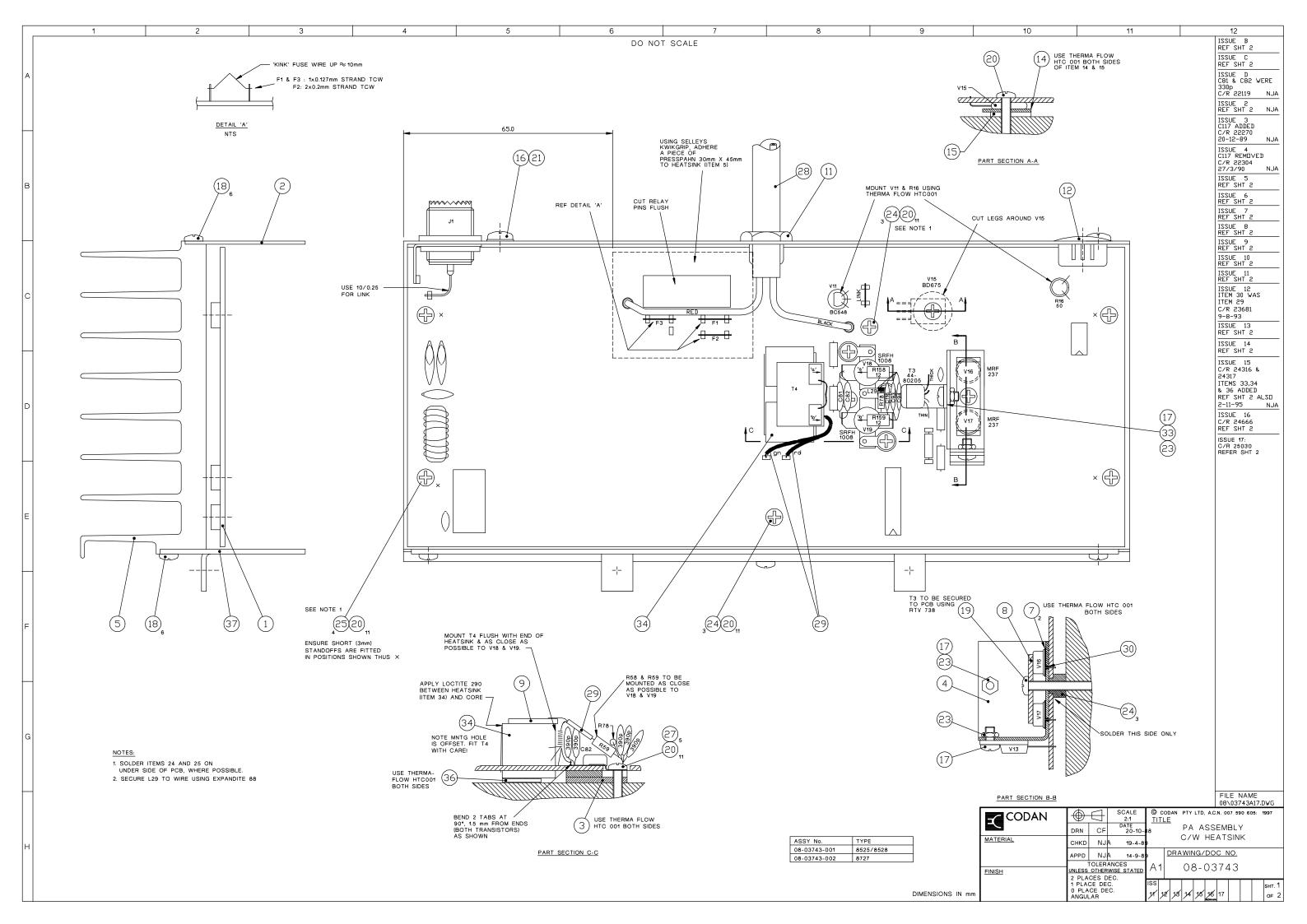
CODAN	SCALE TITLE COPYRIGHT © CODAN 19
	DRN. O.R. 8-9-88 PCB FAN CONTROLLER
MATERIAL	CHKD. 7741 11-10-89
	APPD. 74 14-9-89 DRAWING/DOC NO.
FINISH	TOLERANCES UNLESS OTHERWISE STATED
	2 PLACES DEC ± 1 PLACE DEC ± ANGULAR ±  ISS  OF 1

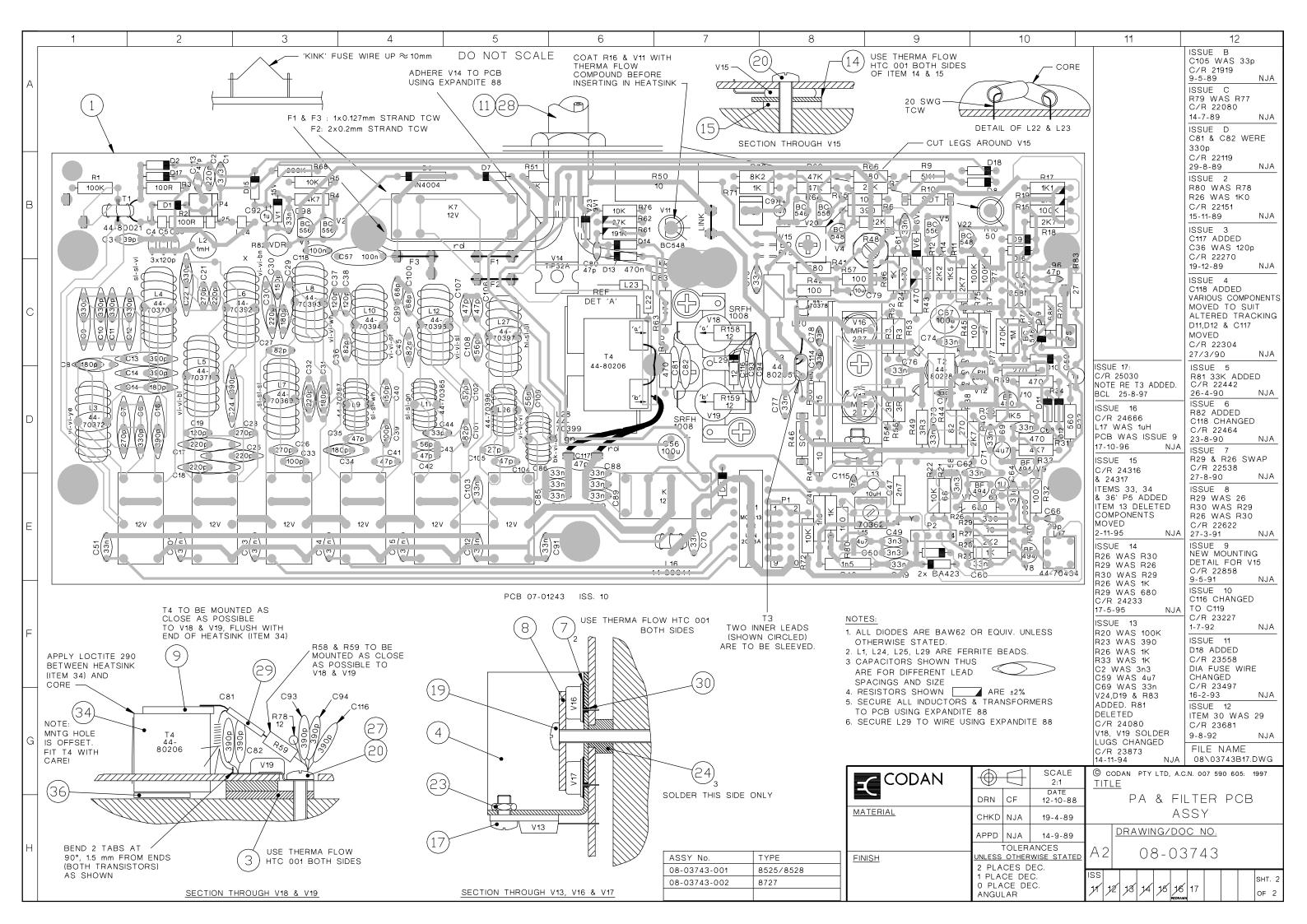


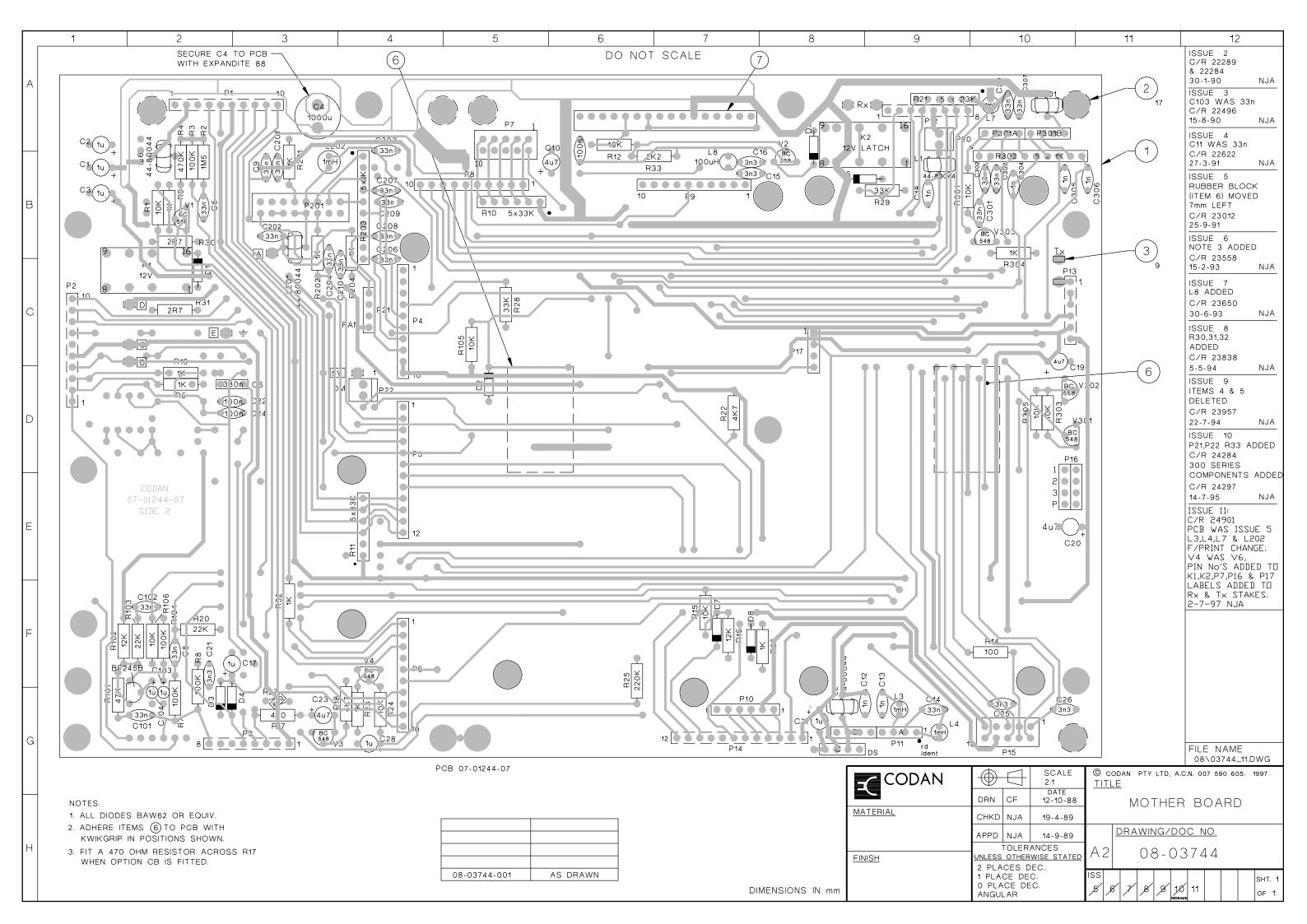


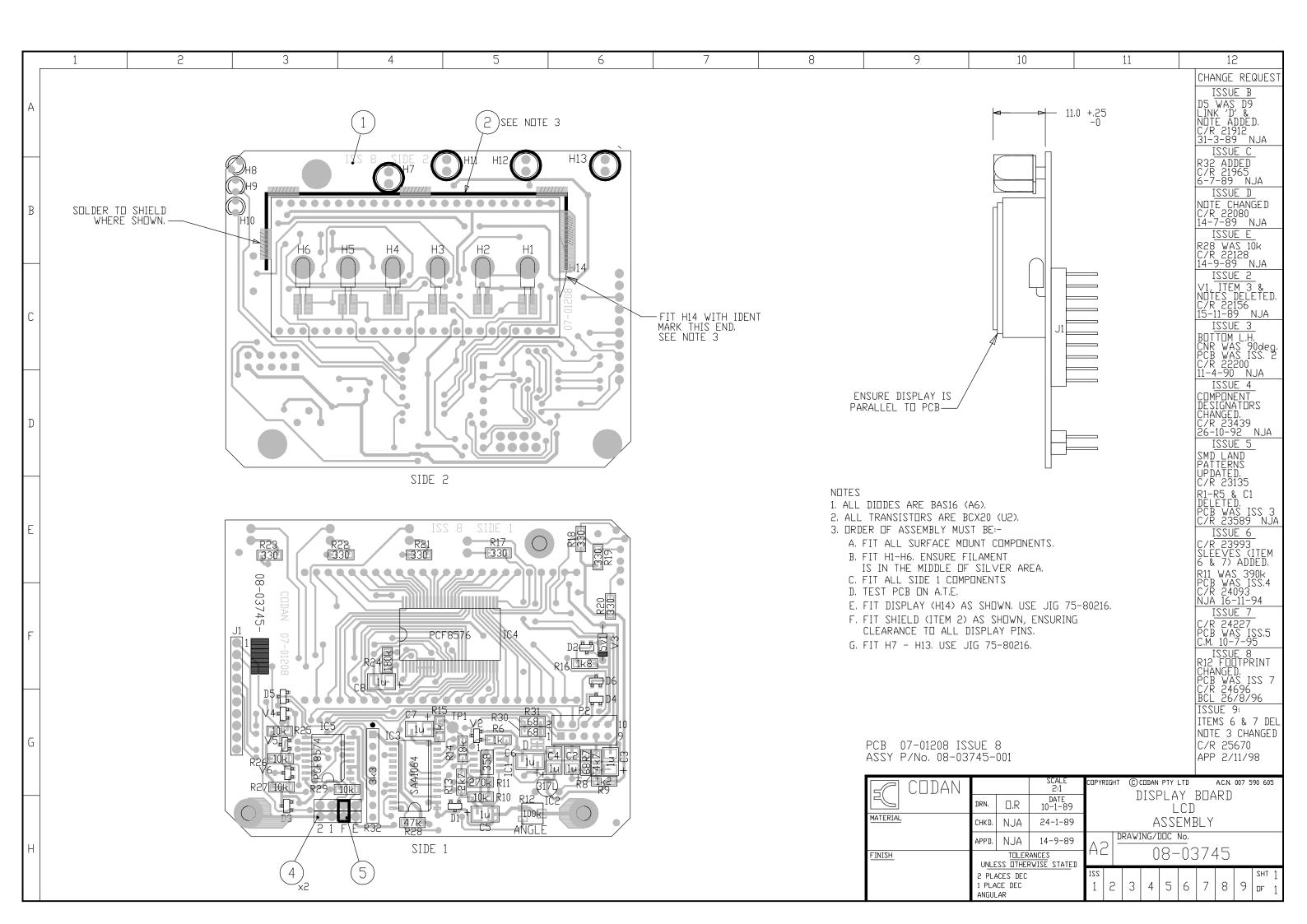


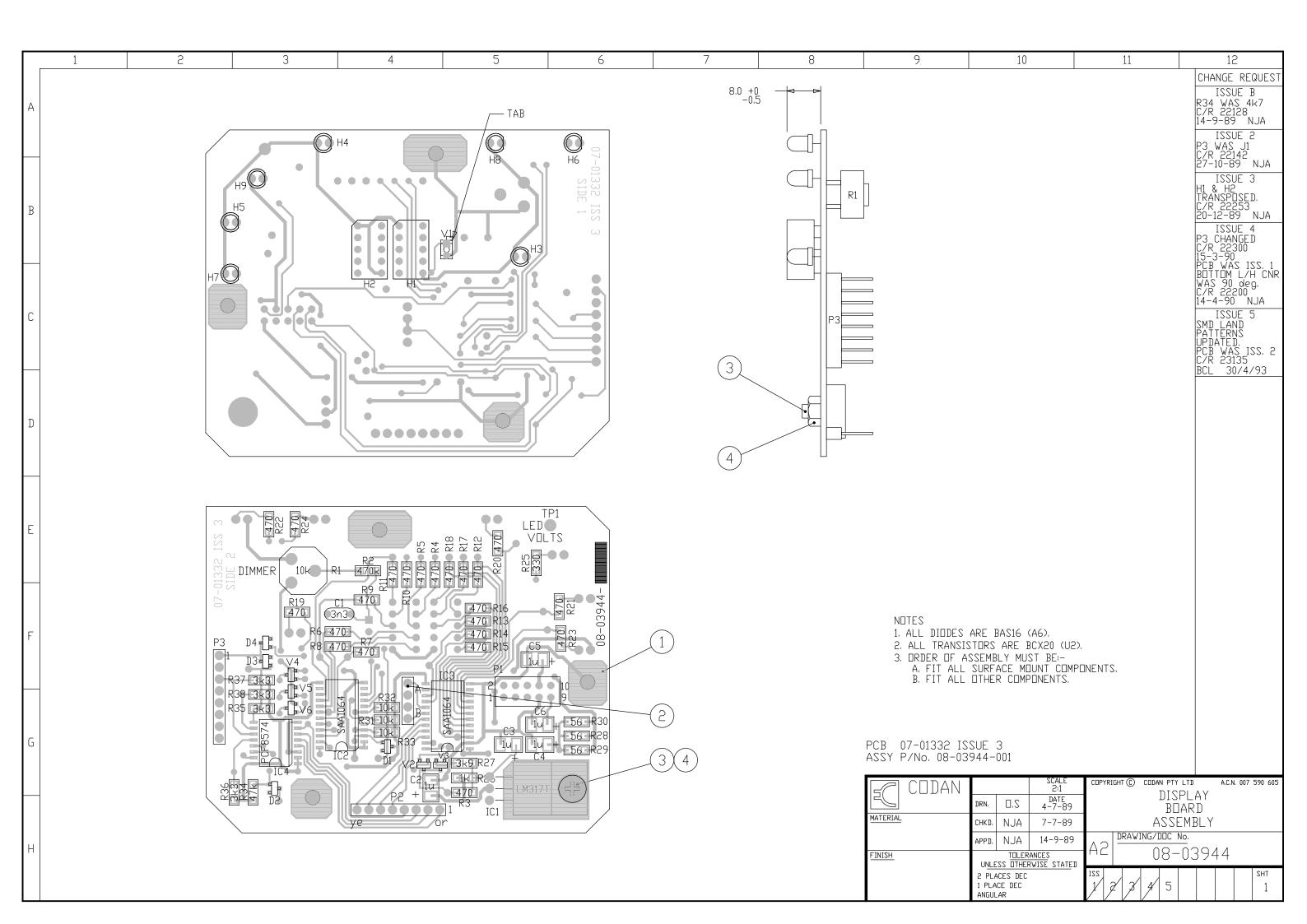


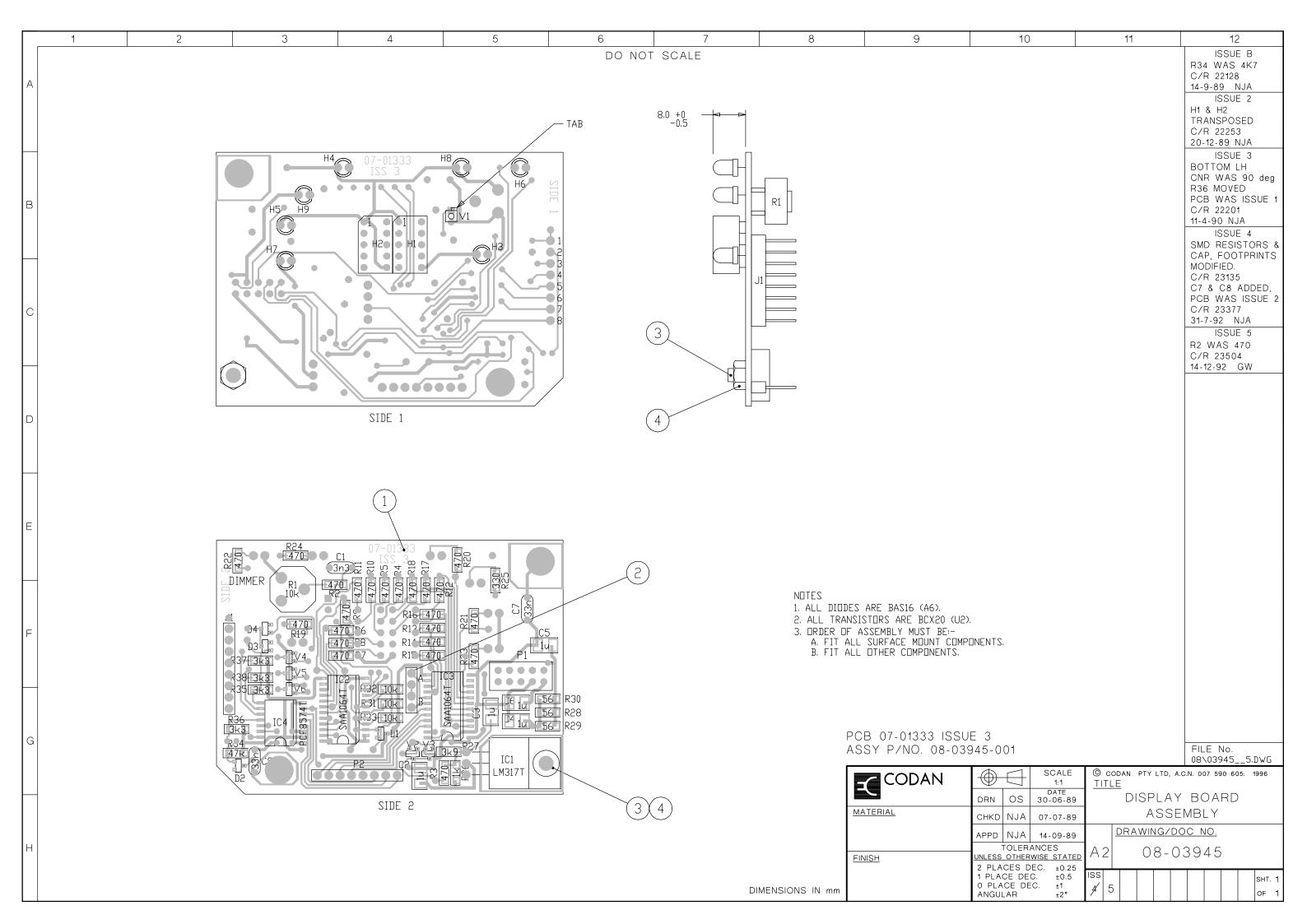


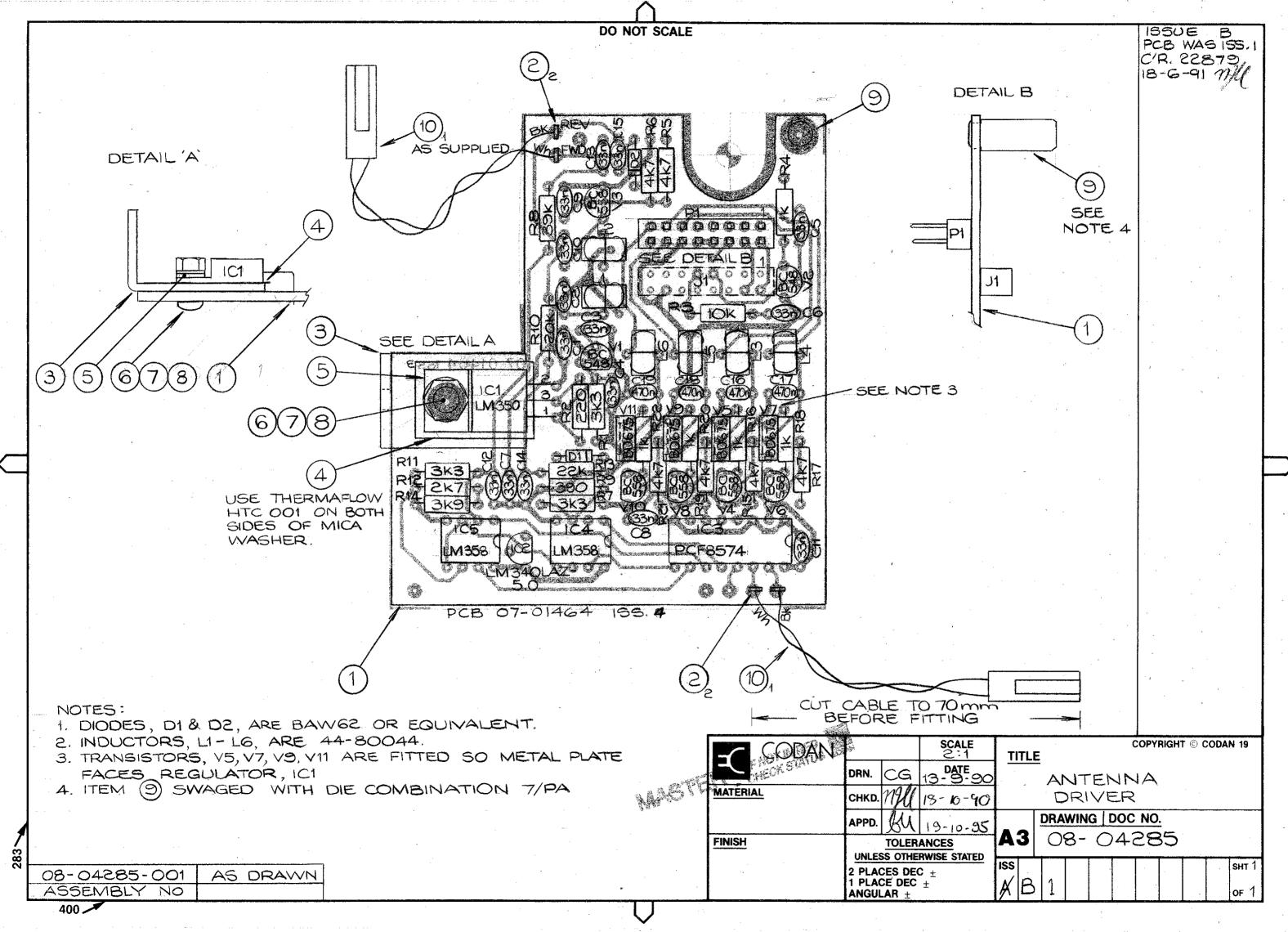


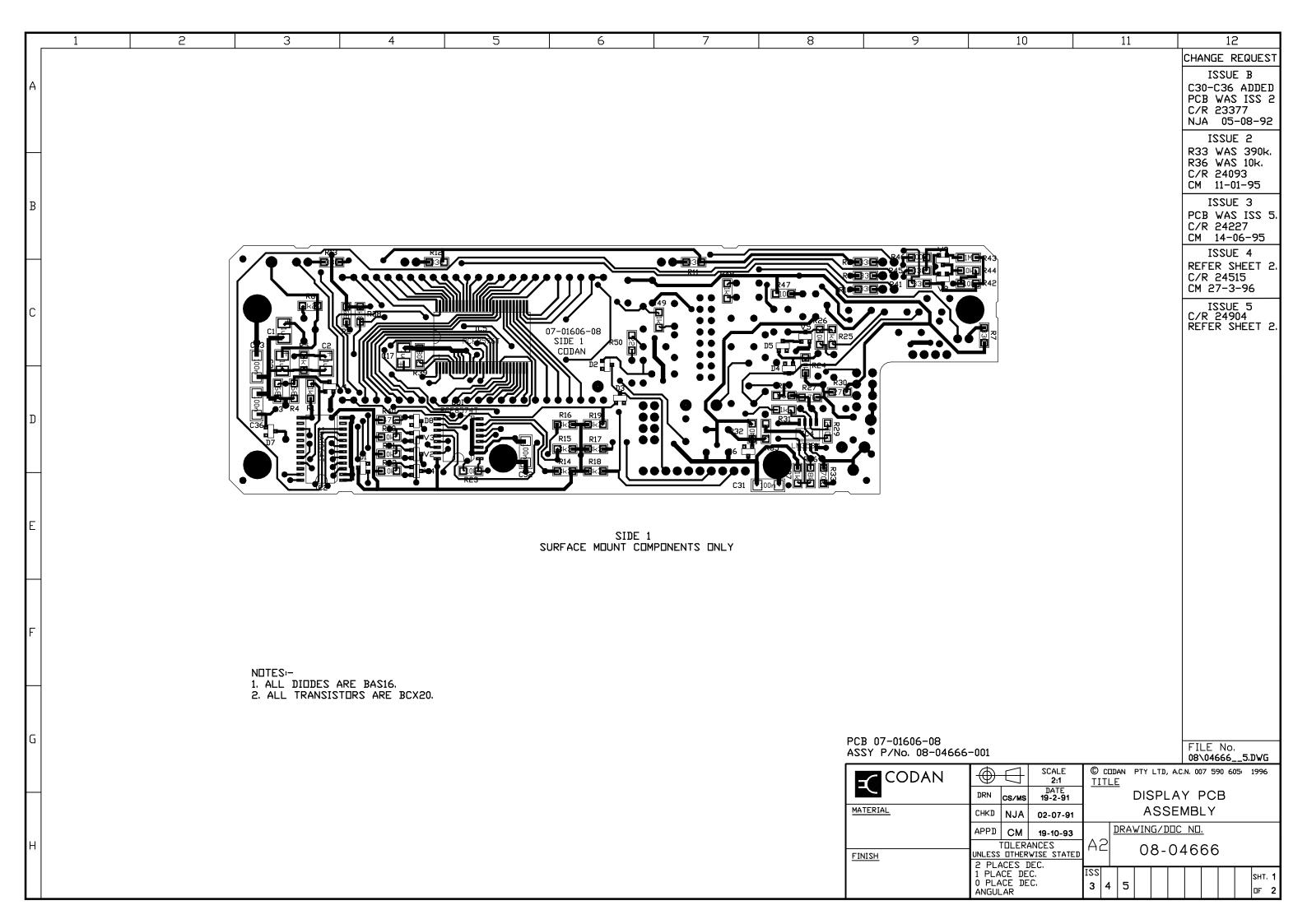


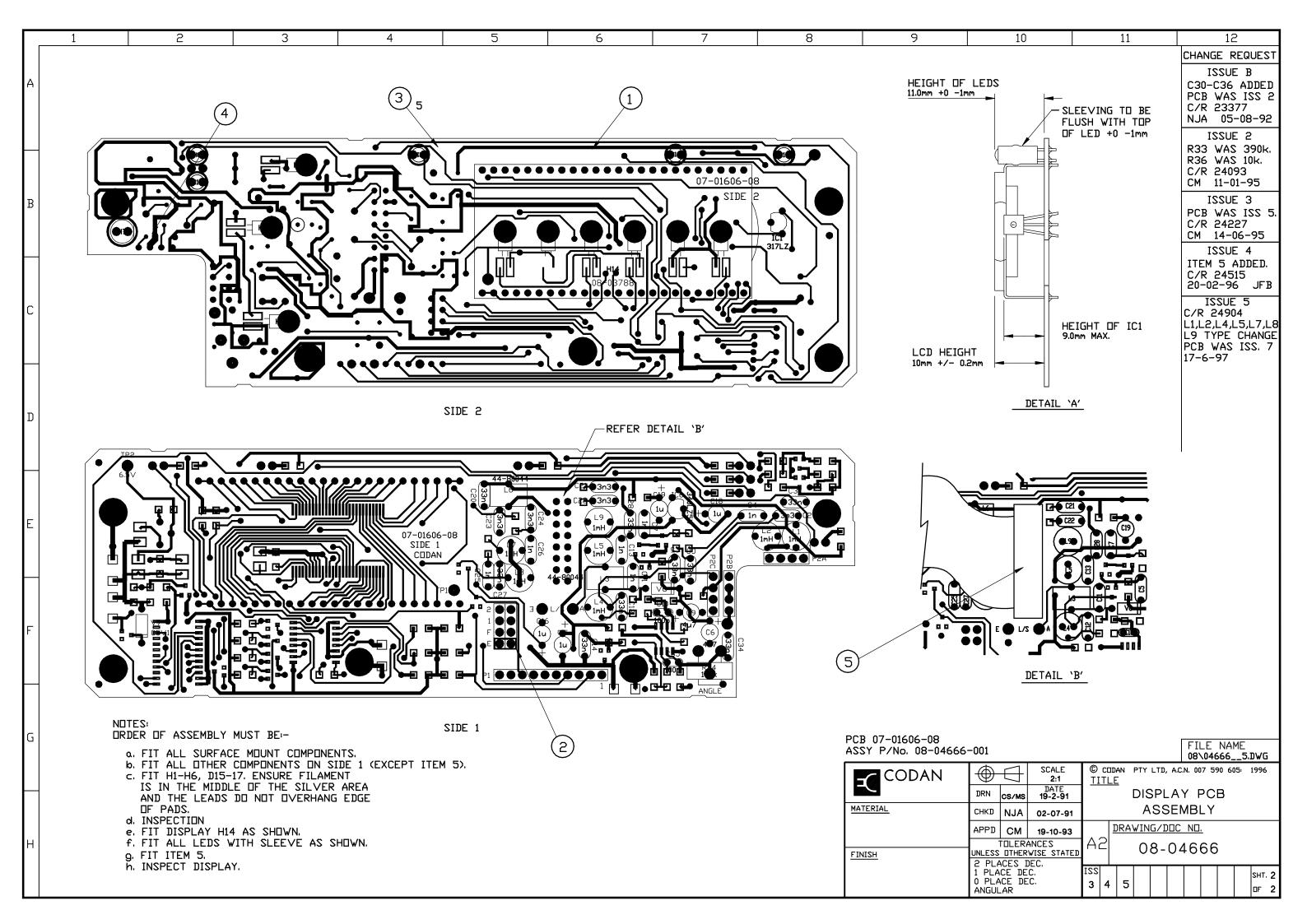


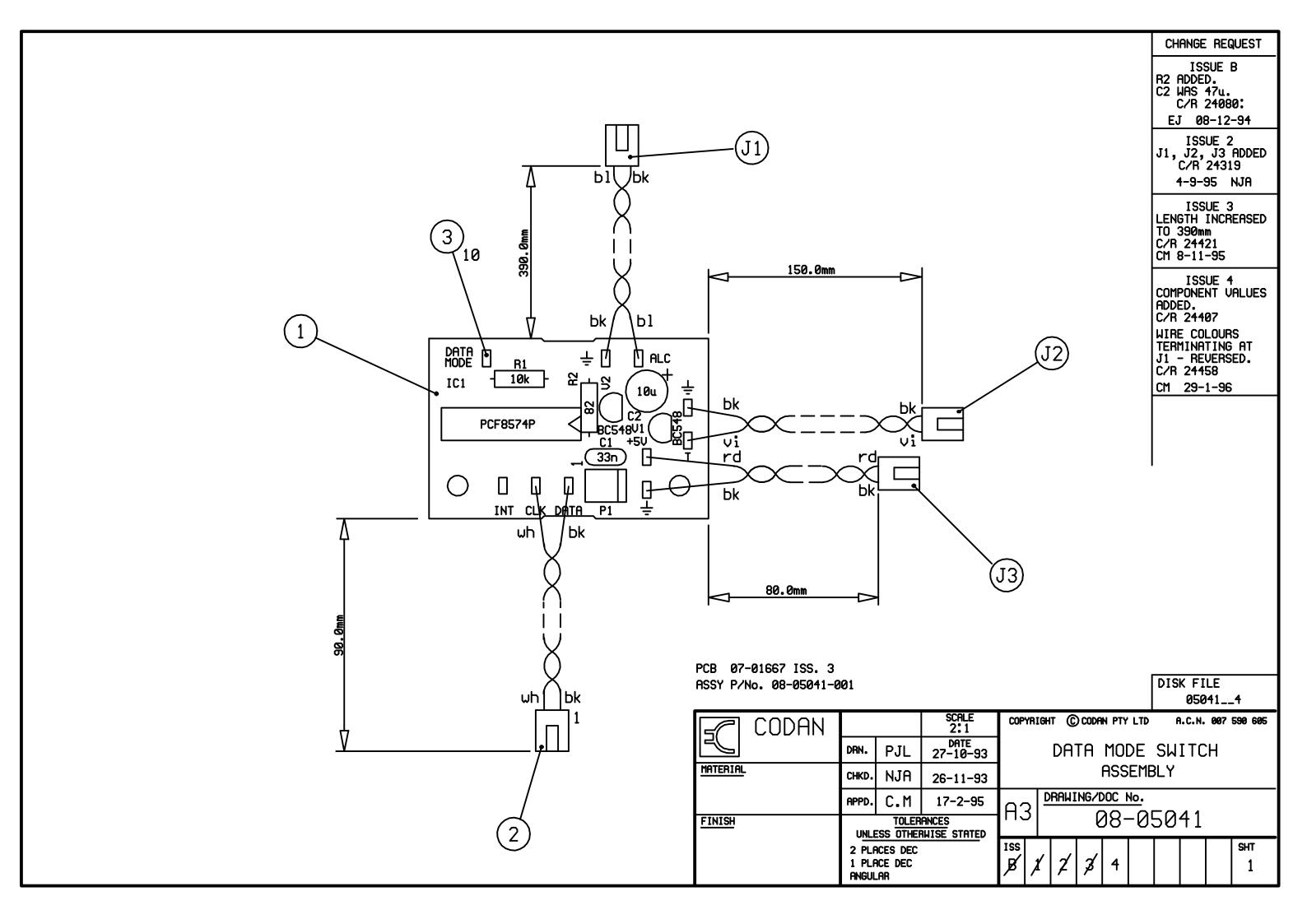


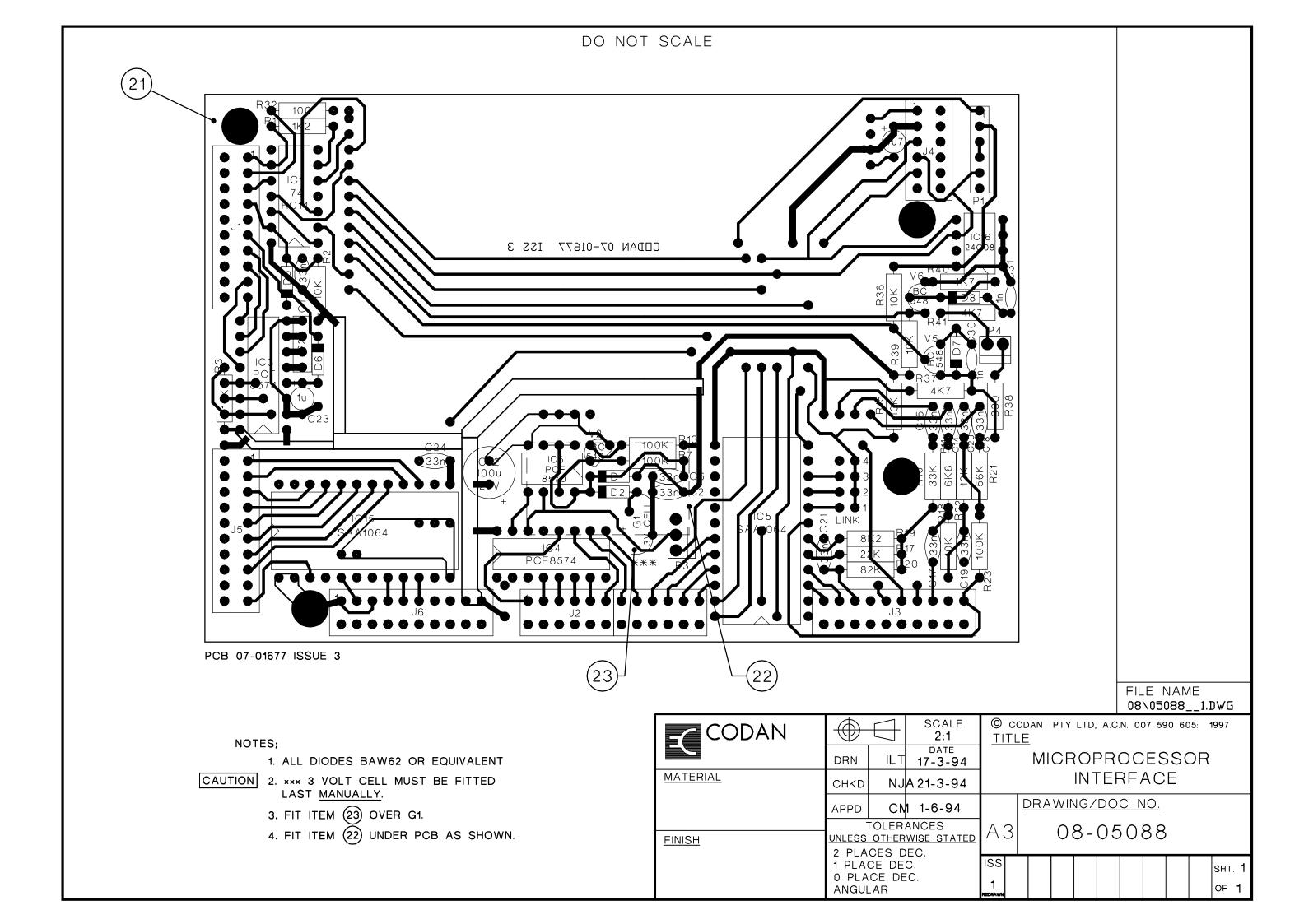


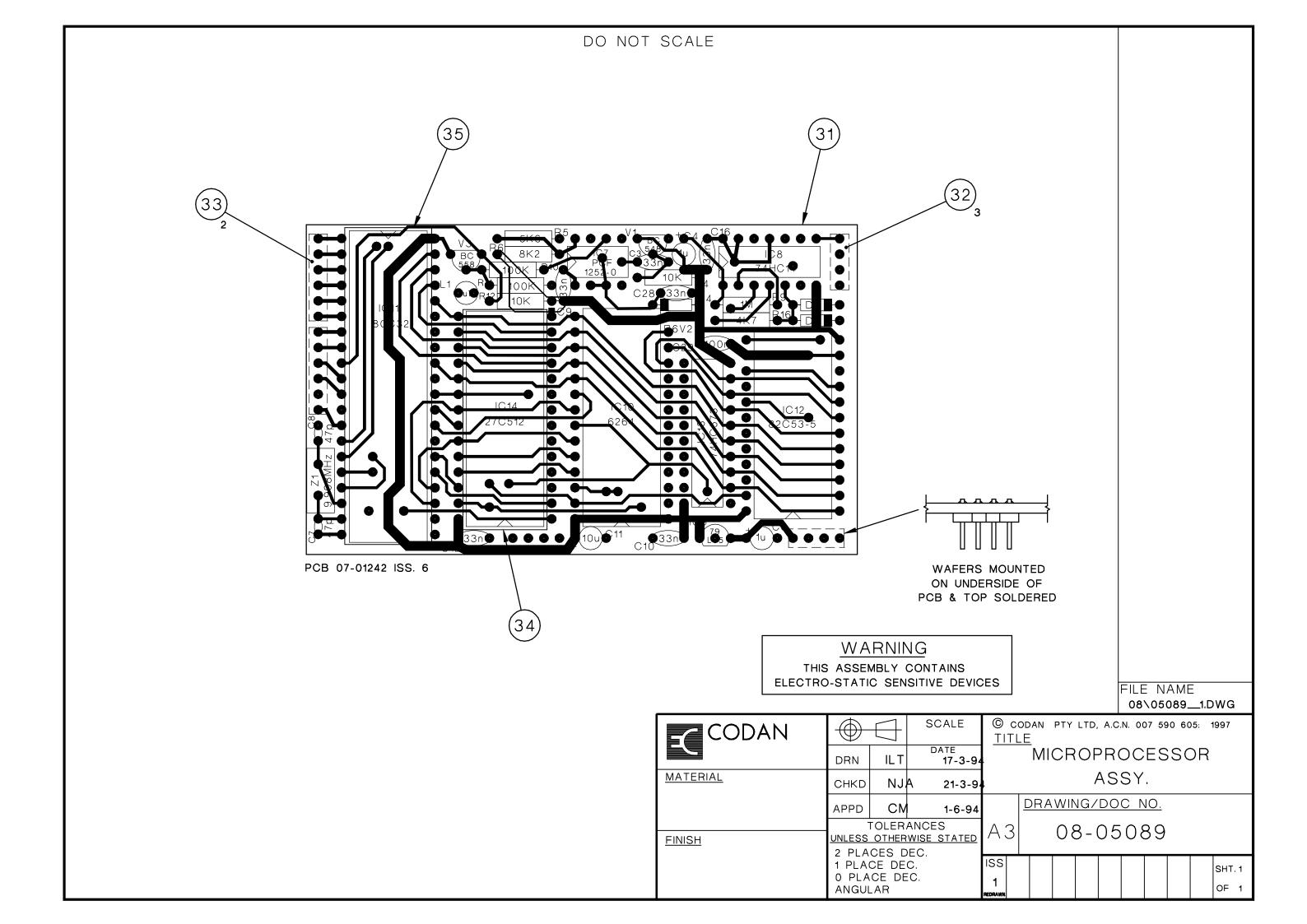


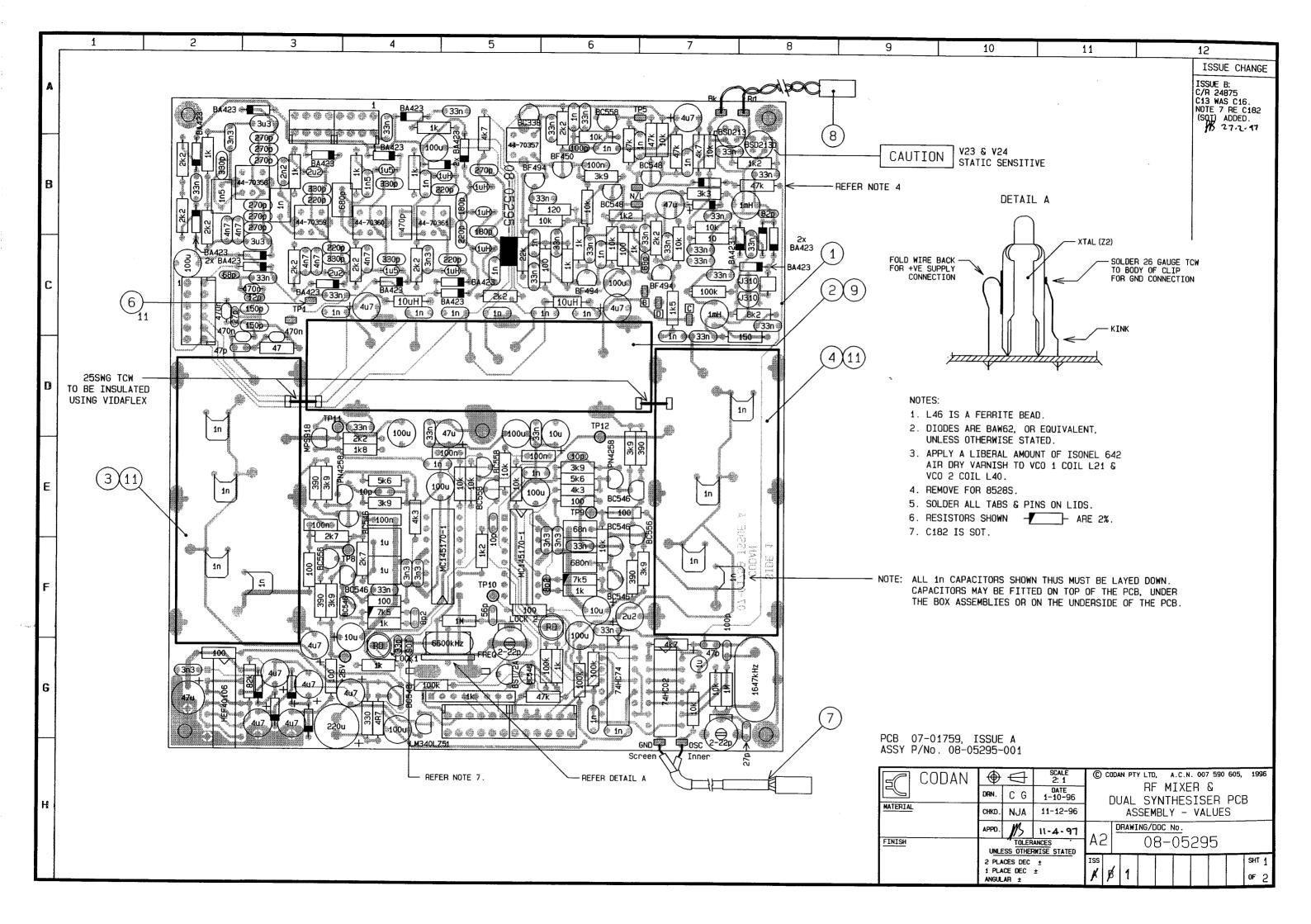


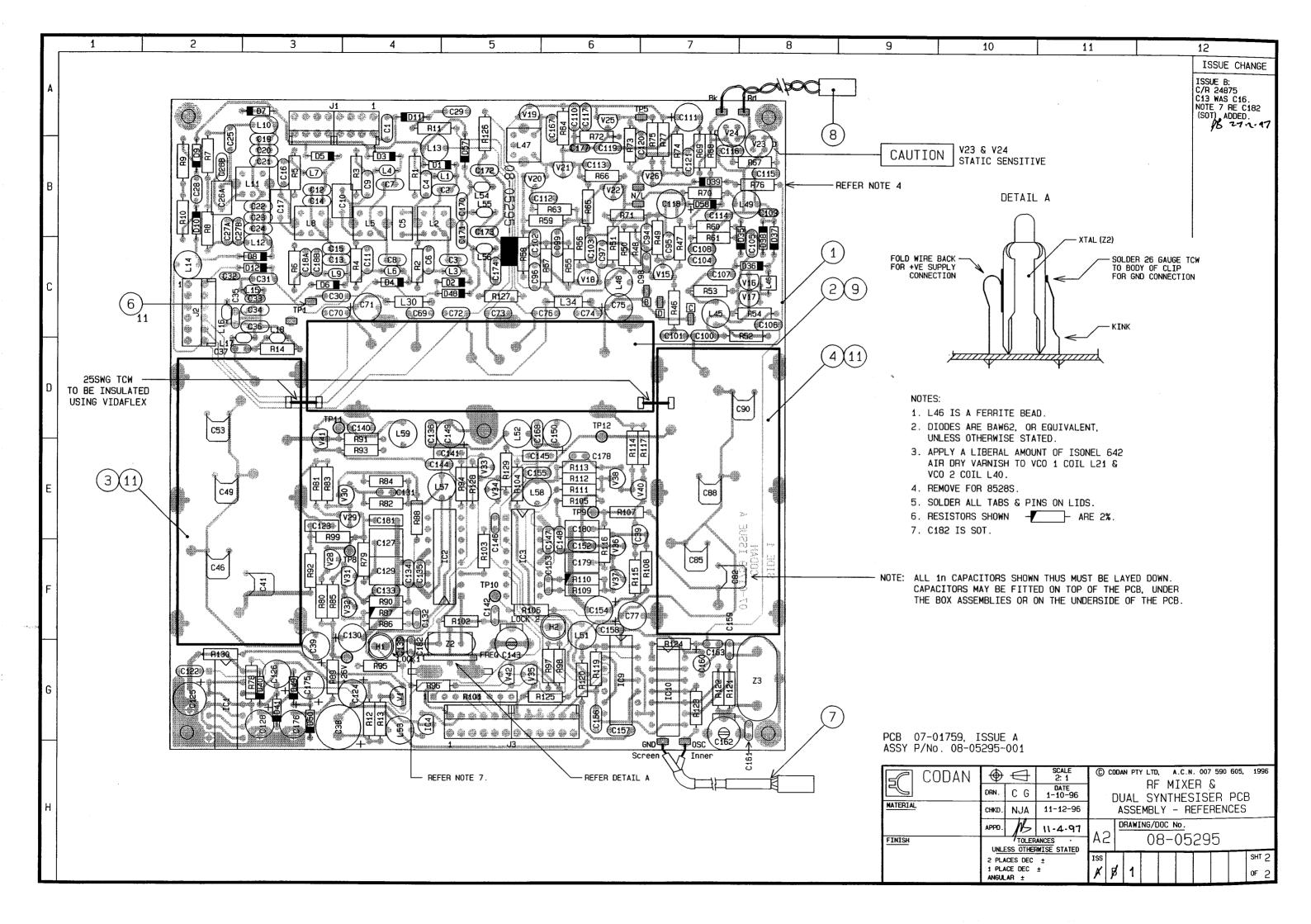


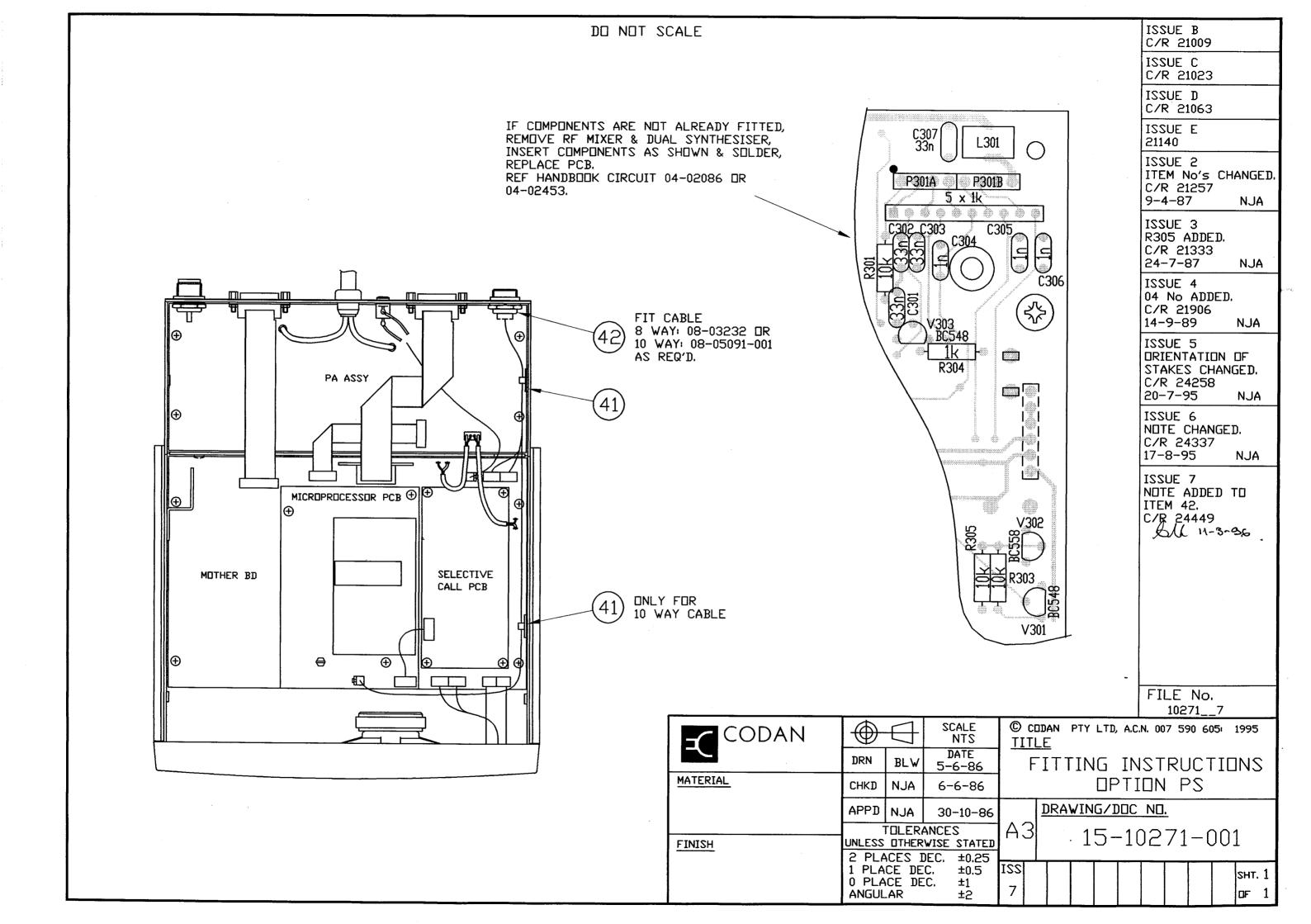


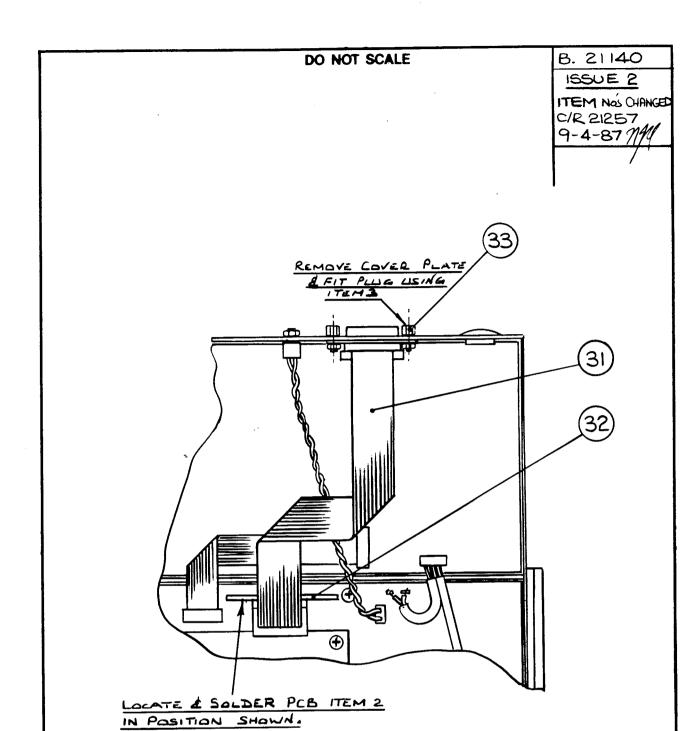












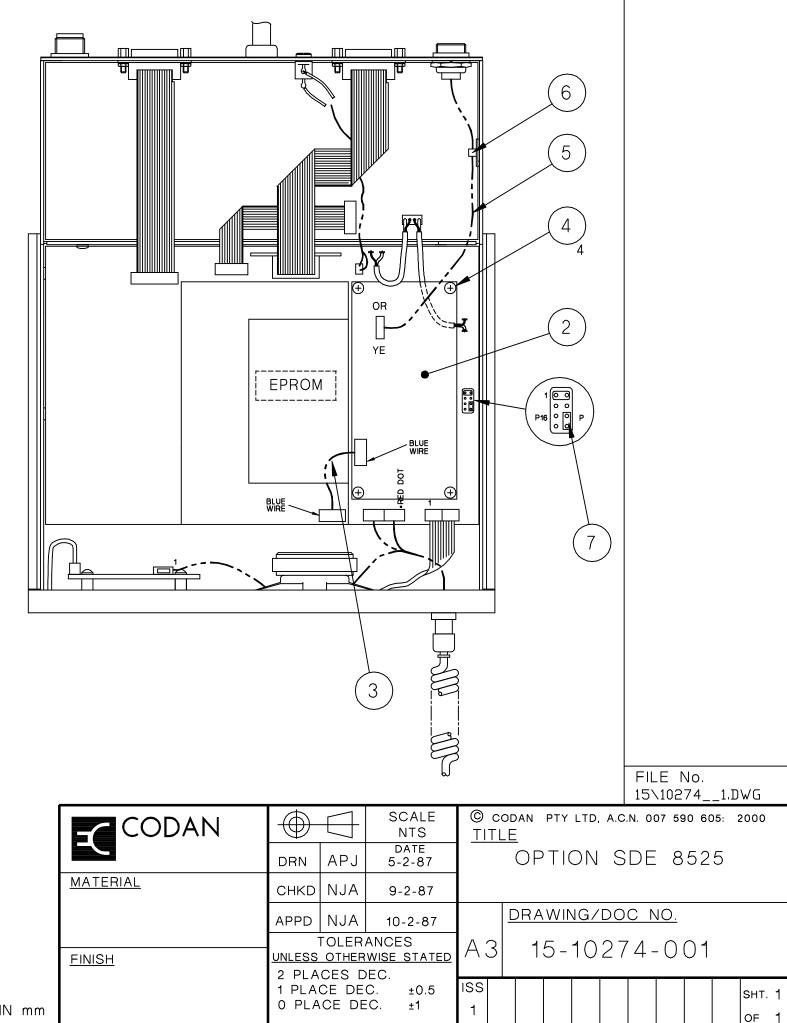
CODAN	SCALE NTS	COPYRIGHT © CODAN 19
	DRN. BLW 16-5-86	
MATERIAL	CHKD. N. 5-6-88	
	APPD. 7411 30-10-85	DRAWING/DOC NO.
FINISH	TOLERANCES	A4 15 - 10272-001
	UNLESS OTHERWISE STATED  2 PLACES DEC ± 1 PLACE DEC ± ANGULAR ±	ISS SHT / OF I

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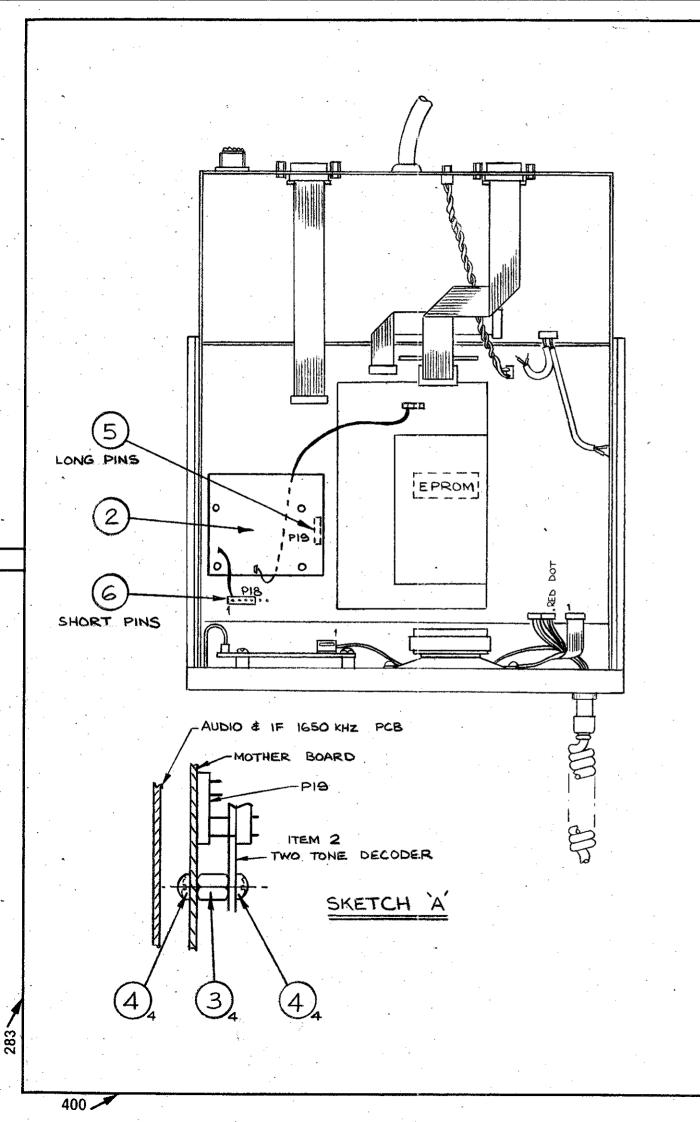
ITEM	PART No.	DESCRIPTION
1	15-10274-001	FITTING INSTRUCTIONS
2	08-03300	SELECTIVE CALL SDE SDEM
3	08-03331	LOOM SELECTIVE CALL
4	31-23006-501	SCREW M3x6 PAN POZI
5	08-03332	LOOM EXTERNAL ALARM
6	30-05102-001	CLAMP CABLE
7	60-90410-001	JUMPER

### NOTES:

- 1. REMOVE BOTTOM COVER.
- 2. WITH THE PCB (ITEM 2) IN THE CORRECT ORIENTATION, ALIGN WITH THE PLUG ON THE MOTHERBOARD. PUSH THE PCB (ITEM 2) DOWN UNTIL IT LOCATES ON THE SPACERS. SECURE USING THE 4 SCREWS (ITEM 4).
- 3. CONNECT THE SELECTIVE CALL PCB (ITEM 2) TO THE MICRO PROC. PCB USING ITEM 3. ENSURE CORRECT ORIENTATION OF SOCKETS.
- 4. CHECK ISSUE No. OF EPROM (TO BE AT LEAST 2.2).
- 5. REMOVE PLUG BUTTON FROM REAR PANEL.
- 6. REMOVE THE NUT & WASHER FROM PLUG END OF CABLE (ITEM 5).
  PASS THE SOCKET END OF THIS CABLE THROUGH THE HOLE IN THE BACK
  PANEL & LOCATE THE PLUG IN THE HOLE, REPLACE THE NUT & WASHER.
  CONNECT THE CABLE TO THE SELECTIVE CALL PCB (ITEM 2) AS SHOWN.
  SECURE CABLE TO THE SIDE BRACKET USING ITEM 6.
- 7. FIT JUMPER (ITEM 7) TO P16 ON MOTHER PCB AT "P" FOR SHORT PREAMBLE OR POSITION "1" FOR LONG PREAMBLE IN SCANNING SYSTEMS.
- 8. TEST FOR CORRECT OPERATION (SEE HANDBOOK IF REQUIRED).
- 9. REPLACE BOTTOM COVER.



DO NOT SCALE



DO NOT SCALE

ITEM	PART Nº	DESCRIPTION	
1	15-10276-001	FITTING INSTRUCTIONS	
2	08-03273	TWO TONE DECODER	<del></del>
3	05-03312-090	SPACER	
4	31-23006-501	SCREW M3 × 6	
PI9	60-00041-102	PLUG 4 WAY	
PI8	60-00041-100	PLUG 4 WAY	····

2.C/R. 21367 22-3-88 9/0 185UE 3 TWO UNUSED HOLES ADDED TO PIB. C/R 23793

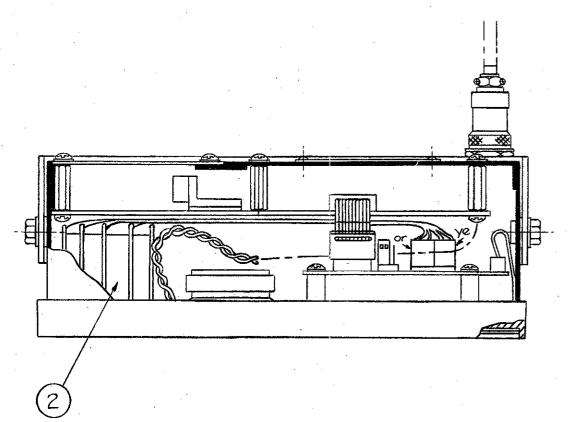
### NOTES:

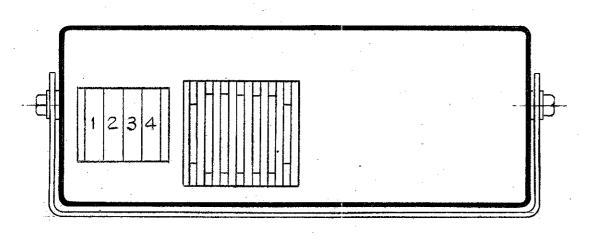
- I. REMOVE TOP & BOTTOM COVER.
- 2. DISCONNECT & REMOVE AUDIO & IF PCB (08-03025)
- 3, SOLDER PIS & PI9 IN POSITION SHOWN.
- 4. ASSEMBLE 4 SPACERS AS SHOWN IN SKETCH 'A'
- 5. WITH THE PCB (ITEM 2) IN THE CORRECT ORIENTATION ALIGN WITH THE PI9 ON THE MOTHER BOARD. PUSH THE PCB (ITEM 2) DOWN UNTIL IT LOCATES ON THE SPACERS. SECURE USING THE 4 SCREWS (ITEM 4)
- 6. CONNECT THE TWO SOCKETS AS SHOWN ONE TO THE MOTHER BOARD, THE OTHER TO THE MICRO PROC. BOARD ENSURE CORRECT ORIENTATION OF SOCKETS.
- 7. REPLACE & RECONNECT THE AUDIO & IF PCB.
- 8. TO ENABLE TONE CALLING THE EPROM MUST BE PROGRAMMED AS REQUIRED.
- 9. TEST FOR CORRECT OPERATION (SEE HANDBOOK IF REQUIRED)
- 10. REPLACE TOP & BOTTOM COVERS.

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### DO NOT SCALE

ITEM	PART NO	DESCRIPTION
	15-10282-001	FITTING INSTRUCTIONS
2	08-03301-004	SWITCH, MESH SELECTIVE CALL





FRONT VIEW

### NOTES:

- I. REMOVE FRONT PANEL BY UNSCREWING THE FOUR COUNTERSUNK SCREWS IN THE SIDES.
- 2. DISCONNECT ALL WIRING BETWEEN FRONT PANEL & MIC AMP & INTERFACE PCB.
- 3. USING FLAT NOSE PLIERS BREAK OUT THE FOUR SUPPORTING BARS IN THE DIGI SWITCH HOLE AREA.
- 4. USING A SHARP TOOL CUT OUT THE SWITCH LAYER & ESCUTCHEON 'IMPORTANT' THIS MUST BE DONE FROM THE INSIDE.
- 5. FROM THE FRONT FILE USING FORWARD STROKES ONLY, CLEAN THE SWITCH LAYER ESCUTCHEON & CASTING TO THE CUTOUT SIZE IN THE CASTING. 'IMPORTANT' ENSURE THIS DOES NOT EXCEED THE CAST CUTOUT SIZE.
- 6. PASS THE CABLE OF THE DIGI SWITCH ASSEMBLY THROUGH THE CUTOUT AND CLIP THE SIDE PLATE INTO POSITION ENSURING THE DIGI SWITCHES CAN BE READ THE CORRECT WAY ROUND. PLUG THE CABLE FROM THE DIGI SWITCHES INTO THE DISPLAY BOARD AS SHOWN. (LOOP EXCESS CABLE)
- 7. RECONNECT FRONT PANEL WIRING & PLUG. REPLACE FRONT PANEL USING EXISTING SCREWS.

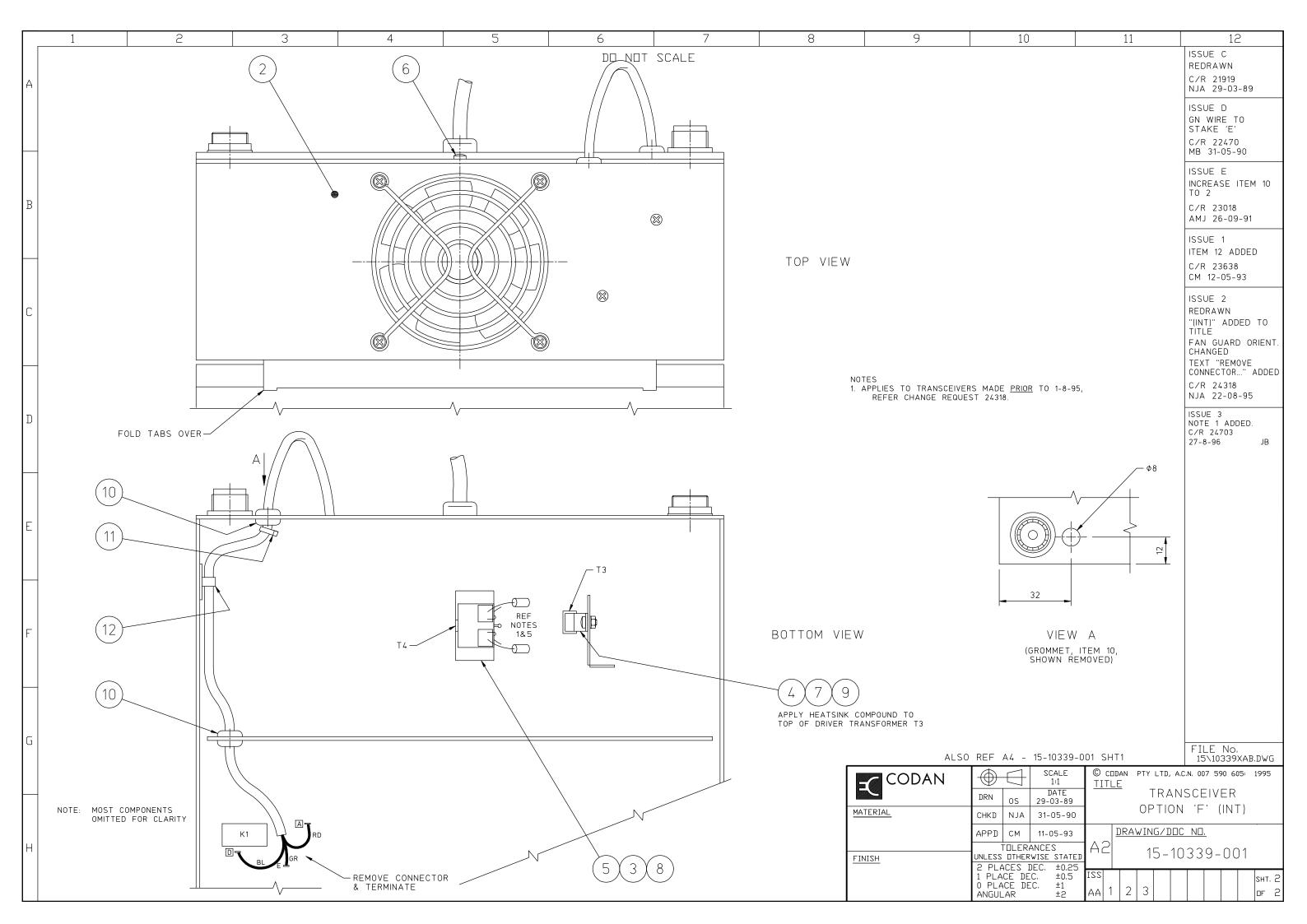
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### FITTING INSTRUCTIONS TRANSCEIVER - OPTION F

### (PA ASSEMBLY 08-03743 SERIES)

- 1. Obtain unfinished PA from current production batch and proceed to fit fan option as follows.
- 2. Secure existing resistor MTG PCB to top of O/P transformer heatsink with silastic and terminate 2 wires from T4 to PCB.
- 3. Drill out blind holes beneath T4 in PA heatsink Ø3.0 (through PCB) and remove all swarf.
- 4. Apply heatsink compound to fully line inside surface of O/P transformer heatsink (3).
- 5. Fit O/P transformer (5) T4 to heatsink (3), with MTG base on underside of s/assy.
- 6. Place O/P transformer s/assy in position and trim wires to suit ensuring centre wire is long enough to just go through PA PCB and doesn't touch PA heatsink.
- 7. Remove s/assy and apply heatsink compound to base and secure to PA heatsink with screw (8).
- 8. Terminate wires to PA PCB and terminate 2 wire from T4 to resistor PCB.
- 9. Remove diode D15 for 100 watt CW operation (60 watts CW when fitted)
- 10. Apply heatsink compound to top of driver heatsink bracket (4).
- 11. Fit driver heatsink bracket (4) using screw and nut (7) and (9) ensuring bracket is placed squarely and firmly on top of T3.
- 12. Drill hole  $\emptyset$ 8.0 in rear panel as shown and fit grommet.
- 13. Fit fan/cover assembly (2) to top of PA heatsink using screw (6) and folding tabs over heatsink flange. Fit grommet to screen.
- 14. Fit cable through rear panel and screen.
- 15. Fit cable clamp and route cable as shown, fit cable tie, remove connector (leaving long leads) and solder ends as shown.

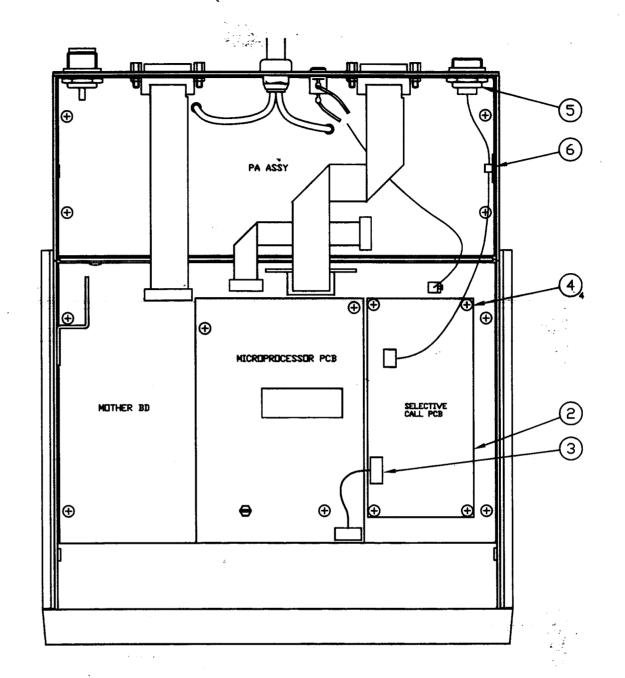
CODAN	TRANSCE	EIVER OPTI	ON F	DRG 15-10339-001	iss 3	SHT 1 of 2
ORIG	DATE	CHKD	DATE	APPD	DATE	
OS	11/4/89		13/4/89	CM	11/5/93	



REF	PART No	DESCRIPTION	QTY
1	15-10366-001	FITTING INSTRUCTIONS	1
2	08-03300-000	SELECTIVE CALL SD	1
3	08-03331	LODM, SELECTIVE CALL	1
4	31-23006-501	SCREW M3x6 PAN POZI	4
5	08-03332	LOOM, EXTERNAL ALARM	1
6	30-05102-001	CLAMP, CABLE ADHESIVE BACKED	1

### NOTES

- 1. REMOVE BOTTOM COVER.
- 2. WITH THE PCB (ITEM 2) IN CORRECT DRIENTATION, ALIGN WITH THE PLUG ON THE MOTHER BOARD, PUSH THE PCB (ITEM 2) DOWN UNTIL IT LOCATES ON THE SPACERS. SECURE USING THE 4 SCREWS (ITEM 4).
- 3. CONNECT THE SELECTIVE CALL PCB (ITEM 2) TO THE MICRO-PROCESSOR PCB USING CABLE (ITEM 3) ENSURING CORRECT ORIENTATION OF SOCKETS.
- 4. CHECK ISSUE No OF EPROM (TO BE AT LEAST 1.2 FOR 8528S).
- 5. REMOVE PLUG BUTTON FROM REAR PANÊL.
- 6. REMOVE THE NUT & WASHER FROM PLUG END OF CABLE (ITEM 5), PASS THE SOCKET END OF THIS CABLE THROUGH THE HOLE IN THE REAR PANEL, LOCATE THE PLUG IN THE HOLE & REPLACE THE NUT & WASHER, CONNECT THE CABLE TO THE SELECTIVE CALL PCB (ITEM 2) AS SHOWN. SECURE CABLE TO THE SIDE BRACKET USING THE CABLE CLAMP (ITEM 6).
- 7. TEST FOR CORRECT OPERATION (SEE HANDBOOK IF REQUIRED).
- 8. REPLACE BOTTOM COVER.



P/No 15-10366-001 - AS DETAILED

DISC FILE 10366\_1

CODAN	<b>(</b>	$ \!\!\!\!\!/$	SCALE 1:1	_	CODA		PTY	LTD,	A.C.N	l. 007	59	0 605	jı :	1993
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MATERIAL	CHKD	ALN	13-8-89		F]		Π	IG	IN	ST	RL	JCT	II	JNS
	APPD	ALM	14-9-89	^		RAV						_		_
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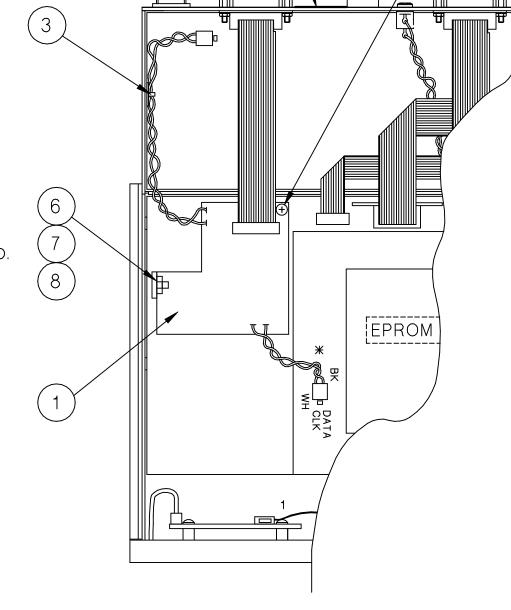
ITEM	PART No.	DESCRIPTION
1	08-04285-001	ANTENNA DRIVER PCB
2	06-01453	LABEL, WARNING
3	30-05102-001	CLAMP, CABLE ADHESIVE
4	31-23020-580	SCREW, M3×20

# DO NOT SCALE 2 3

ISSUE 1 NOTE 2 CHANGED ITEM 5 DELETED. C/R 23769 CM 1-11-93

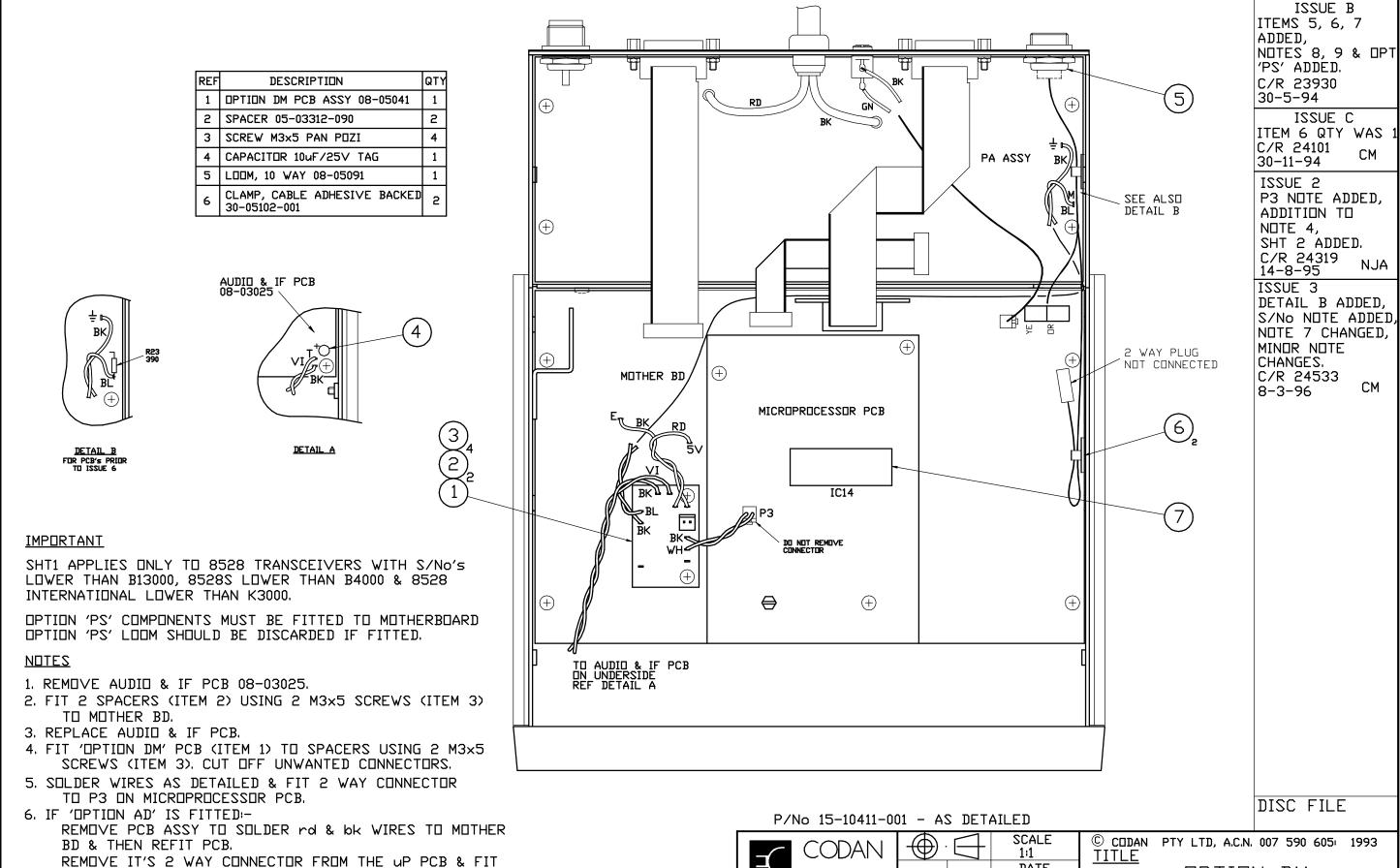
# NOTES:

- 1. REMOVE BOTTOM COVER.
- 2. IF REQUIRED FIT TWO PIN CONNECTOR \*(PART No. 60-00020-260 NOT SUPPLIED) ΤΟ THE μP ASSY NOTE CORRECT ORIENTATION.
- 3. REMOVE (ITEMS 6,7&8) AND UNPLUG 16 WAY RIBBON CONNECTOR FROM MOTHERBOARD. REMOVE SCREW FROM MOTHERBOARD SO (ITEM 4) MAY BE FITTED.
- 4. PLUG (ITEM 1) INTO P201 MAKING SURE THAT THE PINS ENGAGE CORRECTLY.
- 5. REPLACE (ITEMS 6,7&8) AND FIT (ITEM 4).
- 6. PLUG 16 WAY RIBBON CABLE INTO (ITEM 1) AND PLUG THE TWO WAY CABLES \*INTO THE PA AND UP PCB AS SHOWN.
- 7. FIT ADHESIVE CLIP (ITEM 3) AS SHOWN AND SECURE CABLE.
- 8. CHECK THAT THE SOFTWARE IS ISSUE 3 OR GREATER.
- 9. FIT WARNING LABEL (ITEM 2) OVER ANTENNA CONTROL ON REAR PANEL.
- 10. REPLACE BOTTOM COVER.
  - \* IF NECESSARY THE WIRES OF THE SHORT TWISTED PAIR CAN BE SOLDERED DIRECTLY TO THE PCB (BK = DATA WH = CLK)

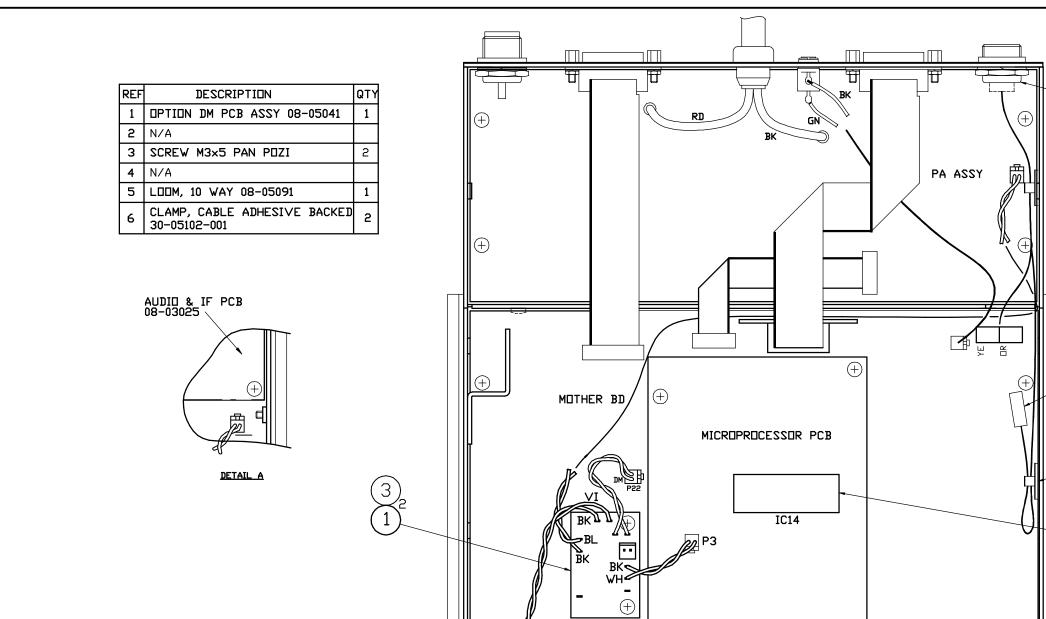


FILE No. 15\10384\_\_1.DWG

CODAN			SCALE NTS	© d	CODAN	PTY	LTD,	A.C.	.N. 00	O7 59	90 60	)5: :	2000
	DRN	APJ	DATE 15-11-90			0	РΤ	10	Ν.	АD			
MATERIAL	CHKD	NJA	15-11-90	F		NG	11	1S	TRI	JC	TI(	NC	S
	APPD	СМ	1-11-93		DRA	<u> WIN</u>	NG/	DO	<u>C N</u>	<u>10.</u>			
<u>FINISH</u>	UNLESS		ANCES WISE STATED	A3 15-10384-001									
	1 PLA	CE DE	C. ±0.5	ISS 1									SHT. 1 OF 1



DATE OPTION DM ONTO 'OPTION DM' PCB & THEN FIT 'OPTION DM' CONNECTOR DRN DB28-10-93 TO UP BD P3. FITTING INSTRUCTIONS **MATERIAL** ALN 9-11-93 CHKD 7. CHANGE C57 1uF ON AUDIO & IF PCB TO 10uF (ITEM 4). 8. FIT 10 WAY CONNECTOR & LOOM (ITEM 5) & ATTACH WITH DRAWING/DOC NO. APPD CM 30-11-94 CLAMP (ITEM 6) AS SHOWN. 15-10411-001 TOLERANCES FINISH 9. ENSURE EPROM (ITEM 7) IS AT LEAST ISSUE 5.0. UNLESS OTHERWISE STATED 2 PLACES DEC. ± 0.25 SHT. 1 PLACE DEC. ± 0.5 2 3 of 2 ANGULAR ±



(+)

TO AUDIO & IF PCB ON UNDERSIDE REF DETAIL A  $\ominus$ 

 $\oplus$ 

### <u>IMPORTANT</u>

SHT2 APPLIES ONLY TO 8528 TRANSCEIVERS WITH S/No's HIGHER THAN B12999, 8528S HIGHER THAN B3999 & 8528 INTERNATIONAL HIGHER THAN K2999.

OPTION 'PS' LOOM SHOULD BE DISCARDED IF FITTED.

### <u>NOTES</u>

- 1. FIT 'OPTION DM' PCB (ITEM 1) TO EXISTING SPACERS USING 2 M3×5 SCREWS (ITEM 3) & FIT TO CONNECTORS AS SHOWN.
- 2. IF 'OPTION AD' IS FITTED:REMOVE IT'S 2 WAY CONNECTOR FROM THE UP PCB & FIT
  ONTO 'OPTION DM' PCB & THEN FIT 'OPTION DM' CONNECTOR
  TO UP BD P3.
- 3. FIT 10 WAY CONNECTOR & LOOM (ITEM 5) & ATTACH WITH CLAMP (ITEM 6) AS SHOWN.
- 4. ENSURE EPROM (ITEM 7) IS AT LEAST ISSUE 5.0.

P/No 15-10411-0							D		F I 411_					
CODAN	© CODAN PTY LTD, A.C.N. 007 590 605: 1993 TITLE									1993				
	DRN	DB	DATE 11-8-95			-		]P	ΓΙΕ	]N	DI	М		
MATERIAL CHKD NJA 19-8-95						FITTING INSTRUCTIONS								
	APPD	СМ	14-8-95	Λ ′		RAV	√INC	5/D	ПС	ND.				
FINISH	UNLESS		ANCES WISE STATED		)		1	.5	<u>-1</u>	04	<u> 111</u>	_ [	<u> </u>	1
		CES D	EC. ± 0.25	SSI										знт. 2
1 PLACE DEC. ± 0.5 ANGULAR ±					3									of 2

ISSUE B

ISSUE C

S/No NOTE ADDED,

CM

REF SHT 1

REF SHT 1

ISSUE 2

REF SHT1

C/R 24319 14-8-95

MINOR NOTE

ISSUE 3

CHANGES. C/R 24533

8-3-96

SHT 2 ADDED.

(5)

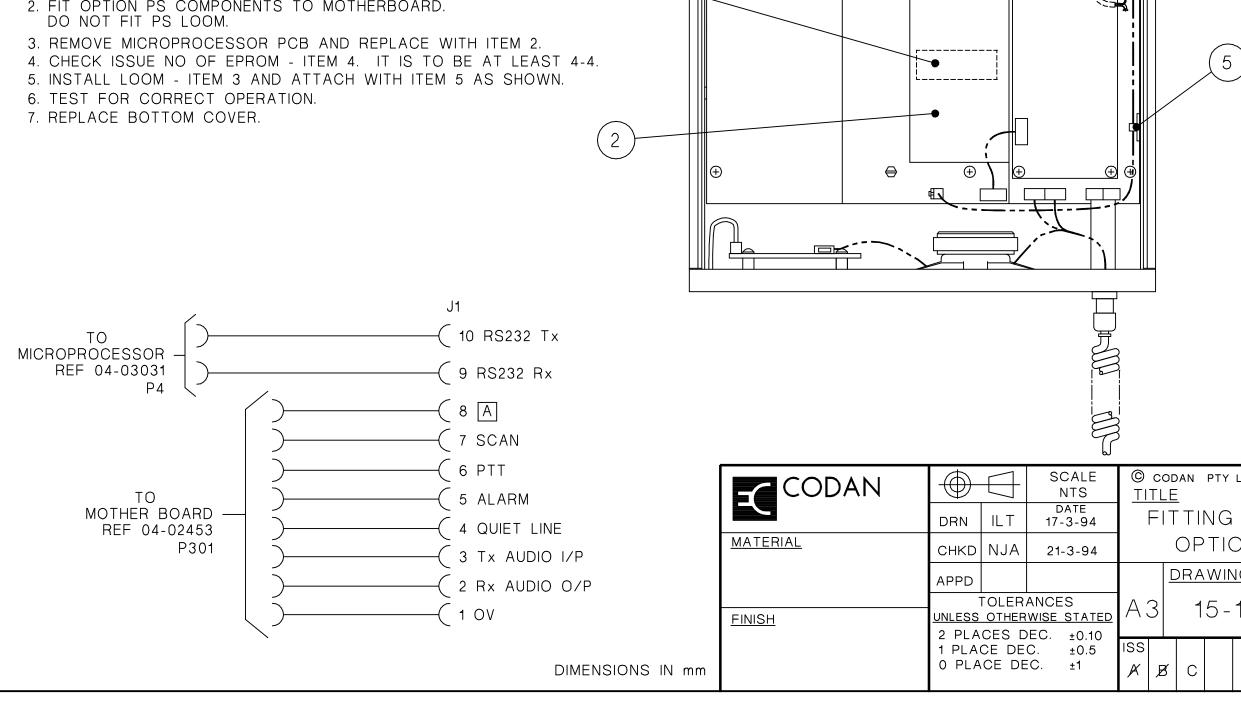
2 WAY PLUG NOT CONNECTED

(6)

ITEM	PART No.	DESCRIPTION
1	15-10412-001	FITTING INSTRUCTIONS
2	08-05090-001	MICROPROCESSOR PCB
3	08-05091-001	LOOM, OPT RS
4	90-20540	EPROM
5	30-05102-001	CABLE CLAMP
6	15-10271	OPTION PS

### NOTES:

- 1. REMOVE BOTTOM COVER.
- 2. FIT OPTION PS COMPONENTS TO MOTHERBOARD.



<u>ISSUE B</u> OPTION RS ADDED. DO NOT SCALE C/R 23929 7-4-94 NJA ISSUE C ITEM 4 WAS 90-20278 C/R 23930 30-5-94 CAH  $\oplus$ 4  $\oplus$ FILE No. 15\10412\_\_C.DWG © CODAN PTY LTD, A.C.N. 007 590 605: 2000 FITTING INSTRUCTIONS OPTIONS RS 8528 DRAWING/DOC NO. 15-10412-001 SHT. 1 OF 1

# **Addendum**



The following sections are added to the HF SSB transceiver 8525B/8528 series Technical Service Manual, Codan part number 15-02036 Issue 6, November 1995.

This information is provided in addition to that existing in Chapter 4, *Local Oscillators*. It explains the operation of the new synthesisers.

Drawings associated with this addendum are:

• RF Mixer and Dual Synthesiser Circuit Diagram 04-03120

• RF Mixer and Dual Synthesiser PCB Assembly 08-05295

# **Local Oscillators (04-03120)**

Two synthesised oscillators are used to drive the first and second mixers. The first oscillator operates between 45.250 MHz and 75 MHz in 2 kHz steps. It converts the 250 kHz to 30 MHz receive signal to 45 MHz for the first IF. The second oscillator operates between 43.350 MHz and 43.352 MHz in 10 Hz steps. It converts the 45 MHz first IF to 1650 kHz second IF.

Each oscillator consists of a:

- voltage controlled oscillator
- phase/frequency detector
- loop amplifier
- filter
- reference crystal oscillator (common)

The division ratios required are provided in serial data form by the microprocessor from data stored in the memory.

The reference frequency crystal Z2 (6600 kHz) is held at a constant temperature by a power PTC thermistor (R100) or by a high stability temperature controlled oven (08-05235). The crystal oscillator provides the reference frequency for the two synthesisers. The reference, after being divided by four (1650 kHz), provides the local oscillator drive to the modulator/demodulator for USB operation.

A 1647 kHz (Z3) crystal oscillator drives the modulator/demodulator stages for LSB operation.

# **Voltage Controlled Oscillator (VCO)**

As VCO1 and VCO2 are very similar, only VCO1 will be described in detail. The differences between the two VCOs are detailed in the table below.

	VCO1	VCO2
Varicaps	D17 to D20	D46 and D47
VCO output to synthesiser input pin 4	Via buffer V41 and coupling capacitor C144	Via R45 and C155
Division ratio	Divides to 2 kHz	1.1 kHz to 2 kHz in 10 Hz steps
Filter components	C127, C129, C181, R79, R99 and C123	C179, R166 and C180
Reference frequency	Reference crystal oscillator (6600 kHz)	6600 kHz obtained from IC2 pin 3 and applied to IC3 pin 1

FET V2 operates as a Hartley oscillator tuned by four varicaps D17 to D20. The output level is kept near constant frequency by a negative bias voltage applied to the V2 gate. This voltage is generated from schottky diode D21 charging capacitor C44.

Unity gain feedback amplifier V3 and V4 provides a buffer between the oscillator and the cascade connected amplifier V5 and V6. The second amplifier provides +7 dBm drive to the ring diode mixer D13. The VCO output is buffered by V41, before being applied to synthesiser IC2 pin 4.

# **Synthesiser for VCO1**

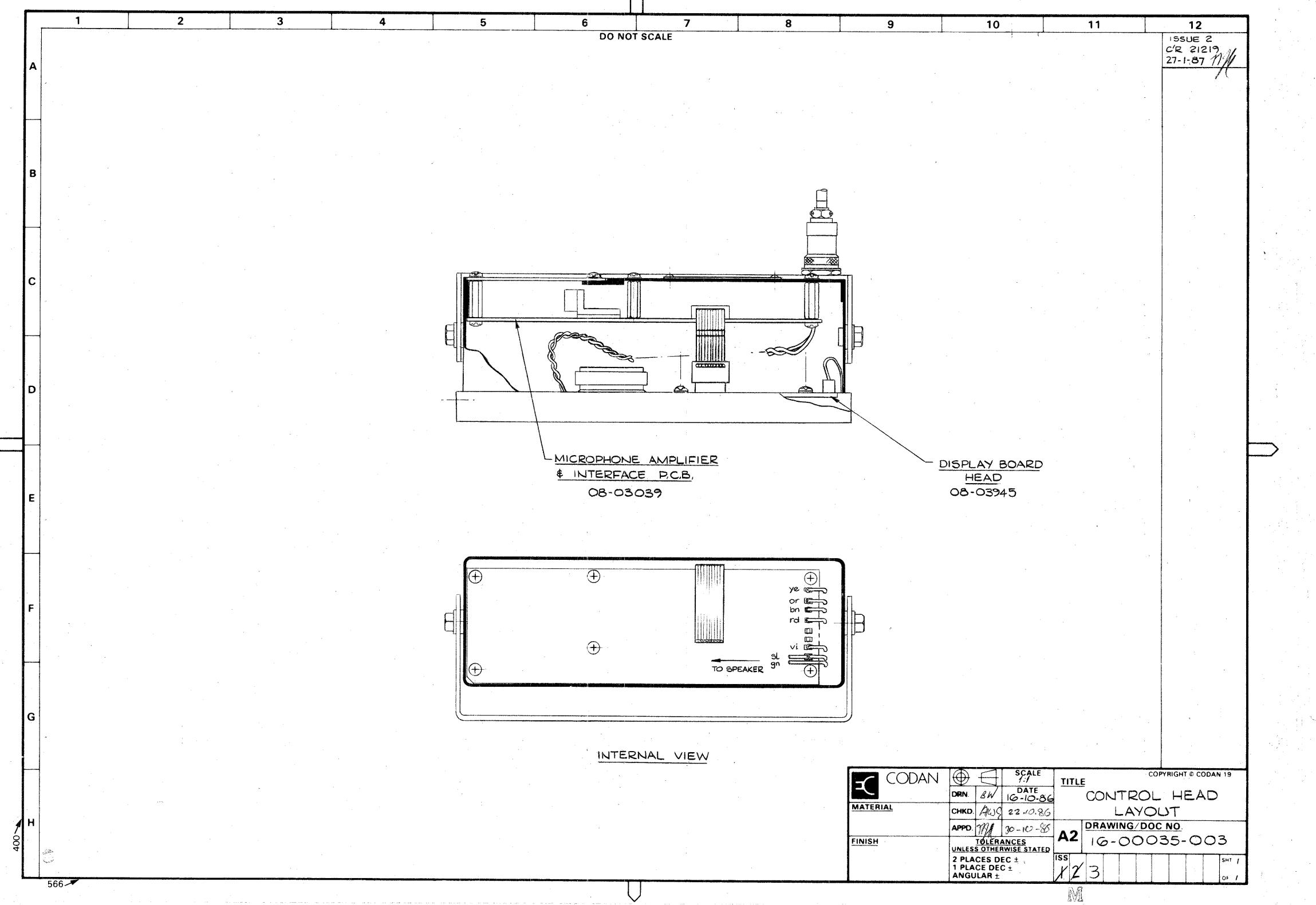
Synthesiser IC2 generates a DC control voltage via the phase/frequency detector and control amplifier. This is applied to the varicaps to lock the VCO1 to the nominated frequency.

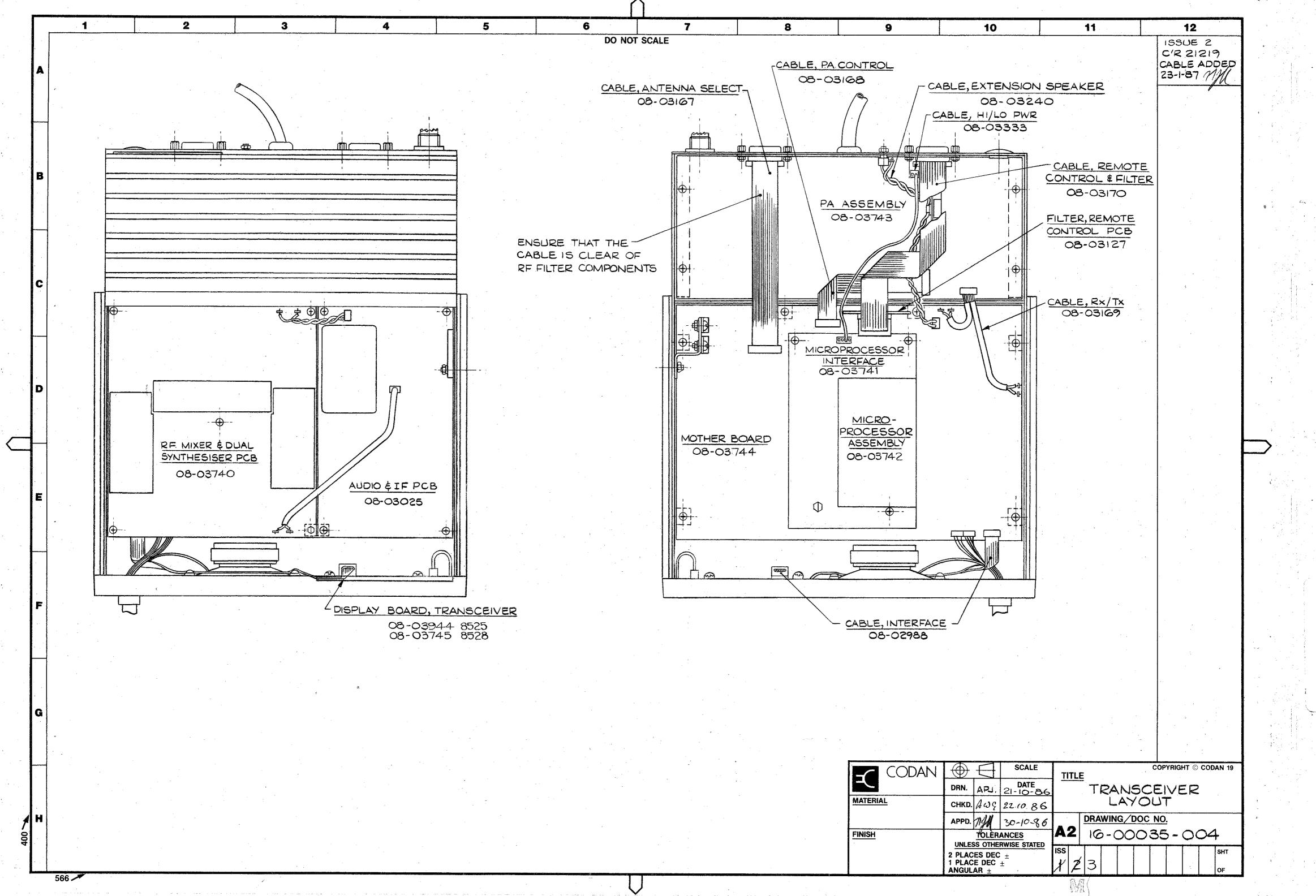
The 6600 kHz oscillator, part of IC2, is divided down to a 2 kHz reference frequency. The VCO signal applied to pin 4 of IC2 is also divided to produce 2 kHz when VCO1 is at the nominated frequency. Both of these signals are applied to the phase/frequency detector in IC2.

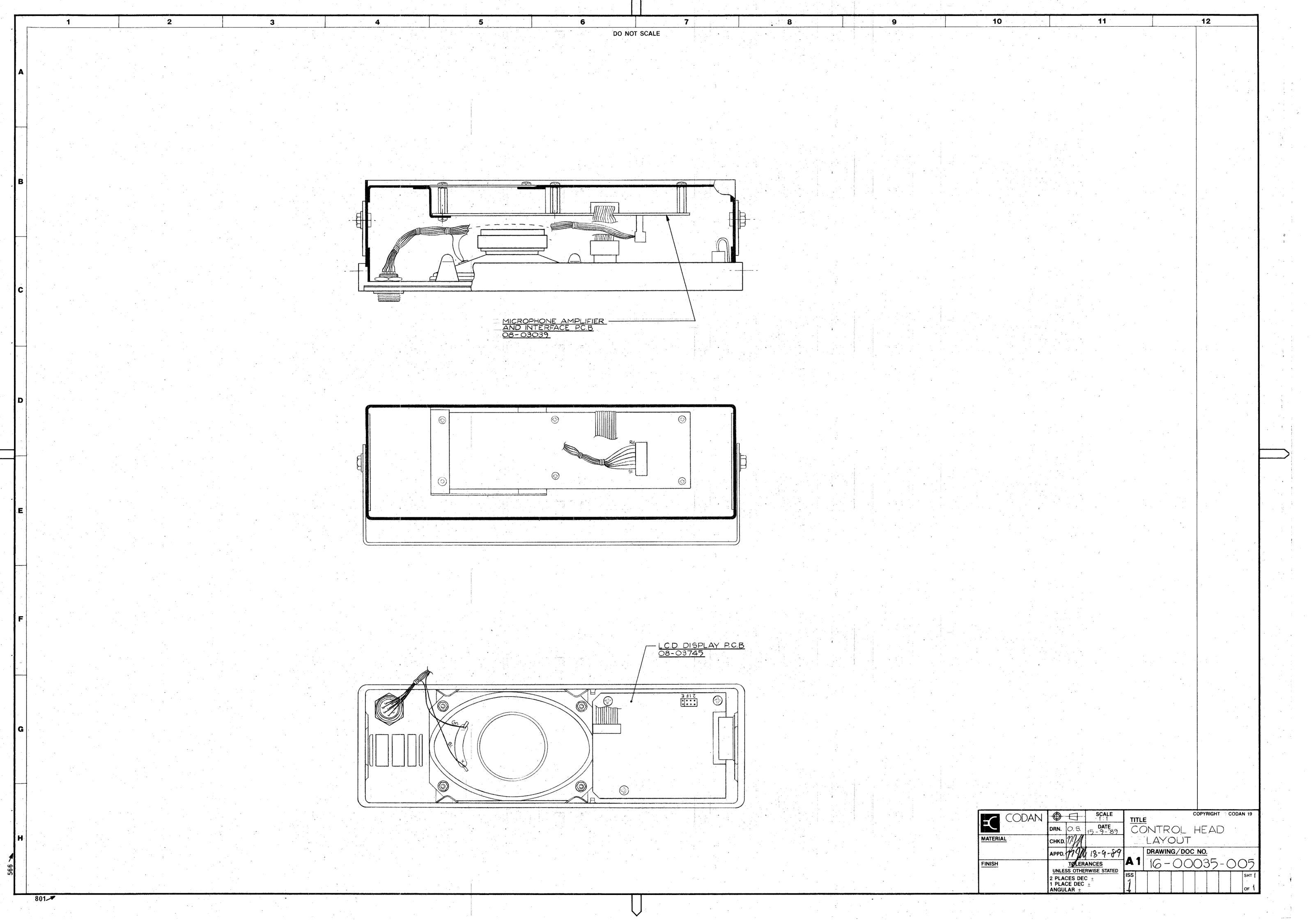
The phase/frequency detector (IC2) provides two outputs. Pin 14 provides a "go down" pulse. Pin 15 provides a "go up" pulse. These outputs are high (+5 V) when locked, except for a very narrow negative going pulse that occurs simultaneously on both outputs.

V29 and V30 provide a 3 mA discharging current pulse when pin 14 is low. V28, V31 and V32 provide a 3 mA charging current pulse when pin 15 is low.

These current pulses accumulate in the loop filter (C127, C129, C181 and R79). The resulting voltage is applied to the varicaps (D17 - D20) via a filter (R99 and C123). This filter removes any reference components from the control voltage.









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Service bulletin Page 1 of 2

17-00107 ISSUE 1 October 1991

### 9103 Automatic Antenna Tuner - Serial Nos A1312 - A1342

### **Symptom**

The 9103 tuner has recently incorporated a revised Printed Circuit Board (PCB) Artwork to include components necessary for scanning when used with Coda 85... series transceivers.

An error has been discovered on the PCB silkscreen whereby Capacitor C88 ( $2.2\mu$ F) may have been inserted incorrectly. The silkscreen identifies the +ve terminal of C88 closest to capacitor C87 and is incorrect. The +ve terminal should be located closest to Resistor R64.

On units with Serial Nos greater then A1342 the error has been rectified and the incorrect identifier on the PCB removed however, the possibility exists for some units in the Serial No. range A1312 - A1342 to have had the capacitor wrongly inserted. Whilst this may not be apparent from tuner operation, the capacitor may break down over a long period of time resulting in poor scan operation.

### Remedy

Owners of Tuners with Serial Nos in the above range are advised to check the orientation of capacitor C88 and replace it where incorrect. This can be accomplished by the following:

- I. Remove 10 screws securing the Tuner lid
- II. Remove 4 screws securing the shielding plate
- III. Check the orientation of Capacitor C88 as per the attached Fig 1 and if incorrect -
  - (a) Remove 4 screws securing the PCB, desolder Capacitor C88 and replace it with one correctly oriented.
  - (b) Carefully scratch off the + sign on the PCB adjacent to C88 to avoid future confusion;
  - (c) Replace the PCB
- IV. Re-assemble the Tuner

The Tuner should then be checked for correct operation.

Replacement capacitors can be made available from Codan's Service Department.

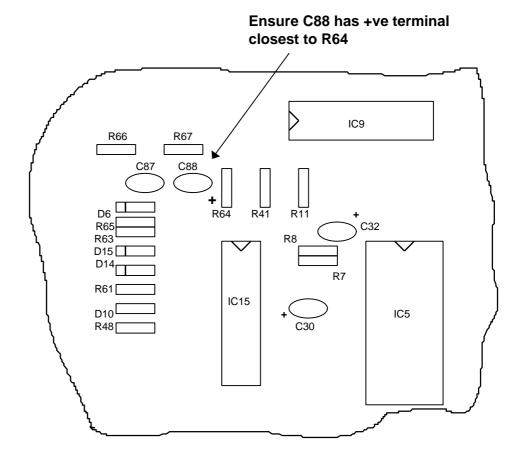


Figure 1. Part of 9103 Tuner Assembly