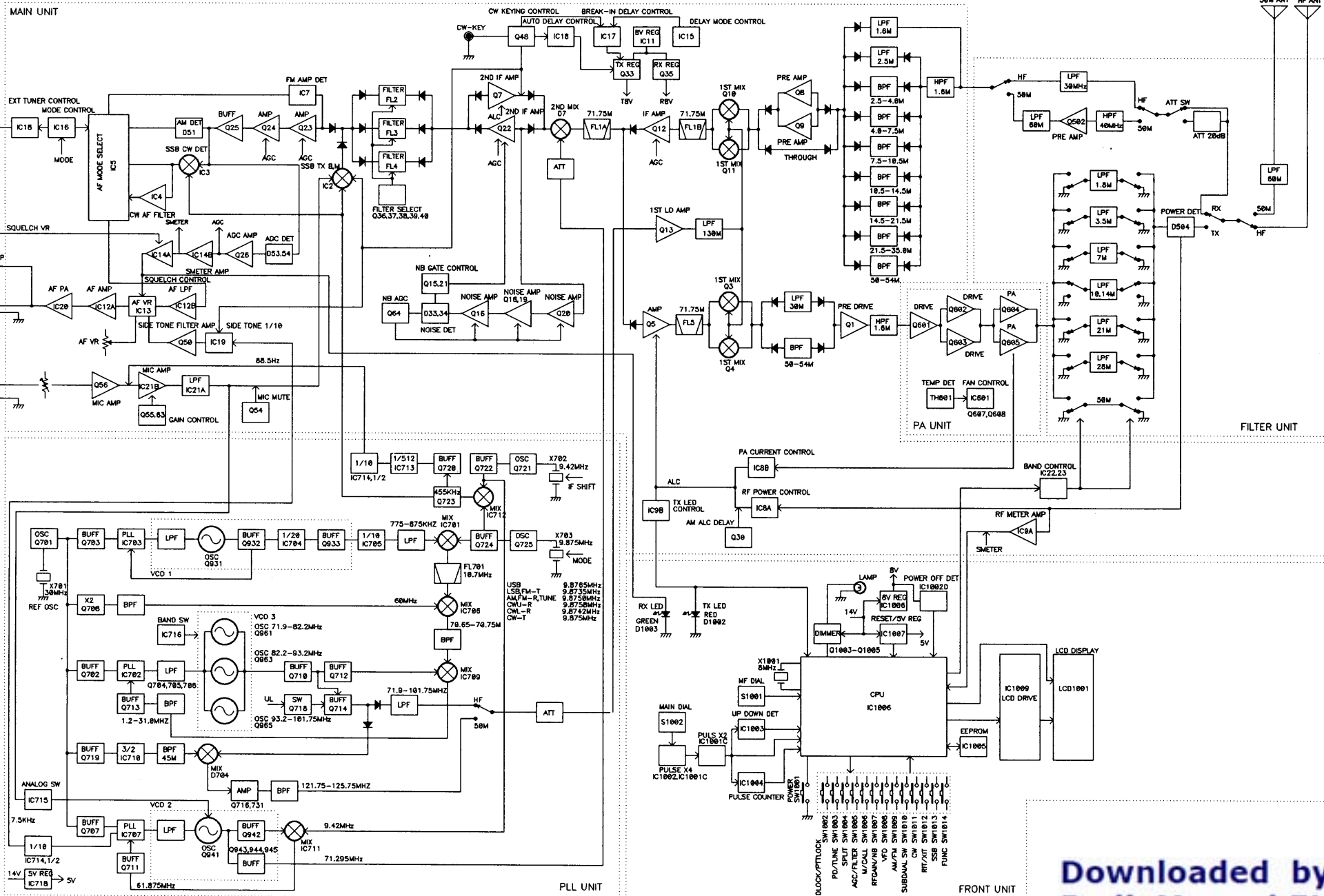
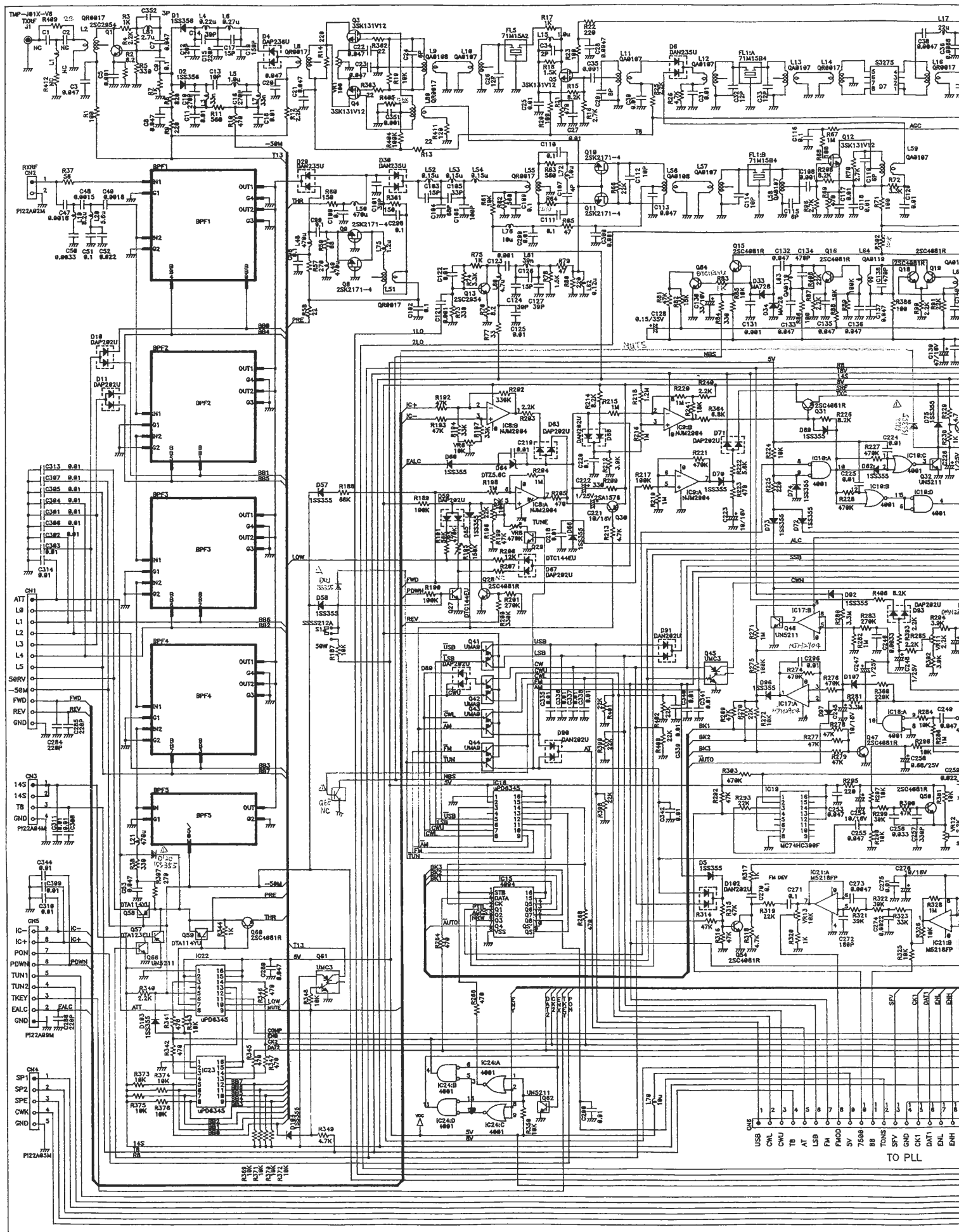


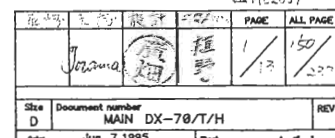
BLOCK DIAGRAM

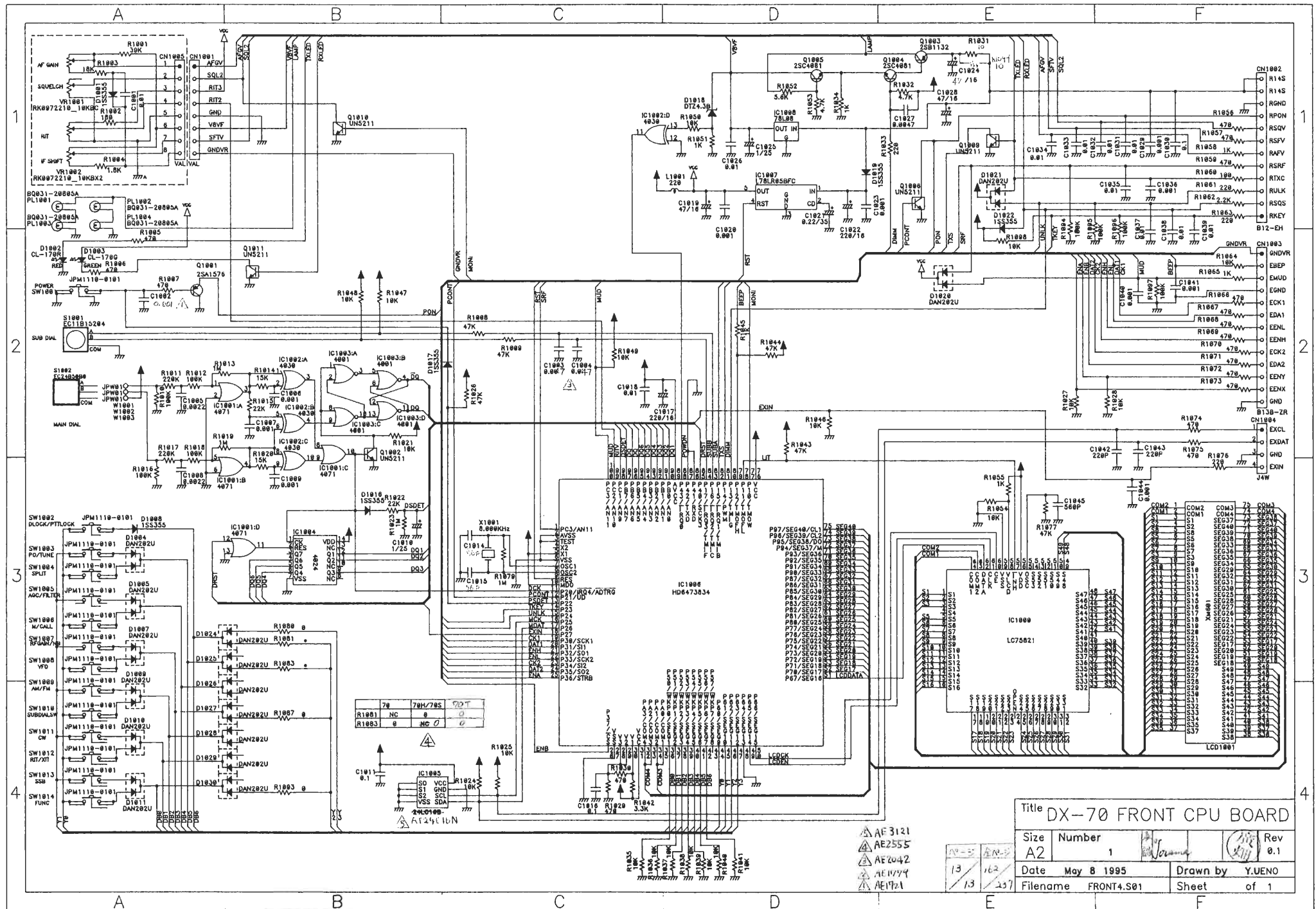


57







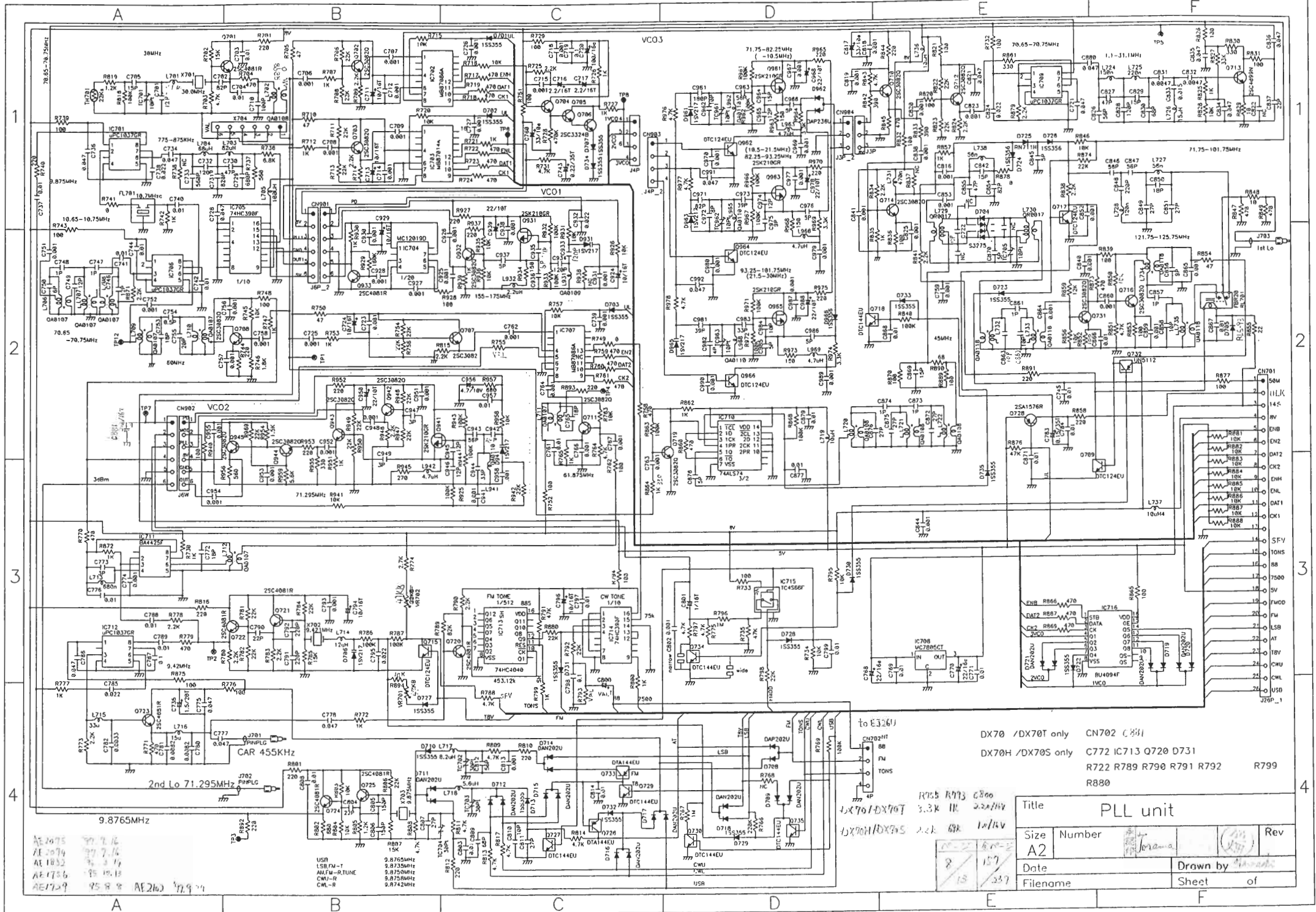


Title **DX-70 FRONT CPU BOARD**

Size	Number	Rev
A2	1	0.1

Date **May 8 1995** Drawn by **Y.UENO**

Filename **FRONT4.S01** Sheet **of 1**

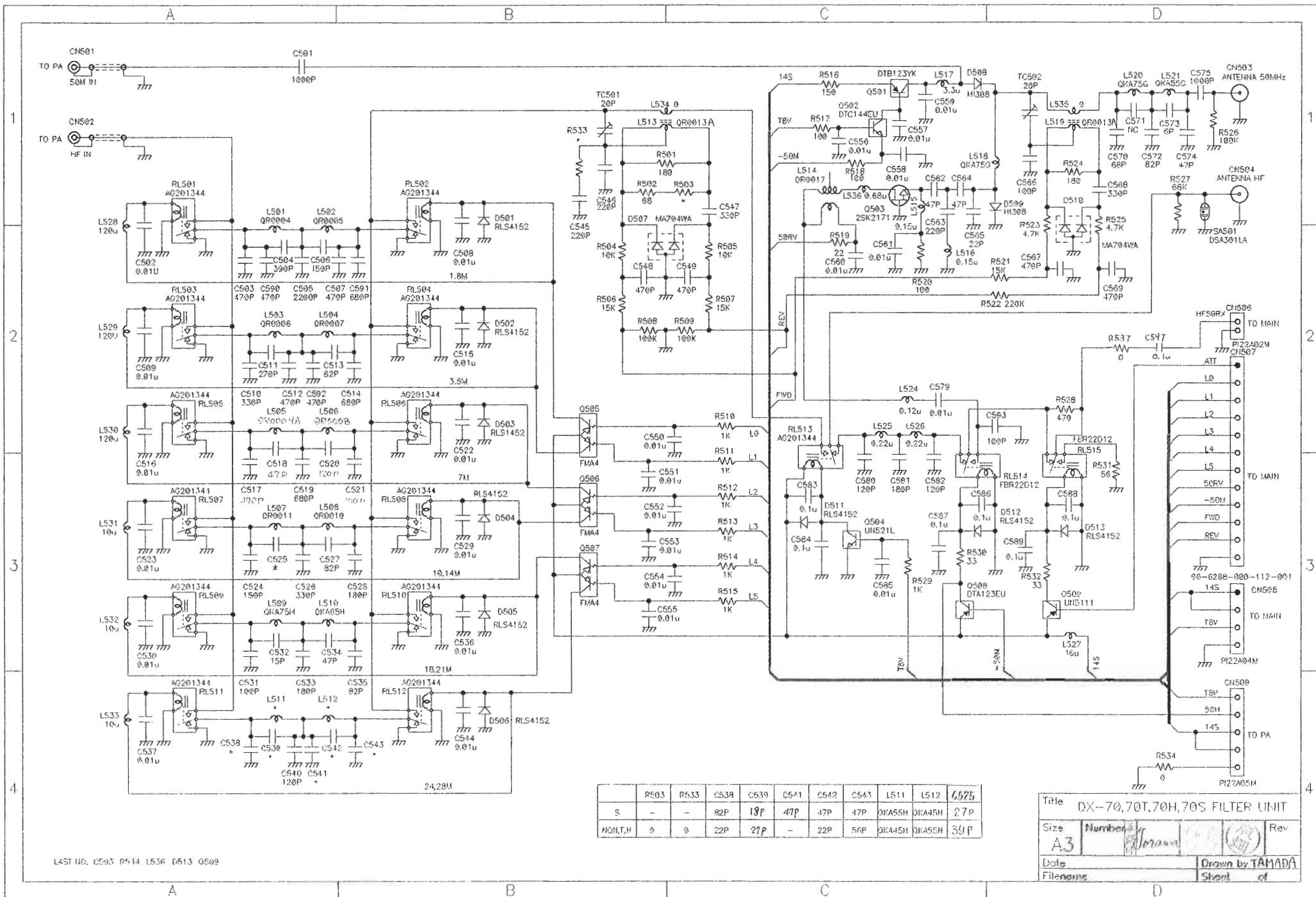


Title		PLL unit	
Size	A2	Number	1
Date	1987	Drawn by	1010-2
Filename	13	Sheet	of

AE1075 97.7.16
 AE1074 97.7.16
 AE1032 97.7.16
 AE1031 97.7.16
 AE1029 97.7.16

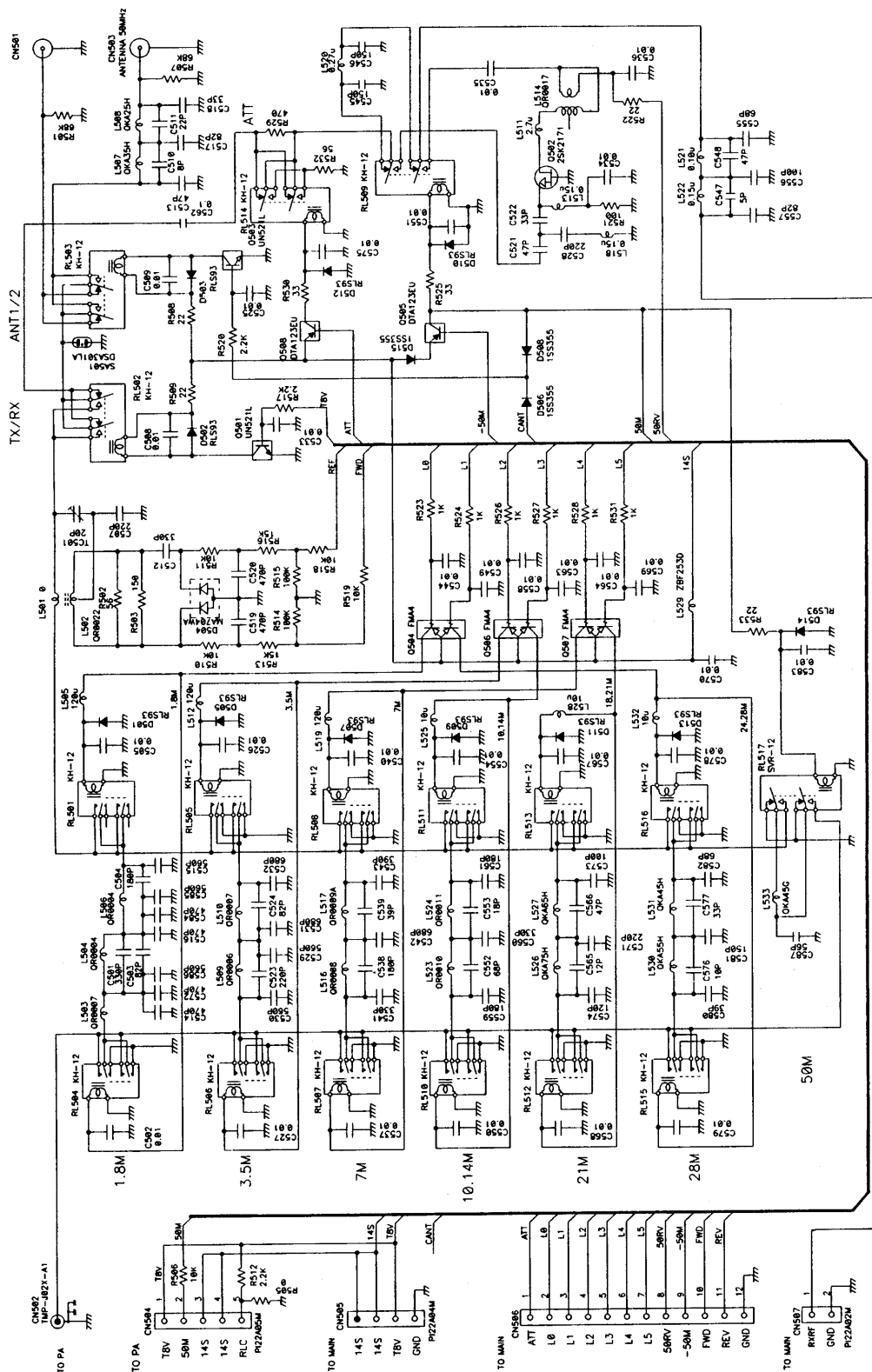
DX70 /DX70T only CN702 C841
 DX70H /DX70S only C772 IC713 Q720 D731
 R722 R789 R790 R791 R792 R799
 R880



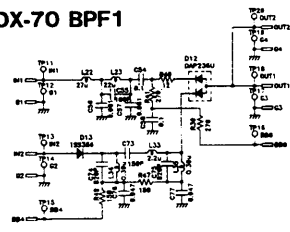


12-61
13-27

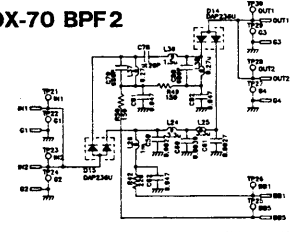
FILTER UNIT



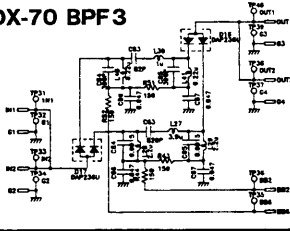
DX-70 BPF1



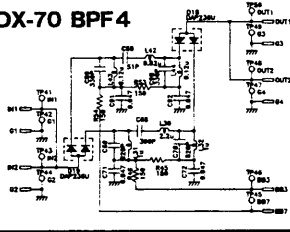
DX-70 BPF2



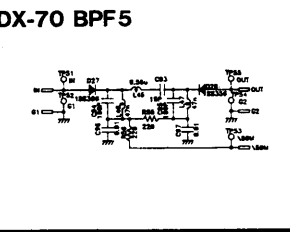
DX-70 BPF3



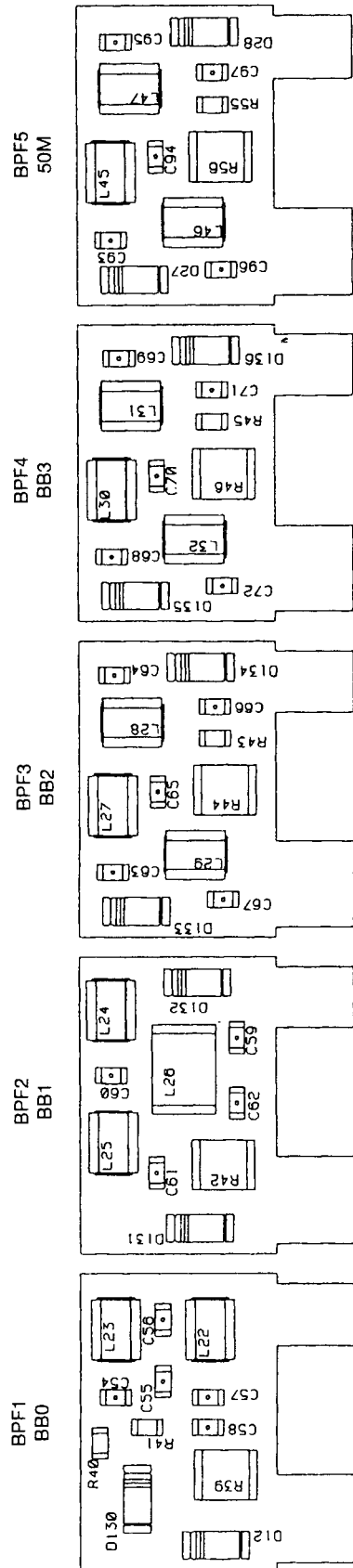
DX-70 BPF4



DX-70 BPF5



BPF UNIT Side A



BPF UNIT Side B

